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October 2003



FDP7030BL/FDB7030BL

N-Channel Logic Level PowerTrench^o MOSFET

General Description

This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

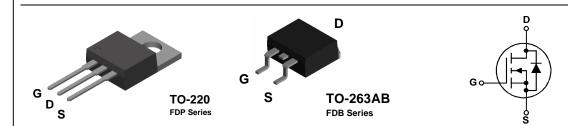
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{\text{DS(ON)}}$ specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

It has been optimized for low gate charge, low $R_{\text{DS}(\text{ON})}$ and fast switching speed.

Features

- 60 A, 30 V $R_{DS(ON)} = 9 \ m\Omega \ @ V_{GS} = 10 \ V$ $R_{DS(ON)} = 12 \ m\Omega \ @ V_{GS} = 4.5 \ V$
- Critical DC electrical parameters specified at elevated temperature
- High performance trench technology for extremely low $R_{\text{DS}(\text{ON})}$
- 175°C maximum junction temperature rating



Absolute Maximum Ratings T_A=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		± 20	V
ID	Drain Current – Continuous	(Note 1)	60	А
	– Pulsed	(Note 1)	180	
P _D	Total Power Dissipation @ $T_c = 25^{\circ}C$		60	W
	Derate above 25°C		0.4	W/°C
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-65 to +175	°C

Thermal Characteristics

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Package Marking and Ordering Information

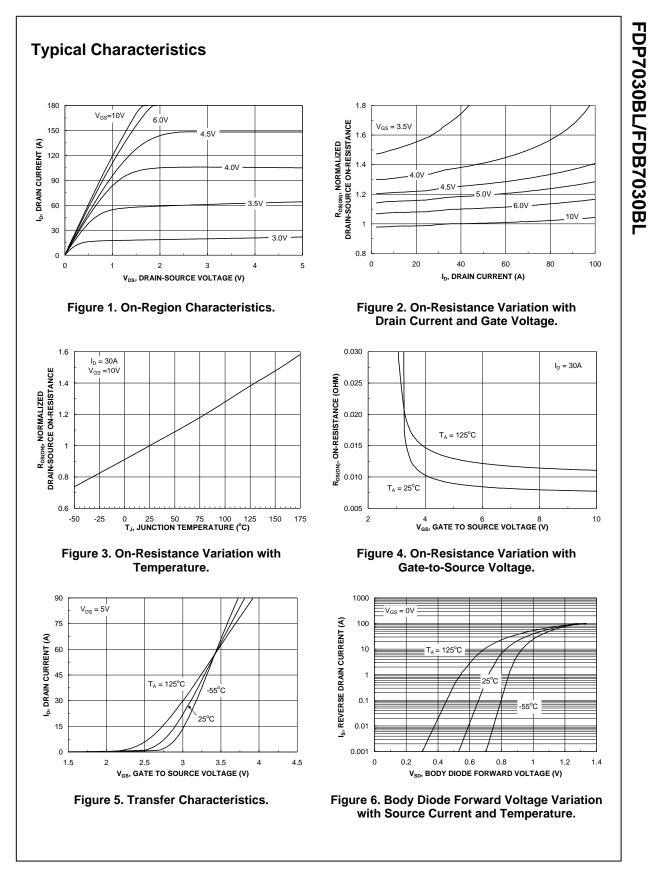
Device Marking	Device	Reel Size	Tape width	Quantity
FDB7030BL	FDB7030BL	13"	24mm	800 units
FDP7030BL	FDP7030BL	Tube	n/a	45

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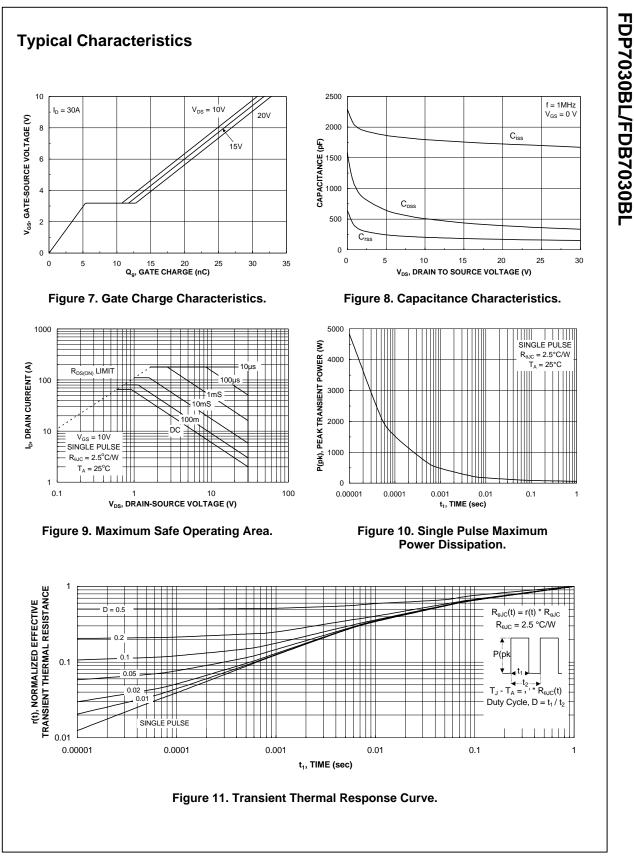
Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
-				1 y P	Мал	onits
	ource Avalanche Ratings (Note			1		
W _{DSS}	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 15 V, I_D = 60 A$			73	mJ
I _{AR}	Maximum Drain-Source Avalanche Current				60	A
Off Char	acteristics					
BV _{DSS}	Drain–Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, \qquad I_D = 250 \mu\text{A}$	30			V
<u>ΔBVdss</u> ΔTj	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$, Referenced to 25°C		22		mV/°C
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA
I _{GSS}	Gate-Body Leakage	$V_{GS}=\pm20~V, V_{DS}=0~V$			± 100	nA
On Char	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	1	1.9	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_{J}}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, Referenced to 25° C		-5		mV/°C
R _{DS(on)}	Static Drain–Source On–	$V_{GS} = 10 \text{ V}, \qquad I_{D} = 30 \text{ A}$		6.8	9	
	Resistance	$V_{GS}=4.5~V, \qquad I_{D}=25~A$		8.5	12	mΩ
		V_{GS} = 10 V, I_D = 30 A, T_J =125°C		10.1	18	
I _{D(on)}	On-State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	30			A
g fs	Forward Transconductance	$V_{DS} = 10V, \qquad I_D = 30 \text{ A}$		85		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 15 V$, $V_{GS} = 0 V$,		1760		pF
Coss	Output Capacitance	f = 1.0 MHz		440		pF
C _{rss}	Reverse Transfer Capacitance	-		185		pF
R _G	Gate Resistance	$V_{GS} = 15 \text{ mV}, \text{ f} = 1.0 \text{ MHz}$		1.2		Ω
Switchin	g Characteristics (Note 2)					
t _{d(on)}	Turn–On Delay Time	$V_{DD} = 15V, I_D = 1 A,$		12	22	ns
tr	Turn–On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		12	22	ns
t _{d(off)}	Turn–Off Delay Time	-		30	48	ns
t _f	Turn–Off Fall Time			19	33	ns
Qg	Total Gate Charge	$V_{DS} = 15 \text{ V}, \qquad I_D = 30 \text{ A},$		17	24	nC
Q _{gs}	Gate-Source Charge	$V_{GS} = 5 V$		5.4		nC
Q _{gd}	Gate-Drain Charge	7		6.4		nC
	ource Diode Characteristics	and Maximum Ratings				
	Maximum Continuous Drain–Source				60	А
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 V, I_S = 30 A$ (Note 1)		0.92	1.3	V
t _{rr}	Diode Reverse Recovery Time	I _F = 30 A,	1		30	nS
Q _{rr}	Diode Reverse Recovery Charge	$d_{iF}/d_t = 100 \text{ A}/\mu\text{s}$			20	nC

1. Pulse Test: Pulse Width < 300 μ s, Duty Cycle < 2.0%

FDP7030BL/FDB7030BL



FDP7030BL/FDB7030BL Rev D1(W)



FDP7030BL/FDB7030BL Rev D1(W)

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