LOW SKEW PCI/PCI-X BUFFER

ICS2304NZ-1

Description

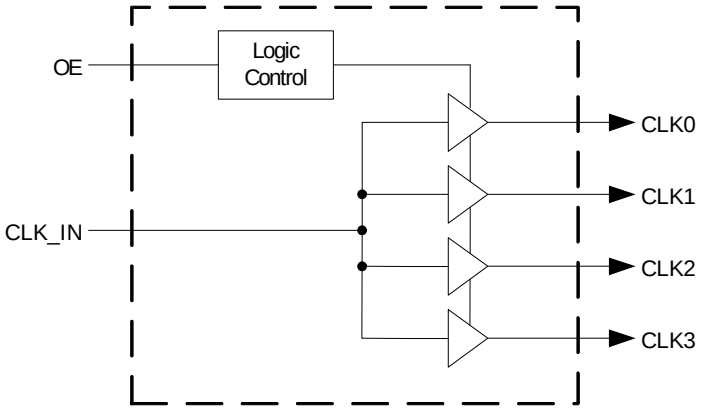
The ICS2304NZ-1 is a high-performance, low skew, low jitter PCI/PCI-X clock driver. It is designed to distribute high-speed signals in PCI/PCI-X applications operating at speeds from 0 to 140 MHz.

The ICS2304NZ-1 is characterized for operation from -40°C to +85°C for automotive and industrial applications.

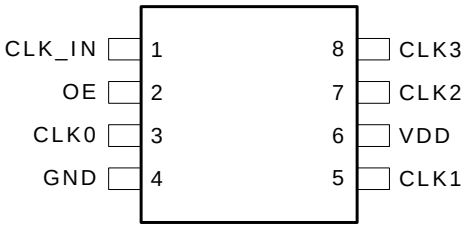
Features

- Packaged in 8-pin TSSOP (4.4 mm body)
- Frequency range of 0 to 140 MHz
- Less than 100 ps skew between outputs
- Distribute one clock input to one bank of four outputs
- Operating voltage of 3.3 V ±10%
- Available in commercial and industrial temperature ranges

Block Diagram



Pin Assignment



Functionality Table

Inputs		Outputs
CLK_IN	**OE**	**CLK(3:0)**
0	0	Tristate
0	1	0
1	0	Tristate
1	1	1

Pin Descriptions

Pin Number	Pin Name	Pin Type	Pin Description
1	CLK_IN	Input	Input reference frequency.
2	OE	Input	Output Enable. When OE is low, it tri-states clock outputs.
3	CLK0	Output	Buffered clock output.
4	GND	Power	Connect to ground.
5	CLK1	Output	Buffered clock output.
6	VDD	Power	Power supply for 3.3 V.
7	CLK2	Output	Buffered clock output.
8	CLK3	Output	Buffered clock output.

Absolute Maximum Ratings

Stresses above the ratings listed below can cause permanent damage to the ICS2304NZ-1. These ratings, which are standard values for IDT commercially rated parts, are stress ratings only. Functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods can affect product reliability. Electrical parameters are guaranteed only over the recommended operating temperature range.

Item	Rating
Supply Voltage Range, V_{DD}	-0.5 V to 4.3 V
Input Voltage Range, V_I (see notes 1 and 2)	-0.5 V to V_{DD} + 0.5 V
Output Voltage Range, V_O (see notes 1 and 2)	-0.5 V to V_{DD} + 0.5 V
Input Clamp Current, I_{IK} (V_I<0 or V_I>V_{DD})	±50 mA
Output Clamp Current, I_{IK} (V_O<0 or V_O)	±50 mA
Continuous Total Output Current, I_O (V_O = 0 to V_{DD})	±50 mA
Package Thermal Impedance, θ_{JA} (see note 3): PW Package	230.5° C/W
Storage Temperature Range, T_{stg}	-65° C to 150° C

Notes:

1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 4.6 V maximum.

3. The package thermal impedance is calculated in accordance with JESD 51.

Recommended Operation Conditions

Parameter	Min.	Typ.	Max.	Units
Supply Voltage, V_{DD}	3	3.3	3.6	V
High-level Input Voltage, V_{IH}	0.7 x V_{DD}			V
Low-level Input Voltage, V_{IL}			0.3 x V_{DD}	V
Input Voltage, V_I	0		V_{DD}	V
High-level Output Current, I_{OH}			-24	mA
Low-level Output Current, I_{OL}			24	mA
Operating Free-air Temperature, T_A	-40	–	+85	° C

Timing Requirements Over Recommended Ranges of Supply Voltage and Operating Free-air Temperature

	Min.	Typ.	Max.	Units
Clock Frequency, f_{CLK}	0		140	MHz

Electrical Characteristics at 3.3 V over Recommended Free-air Temperature Range

VDD = 3.3 V ±10%, T_A = -40°C to +85°C (unless stated otherwise)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Input Voltage	V_{IK}	V_{DD} at 3.3 V, I_I = -18 mA			-1.2	V
High-level Output Voltage	V_{OH}	V_{DD} = min to max, I_{OH} = -1 mA	V_{DD}-0.2	3.3		V
		V_{DD} = 3 V, I_{OH} = -24 mA	2	2.3		
		V_{DD} = 3 V, I_{OH} = -12 mA	2.4	2.7		
Low-level Output Voltage	V_{OL}	V_{DD} = min to max, I_{OH} = 1 mA		0.222	0.2	V
		V_{DD} = 3 V, I_{OL} = 24 mA		0.61	0.8	
		V_{DD} = 3 V, I_{OL} = 12 mA		0.31	0.55	
High-level Output Current	I_{OH}	V_{DD} = 3 V, V_O = 1 V		-53	-40	mA
		V_{DD} = 3.3 V, V_O = 1.65 V		-54		
Low-level Output Current	I_{OL}	V_{DD} = 3 V, V_O = 2 V	40	53		mA
		V_{DD} = 3.3 V, V_O = 1.65 V		57		
Input Current	I_I	V = V_{DD} or V_O	0.1		50	µA
Dynamic Supply Current	I_{DD}	Unloaded outputs at 66.67 MHz		13	37	mA
Input Capacitance (Note 1)	C_I	V_{DD} = 3.3 V, V_I = 0V or 3.3 V		3	5	pF
Output Capacitance (Note 1)	C_O	V_{DD} = 3.3 V, V_I = 0V or 3.3 V		3.2		pF

Note 1: Guaranteed by design, not 100% tested in production.

Switching Characteristics at 3.3 V over Recommended Ranges of Supply Voltage and Operating Free-air Temperature

VDD = 3.3 V ±10%, T_A = -40°C to 85°C (unless stated otherwise)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
High-to-Low Propagation Delay (Note 1)	t_{PLH}	$V_O = V_{DD}/2$	1.8	3.1	3.8	ns
Low-to-High Propagation Delay (Note 1)	t_{PHL}	$V_O = V_{DD}/2$	1.8	2.9	3.8	ns
Output Skew Window (Note 1)	$T_{SK}(o)$	$V_O = V_{DD}/2$		50	100	ps
Pulse Skew = $\lvert t_{PLH} - t_{PHL} \rvert$ (Note 1)	$T_{SK}(p)$	$V_O = V_{DD}/2$			300	ps
Process Skew (Note 1)	$T_{SK}(pr)$	$V_O = V_{DD}/2$			500	ps
CLKIN High Time (Note1)	T_{high}	66 MHz	6			ns
		140 MHz	3			ns
CLKIN Low Time (Note1)	T_{low}	66 MHz	6			ns
		140 MHz	3			ns
Rise Time (Note 1)	T_r	V_{OL}=0.8 V, V_{OH}=2.0 V		1.2	2.0	ns
Fall Time (Note 1)	T_f	V_{OH}=2.0 V, V_{OL}=0.8 V		1.2	2.0	ns
Cycle-to-Cycle Jitter	$T_{cyc-cyc}$	Loaded outputs			200	ps
Jitter, 1-Sigma	T_j1s	10,000 cycles		14	40	ps

Note 1: Guaranteed by design, not 100% tested in production.

Parameter Measurement Information

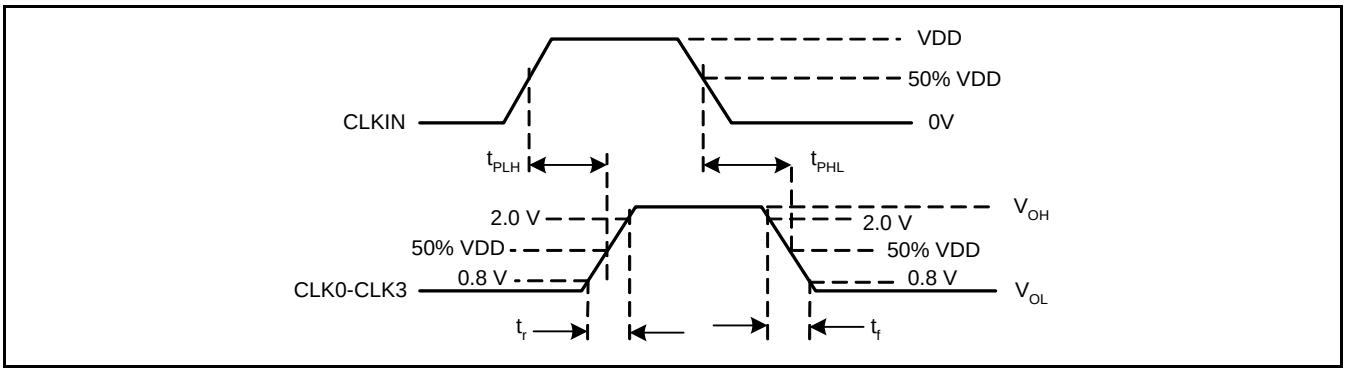


Figure 2. Voltage Thresholds for Propagation Delay (t_{pd}) Measurements

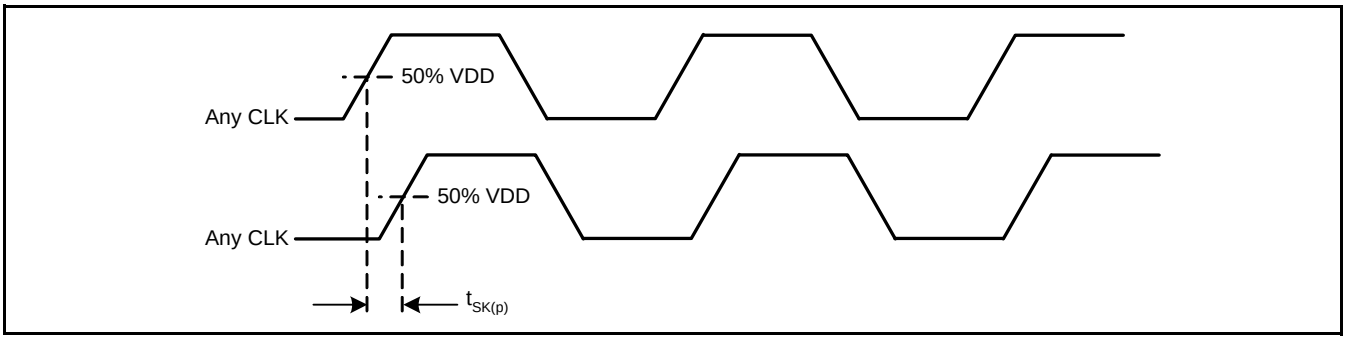


Figure 3. Output Skew

Parameter	Value	Unit
$V_{IH\ (MIN)}$	0.5 V_{DD}	V
$V_{IL\ (MAX)}$	0.35 V_{DD}	V
V_{TEST}	0.4 V_{DD}	V

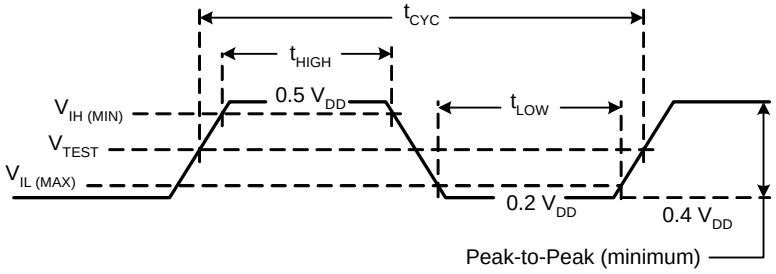


Figure 4. Clock Waveform

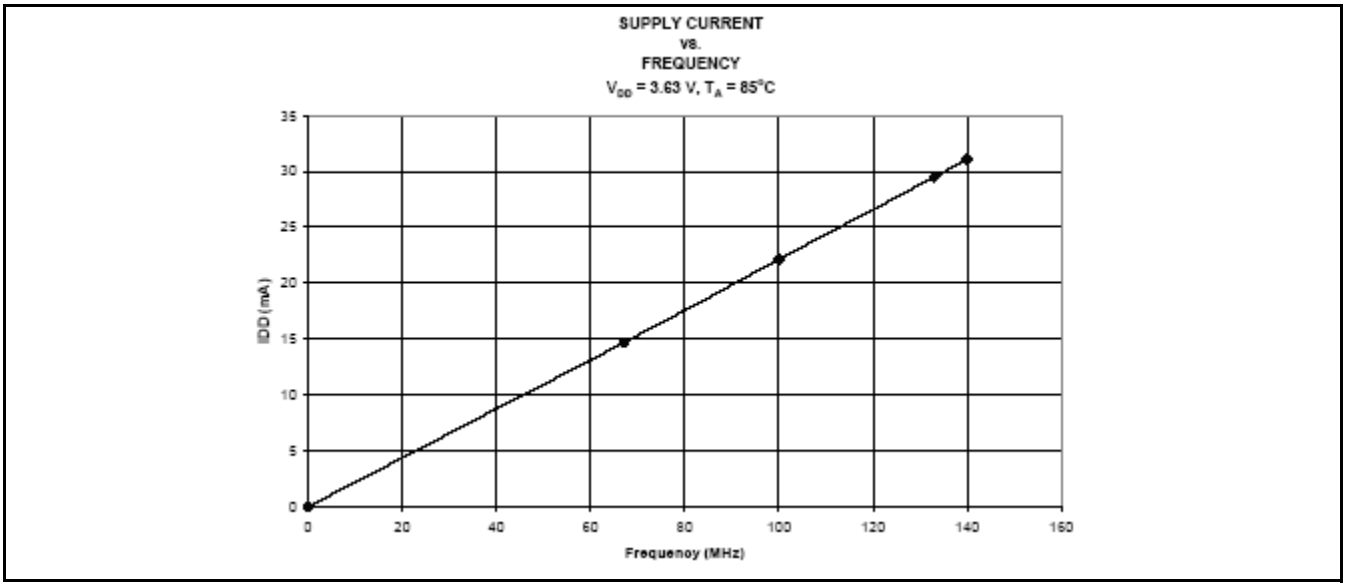


Figure 5. Supply Current vs. Frequency

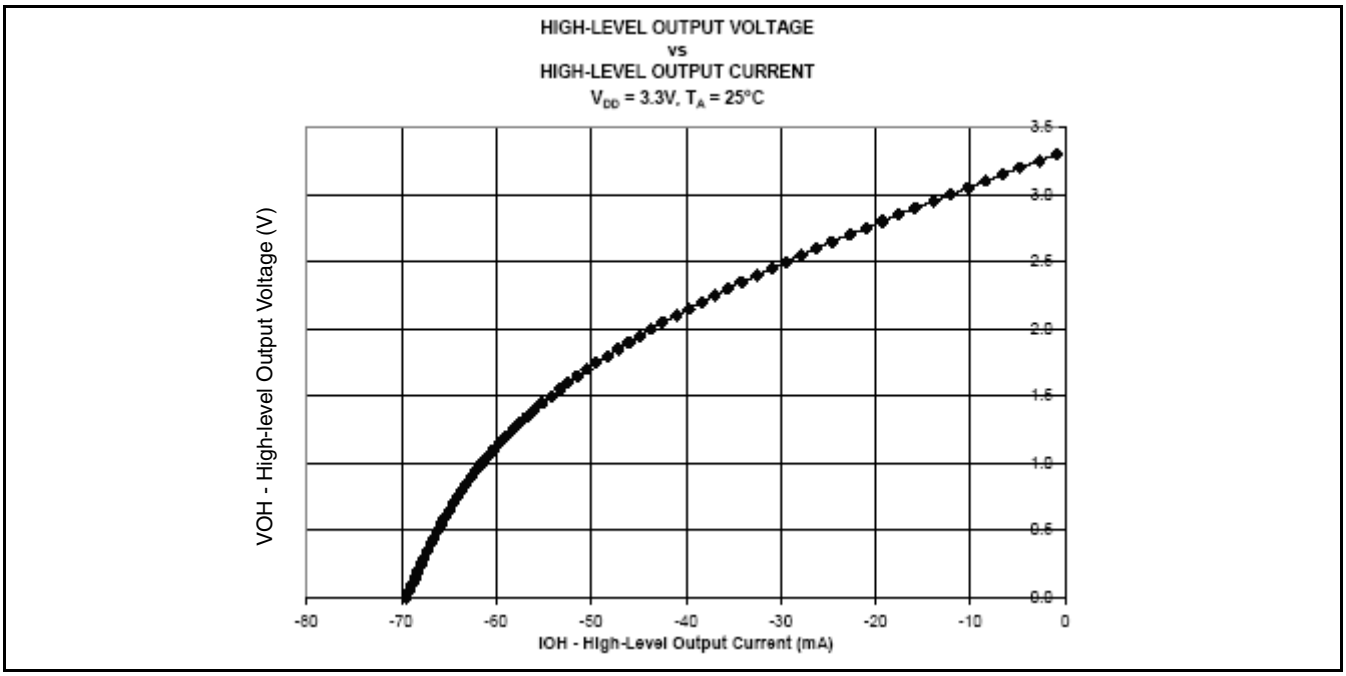


Figure 6. High-level Output Voltage vs. High-level Output Current

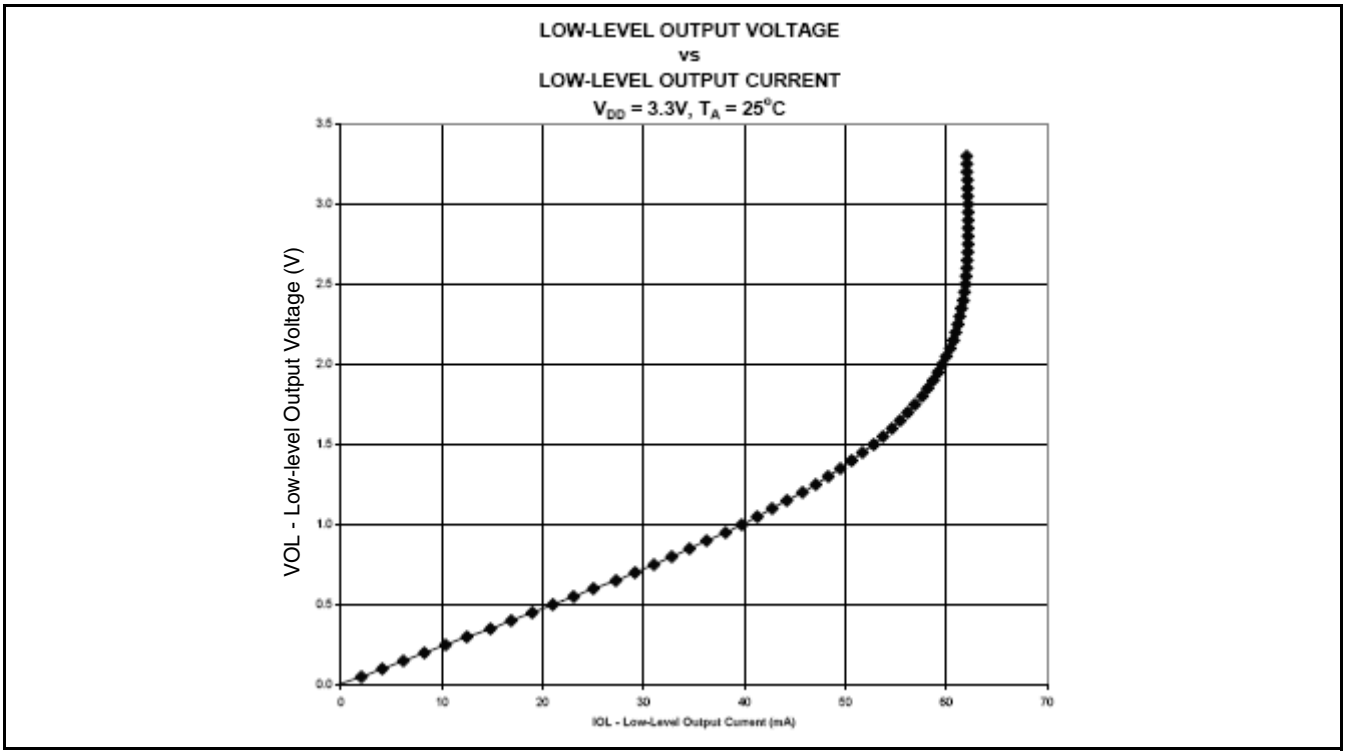
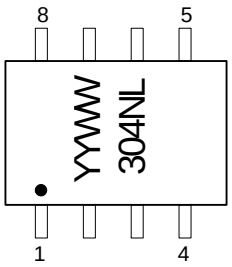
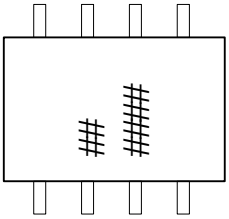


Figure 7. Low-level Output Voltage vs. Low-level Output Current

Marking Diagram (commercial)

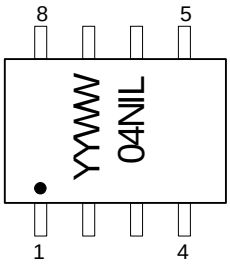


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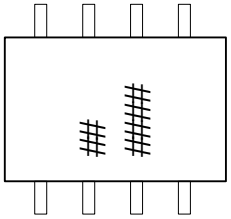


BOTTOM

Marking Diagram (industrial)



TOP



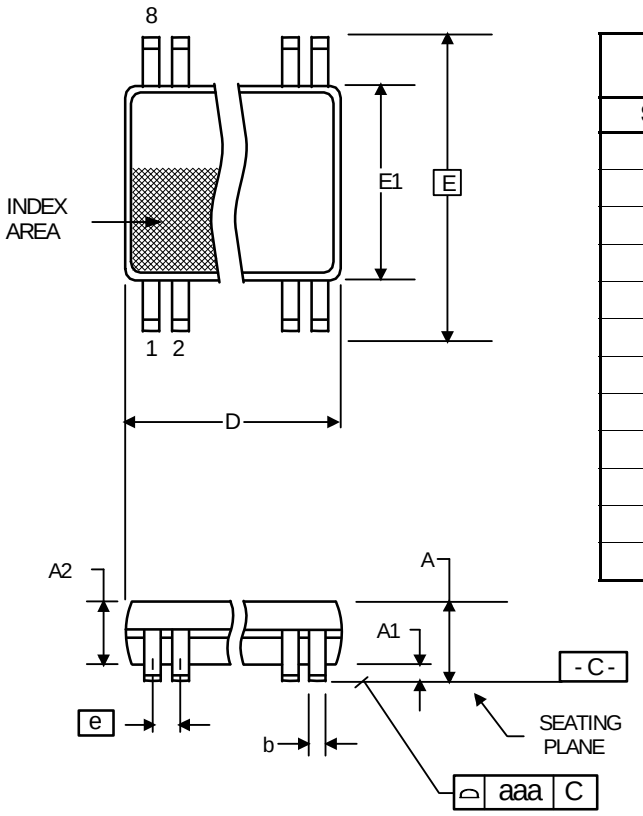
BOTTOM

Notes:

1. ###### is the lot number.
2. YYWW is the last two digits of the year and week that the part was assembled.
3. “L” denotes Pb (lead) free package.

Package Outline and Package Dimensions (8-pin TSSOP, 4.40 mm Body, 0.65 mm Pitch)

Package dimensions are kept current with JEDEC Publication No. 95, MO-153



	Millimeters		Inches	
Symbol	Min	Max	Min	Max
A	--	1.20	--	0.047
A1	0.05	0.15	0.002	0.006
A2	0.80	1.05	0.032	0.041
b	0.19	0.30	0.007	0.012
C	0.09	0.20	0.0035	0.008
D	2.90	3.10	0.114	0.122
E	6.40 BASIC		0.252 BASIC	
E1	4.30	4.50	0.169	0.177
e	0.65 Basic		0.0256 Basic	
L	0.45	0.75	0.018	0.030
α	0°	8°	0°	8°
aaa	--	0.10	--	0.004

Ordering Information

Part / Order Number	Marking	Shipping Packaging	Package	Temperature
2304NZG-1LF	see page 9	Tubes	8-pin TSSOP	0 to +70° C
2304NZG-1LFT		Tape and Reel	8-pin TSSOP	0 to +70° C
2304NZGI-1LF		Tubes	8-pin TSSOP	-40 to +85° C
2304NZGI-1LFT		Tape and Reel	8-pin TSSOP	-40 to +85° C

"LF" suffix to the part number are the Pb-Free configuration, RoHS compliant.

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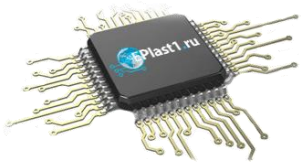
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