

# MC74HC14A

## Hex Schmitt-Trigger Inverter

### High-Performance Silicon-Gate CMOS

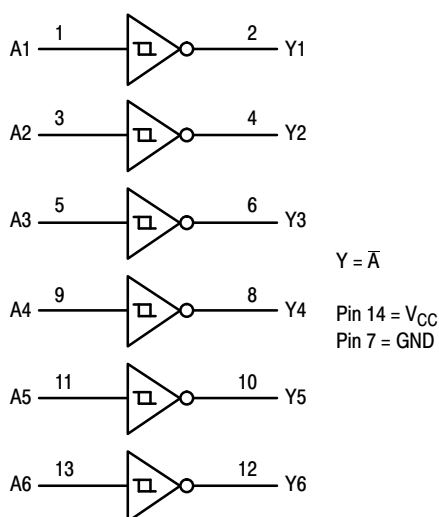
The MC74HC14A is identical in pinout to the LS14, LS04 and the HC04. The device inputs are compatible with Standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs.

The HC14A is useful to “square up” slow input rise and fall times. Due to hysteresis voltage of the Schmitt trigger, the HC14A finds applications in noisy environments.

#### Features

- Output Drive Capability: 10 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0  $\mu$ A
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance With the JEDEC Standard No. 7.0 A Requirements
- Chip Complexity: 60 FETs or 15 Equivalent Gates
- NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### LOGIC DIAGRAM



ON Semiconductor®

<http://onsemi.com>

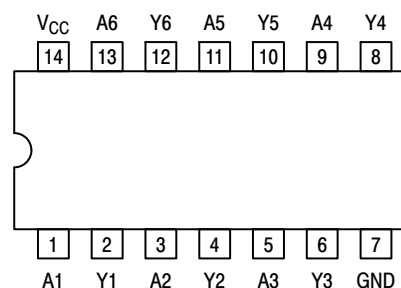


SOIC-14 NB  
D SUFFIX  
CASE 751A

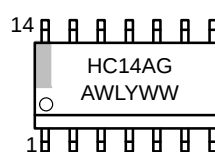


TSSOP-14  
DT SUFFIX  
CASE 948G

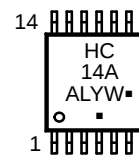
#### PIN ASSIGNMENT



#### MARKING DIAGRAMS



SOIC-14 NB



TSSOP-14

A = Assembly Location  
L, WL = Wafer Lot  
Y, YY = Year  
W, WW = Work Week  
G or ■ = Pb-Free Package

(Note: Microdot may be in either location)

#### FUNCTION TABLE

| Inputs | Outputs |
|--------|---------|
| A      | Y       |
| L      | H       |
| H      | L       |

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

# MC74HC14A

## MAXIMUM RATINGS

| Symbol    | Parameter                                                                             | Value                  | Unit |
|-----------|---------------------------------------------------------------------------------------|------------------------|------|
| $V_{CC}$  | DC Supply Voltage (Referenced to GND)                                                 | -0.5 to +7.0           | V    |
| $V_{in}$  | DC Input Voltage (Referenced to GND)                                                  | -0.5 to $V_{CC} + 0.5$ | V    |
| $V_{out}$ | DC Output Voltage (Referenced to GND)                                                 | -0.5 to $V_{CC} + 0.5$ | V    |
| $I_{in}$  | DC Input Current, per Pin                                                             | $\pm 20$               | mA   |
| $I_{out}$ | DC Output Current, per Pin                                                            | $\pm 25$               | mA   |
| $I_{CC}$  | DC Supply Current, $V_{CC}$ and GND Pins                                              | $\pm 50$               | mA   |
| $P_D$     | Power Dissipation in Still Air,<br>SOIC Package†<br>TSSOP Package†                    | 500<br>450             | mW   |
| $T_{stg}$ | Storage Temperature Range                                                             | -65 to +150            | °C   |
| $T_L$     | Lead Temperature, 1 mm from Case for 10 Seconds<br>Plastic DIP, SOIC or TSSOP Package | 260                    | °C   |

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation,  $V_{in}$  and  $V_{out}$  should be constrained to the range  $GND \leq (V_{in} \text{ or } V_{out}) \leq V_{CC}$ . Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or  $V_{CC}$ ). Unused outputs must be left open.

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

†Derating: SOIC Package: -7 mW/°C from 65° to 125°C  
TSSOP Package: -6.1 mW/°C from 65° to 125°C

## RECOMMENDED OPERATING CONDITIONS

| Symbol            | Parameter                                            | Min                                                                                             | Max                                 | Unit |
|-------------------|------------------------------------------------------|-------------------------------------------------------------------------------------------------|-------------------------------------|------|
| $V_{CC}$          | DC Supply Voltage (Referenced to GND)                | 2.0                                                                                             | 6.0                                 | V    |
| $V_{in}, V_{out}$ | DC Input Voltage, Output Voltage (Referenced to GND) | 0                                                                                               | $V_{CC}$                            | V    |
| $T_A$             | Operating Temperature Range, All Package Types       | -55                                                                                             | +125                                | °C   |
| $t_r, t_f$        | Input Rise/Fall Time<br>(Figure 1)                   | $V_{CC} = 2.0 \text{ V}$<br>0<br>$V_{CC} = 4.5 \text{ V}$<br>0<br>$V_{CC} = 6.0 \text{ V}$<br>0 | No Limit*<br>No Limit*<br>No Limit* | ns   |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

\*When  $V_{in} = 50\% V_{CC}$ ,  $I_{CC} > 1\text{mA}$

# MC74HC14A

## DC CHARACTERISTICS (Voltages Referenced to GND)

| Symbol                         | Parameter                                                    | Condition                                                                       | V <sub>CC</sub><br>V     | Guaranteed Limit             |                              |                              | Unit |
|--------------------------------|--------------------------------------------------------------|---------------------------------------------------------------------------------|--------------------------|------------------------------|------------------------------|------------------------------|------|
|                                |                                                              |                                                                                 |                          | -55 to 25°C                  | ≤85°C                        | ≤125°C                       |      |
| V <sub>T+</sub> max            | Maximum Positive-Going Input Threshold Voltage<br>(Figure 3) | V <sub>out</sub> = 0.1V<br> I <sub>out</sub>   ≤ 20μA                           | 2.0<br>3.0<br>4.5<br>6.0 | 1.50<br>2.15<br>3.15<br>4.20 | 1.50<br>2.15<br>3.15<br>4.20 | 1.50<br>2.15<br>3.15<br>4.20 | V    |
| V <sub>T+</sub> min            | Minimum Positive-Going Input Threshold Voltage<br>(Figure 3) | V <sub>out</sub> = 0.1V<br> I <sub>out</sub>   ≤ 20μA                           | 2.0<br>3.0<br>4.5<br>6.0 | 1.0<br>1.5<br>2.3<br>3.0     | 0.95<br>1.45<br>2.25<br>2.95 | 0.95<br>1.45<br>2.25<br>2.95 | V    |
| V <sub>T-</sub> max            | Maximum Negative-Going Input Threshold Voltage<br>(Figure 3) | V <sub>out</sub> = V <sub>CC</sub> - 0.1V<br> I <sub>out</sub>   ≤ 20μA         | 2.0<br>3.0<br>4.5<br>6.0 | 0.9<br>1.4<br>2.0<br>2.6     | 0.95<br>1.45<br>2.05<br>2.65 | 0.95<br>1.45<br>2.05<br>2.65 | V    |
| V <sub>T-</sub> min            | Minimum Negative-Going Input Threshold Voltage<br>(Figure 3) | V <sub>out</sub> = V <sub>CC</sub> - 0.1V<br> I <sub>out</sub>   ≤ 20μA         | 2.0<br>3.0<br>4.5<br>6.0 | 0.3<br>0.5<br>0.9<br>1.2     | 0.3<br>0.5<br>0.9<br>1.2     | 0.3<br>0.5<br>0.9<br>1.2     | V    |
| V <sub>H</sub> max<br>(Note 1) | Maximum Hysteresis Voltage<br>(Figure 3)                     | V <sub>out</sub> = 0.1V or V <sub>CC</sub> - 0.1V<br> I <sub>out</sub>   ≤ 20μA | 2.0<br>3.0<br>4.5<br>6.0 | 1.20<br>1.65<br>2.25<br>3.00 | 1.20<br>1.65<br>2.25<br>3.00 | 1.20<br>1.65<br>2.25<br>3.00 | V    |
| V <sub>H</sub> min<br>(Note 1) | Minimum Hysteresis Voltage<br>(Figure 3)                     | V <sub>out</sub> = 0.1V or V <sub>CC</sub> - 0.1V<br> I <sub>out</sub>   ≤ 20μA | 2.0<br>3.0<br>4.5<br>6.0 | 0.20<br>0.25<br>0.40<br>0.50 | 0.20<br>0.25<br>0.40<br>0.50 | 0.20<br>0.25<br>0.40<br>0.50 | V    |
| V <sub>OH</sub>                | Minimum High-Level Output Voltage                            | V <sub>in</sub> ≤ V <sub>T-</sub> min<br> I <sub>out</sub>   ≤ 20μA             | 2.0<br>4.5<br>6.0        | 1.9<br>4.4<br>5.9            | 1.9<br>4.4<br>5.9            | 1.9<br>4.4<br>5.9            | V    |
|                                |                                                              | V <sub>in</sub> ≤ V <sub>T-</sub> min     I <sub>out</sub>   ≤ 2.4mA            | 3.0                      | 2.48                         | 2.34                         | 2.20                         |      |
|                                |                                                              | I <sub>out</sub>   ≤ 4.0mA                                                      | 4.5                      | 3.98                         | 3.84                         | 3.70                         |      |
|                                |                                                              | I <sub>out</sub>   ≤ 5.2mA                                                      | 6.0                      | 5.48                         | 5.34                         | 5.20                         |      |
| V <sub>OL</sub>                | Maximum Low-Level Output Voltage                             | V <sub>in</sub> ≥ V <sub>T+</sub> max<br> I <sub>out</sub>   ≤ 20μA             | 2.0<br>4.5<br>6.0        | 0.1<br>0.1<br>0.1            | 0.1<br>0.1<br>0.1            | 0.1<br>0.1<br>0.1            | V    |
|                                |                                                              | V <sub>in</sub> ≥ V <sub>T+</sub> max     I <sub>out</sub>   ≤ 2.4mA            | 3.0                      | 0.26                         | 0.33                         | 0.40                         |      |
|                                |                                                              | I <sub>out</sub>   ≤ 4.0mA                                                      | 4.5                      | 0.26                         | 0.33                         | 0.40                         |      |
|                                |                                                              | I <sub>out</sub>   ≤ 5.2mA                                                      | 6.0                      | 0.26                         | 0.33                         | 0.40                         |      |
| I <sub>in</sub>                | Maximum Input Leakage Current                                | V <sub>in</sub> = V <sub>CC</sub> or GND                                        | 6.0                      | ±0.1                         | ±1.0                         | ±1.0                         | μA   |
| I <sub>CC</sub>                | Maximum Quiescent Supply Current (per Package)               | V <sub>in</sub> = V <sub>CC</sub> or GND<br>I <sub>out</sub> = 0μA              | 6.0                      | 1.0                          | 10                           | 40                           | μA   |

1. V<sub>H</sub>min > (V<sub>T+</sub> min) - (V<sub>T-</sub> max); V<sub>H</sub>max = (V<sub>T+</sub> max) - (V<sub>T-</sub> min).

## AC CHARACTERISTICS (C<sub>L</sub> = 50pF, Input t<sub>r</sub> = t<sub>f</sub> = 6ns)

| Symbol                                 | Parameter                                                                | V <sub>CC</sub><br>V                    | Guaranteed Limit |       |        | Unit |
|----------------------------------------|--------------------------------------------------------------------------|-----------------------------------------|------------------|-------|--------|------|
|                                        |                                                                          |                                         | -55 to 25°C      | ≤85°C | ≤125°C |      |
| t <sub>PLH</sub> ,<br>t <sub>PHL</sub> | Maximum Propagation Delay, Input A or B to Output Y<br>(Figures 1 and 2) | 2.0                                     | 75               | 95    | 110    | ns   |
|                                        |                                                                          | 3.0                                     | 30               | 40    | 55     |      |
|                                        |                                                                          | 4.5                                     | 15               | 19    | 22     |      |
|                                        |                                                                          | 6.0                                     | 13               | 16    | 19     |      |
| t <sub>TLH</sub> ,<br>t <sub>THL</sub> | Maximum Output Transition Time, Any Output<br>(Figures 1 and 2)          | 2.0                                     | 75               | 95    | 110    | ns   |
|                                        |                                                                          | 3.0                                     | 27               | 32    | 36     |      |
|                                        |                                                                          | 4.5                                     | 15               | 19    | 22     |      |
|                                        |                                                                          | 6.0                                     | 13               | 16    | 19     |      |
| C <sub>in</sub>                        | Maximum Input Capacitance                                                |                                         | 10               | 10    | 10     | pF   |
| C <sub>PD</sub>                        | Power Dissipation Capacitance (Per Inverter)*                            | Typical @ 25°C, V <sub>CC</sub> = 5.0 V |                  |       |        | pF   |
|                                        |                                                                          | 22                                      |                  |       |        |      |

\* Used to determine the no-load dynamic power consumption: P<sub>D</sub> = C<sub>PD</sub> V<sub>CC</sub><sup>2</sup>f + I<sub>CC</sub> V<sub>CC</sub>.

# MC74HC14A

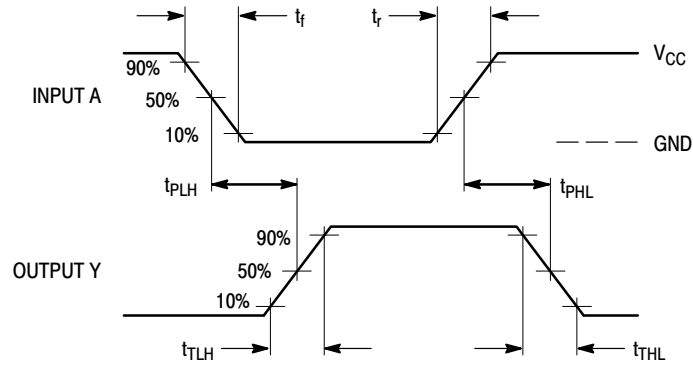
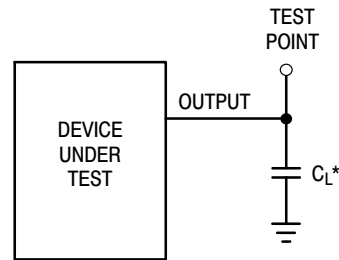


Figure 1. Switching Waveforms



\*Includes all probe and jig capacitance

Figure 2. Test Circuit

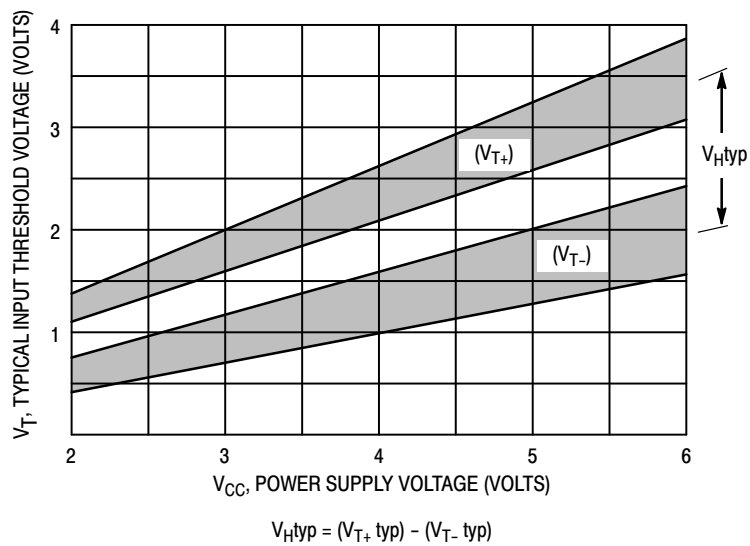
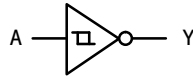
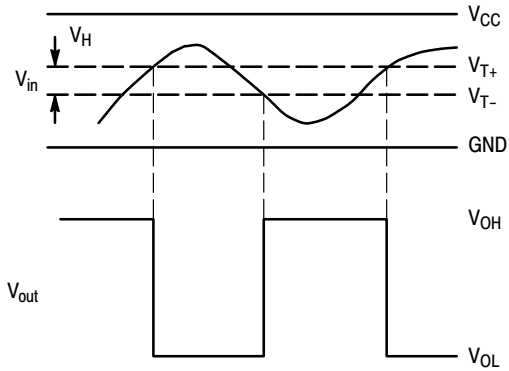


Figure 3. Typical Input Threshold,  $V_{T+}$ ,  $V_{T-}$  versus Power Supply Voltage

## MC74HC14A



(a) A Schmitt-Trigger Squares Up Inputs With Slow Rise and Fall Times



(b) A Schmitt-Trigger Offers Maximum Noise Immunity

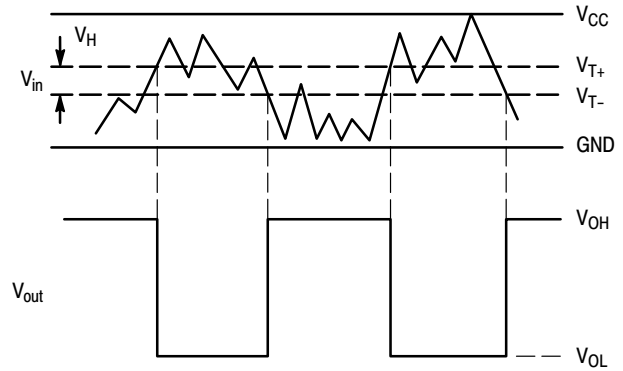


Figure 4. Typical Schmitt-Trigger Applications

### ORDERING INFORMATION

| Device           | Package                 | Shipping <sup>†</sup> |
|------------------|-------------------------|-----------------------|
| MC74HC14ADG      | SOIC-14 NB<br>(Pb-Free) | 55 Units / Rail       |
| MC74HC14ADR2G    | SOIC-14 NB<br>(Pb-Free) | 2500 / Tape & Reel    |
| MC74HC14ADTG     | TSSOP-14<br>(Pb-Free)   | 96 Units / Rail       |
| MC74HC14ADTR2G   | TSSOP-14<br>(Pb-Free)   | 2500 / Tape & Reel    |
| NLV74HC14ADG*    | SOIC-14 NB<br>(Pb-Free) | 55 Units / Rail       |
| NLV74HC14ADR2G*  | SOIC-14 NB<br>(Pb-Free) | 2500 / Tape & Reel    |
| NLV74HC14ADTG*   | TSSOP-14<br>(Pb-Free)   | 96 Units / Rail       |
| NLV74HC14ADTR2G* | TSSOP-14<br>(Pb-Free)   | 2500 / Tape & Reel    |

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

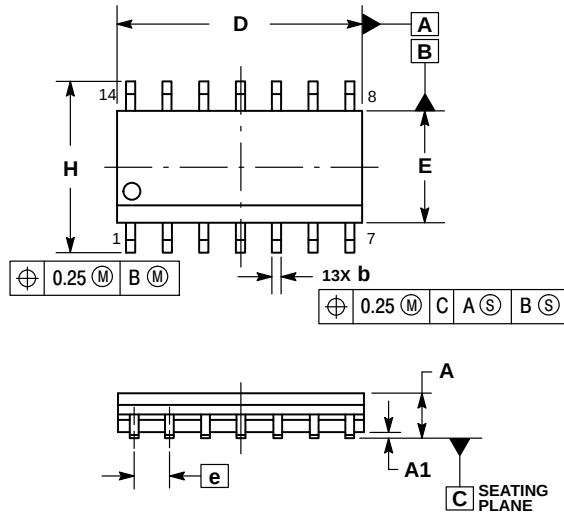
\*NLV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable



# MC74HC14A

## PACKAGE DIMENSIONS

SOIC-14 NB  
CASE 751A-03  
ISSUE K

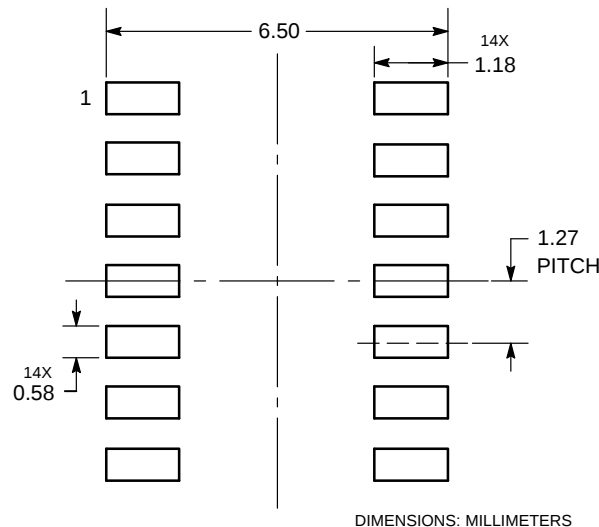


### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF AT MAXIMUM MATERIAL CONDITION.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD PROTRUSIONS.
5. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.

| DIM | MILLIMETERS |      | INCHES    |       |
|-----|-------------|------|-----------|-------|
|     | MIN         | MAX  | MIN       | MAX   |
| A   | 1.35        | 1.75 | 0.054     | 0.068 |
| A1  | 0.10        | 0.25 | 0.004     | 0.010 |
| A3  | 0.19        | 0.25 | 0.008     | 0.010 |
| b   | 0.35        | 0.49 | 0.014     | 0.019 |
| D   | 8.55        | 8.75 | 0.337     | 0.344 |
| E   | 3.80        | 4.00 | 0.150     | 0.157 |
| e   | 1.27 BSC    |      | 0.050 BSC |       |
| H   | 5.80        | 6.20 | 0.228     | 0.244 |
| h   | 0.25        | 0.50 | 0.010     | 0.019 |
| L   | 0.40        | 1.25 | 0.016     | 0.049 |
| M   | 0°          | 7°   | 0°        | 7°    |

### SOLDERING FOOTPRINT\*



DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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MC74HC14A/D



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Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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**Факс:** 8 (812) 320-02-42

**Электронная почта:** [org@eplast1.ru](mailto:org@eplast1.ru)

**Адрес:** 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.