

TOSHIBA Photocoupler GaAs Ired & Photo-Triac

TLP560J

Triac Driver

Programmable Controllers

AC-Output Module

Solid State Relay

The TOSHIBA TLP560J consists of a photo-triac optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package.

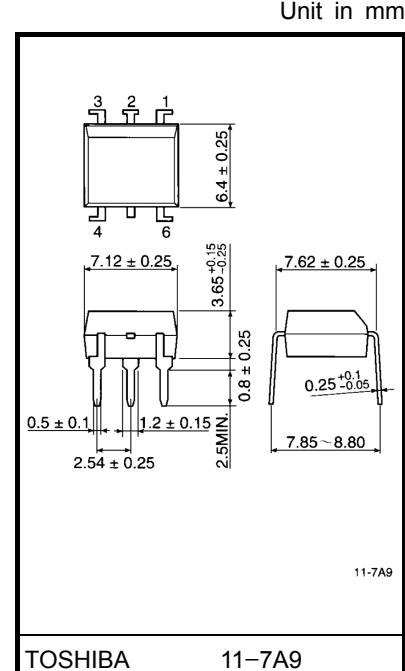
- Peak off-state voltage: 600V(min.)
- On-state current: 100mA(max.)
- Isolation voltage: 2500Vrms (min.)
- UL recognized: UL1577, file no. E67349
- Isolation operating voltage: 2500Vac or 300Vdc for isolation
Group C^{*1}
- Trigger LED current

Classi- fication*	Trigger LED Current (mA)		Marking Of Classification	
	$V_T=6V$, $T_a=25^\circ C$			
	Min.	Max.		
(IFT7)	—	7	T7	
Standard	—	10	T7, blank	

*Ex. (IFT7); TLP560J(IFT7)

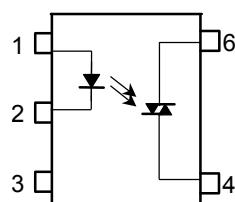
(Note) Application type name for certification test, please
use standard product type name, i.e.
TLP560J(IFT7): TLP560J

*1: According to VDE0110, table 4.



TOSHIBA 11-7A9

Weight: 0.39 g

Pin Configuration (top view)

- 1: Anode
- 2: Cathode
- 3: N.C.
- 4: Terminal 1
- 6: Terminal 2

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit
LED	Forward current	I _F	50	mA
	Forward current derating (Ta ≥ 53°C)	ΔI _F / °C	-0.7	mA / °C
	Peak forward current (100μs pulse, 100pps)	I _{FP}	1	A
	Reverse voltage	V _R	5	V
	Junction temperature	T _j	125	°C
Detector	Off-state output terminal voltage	V _{DRM}	600	V
	On-state RMS current	I _{T(RMS)}	100	mA
	Ta=70°C		50	
	On-state current derating(Ta ≥ 25°C)	ΔI _T / °C	-1.1	mA / °C
	Peak on-state current (100μs pulse, 120pps)	I _{TP}	2	A
	Peak nonrepetitive surge current (Pw=10ms,DC=10%)	I _{TSM}	1.2	A
	Junction temperature	T _j	115	°C
	Storage temperature range	T _{stg}	-55~125	°C
Operating temperature range		T _{opr}	-40~100	°C
Lead soldering temperature (10s)		T _{sol}	260	°C
Isolation voltage (AC, 1min., R.H. ≤ 60%)		BVs	2500	V _{rms}

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Recommended Operating Conditions

Characteristic	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	V _{AC}	—	—	240	V _{ac}
Forward current	I _F	15	20	25	mA
Peak on-state current	I _{TP}	—	—	—	A
Operating temperature	T _{opr}	-25	—	85	°C

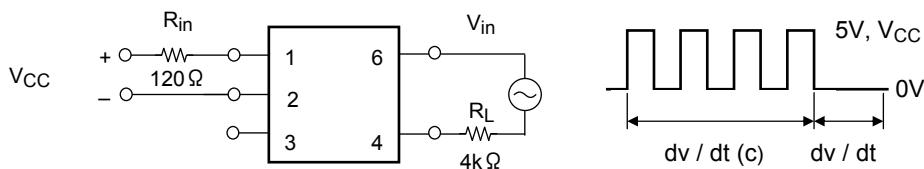
Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

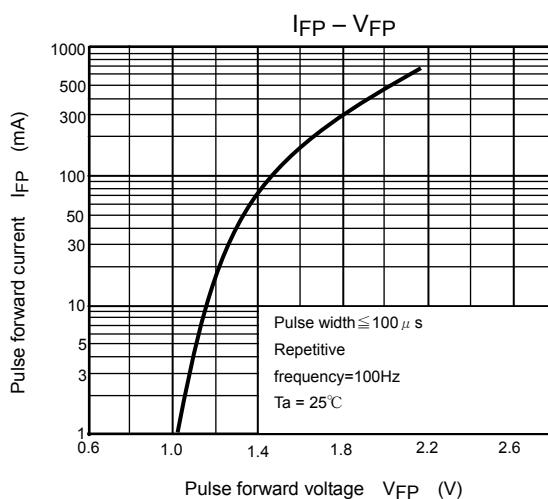
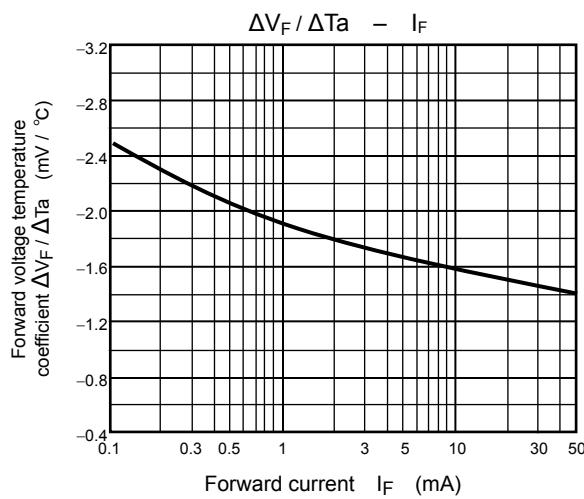
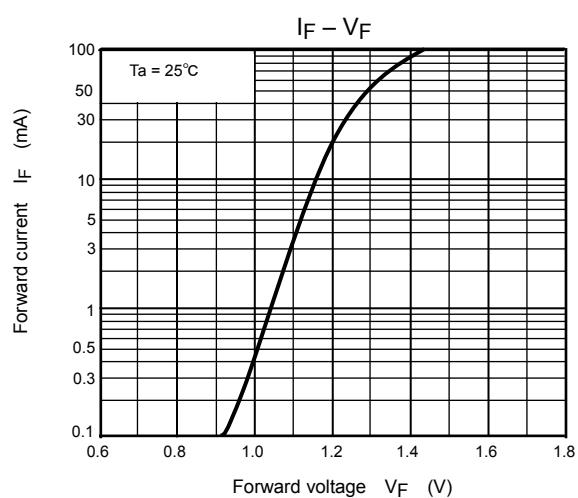
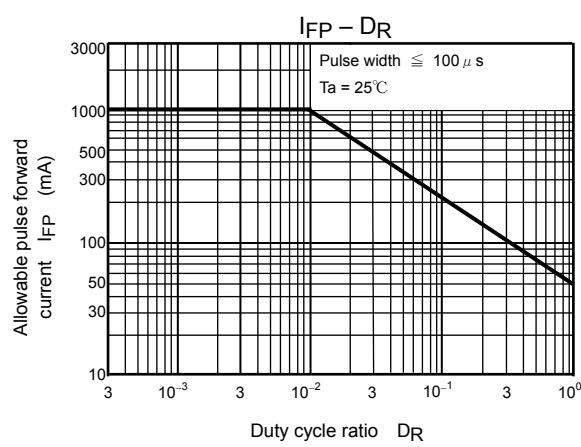
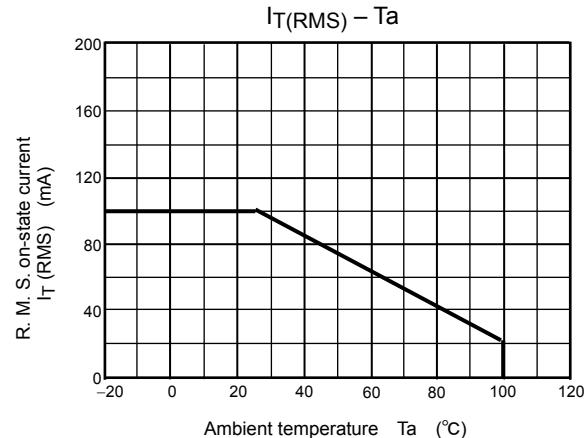
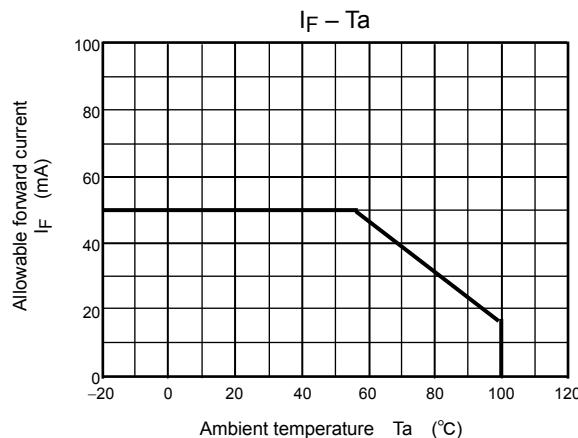
Individual Electrical Characteristics ($T_a = 25^\circ C$)

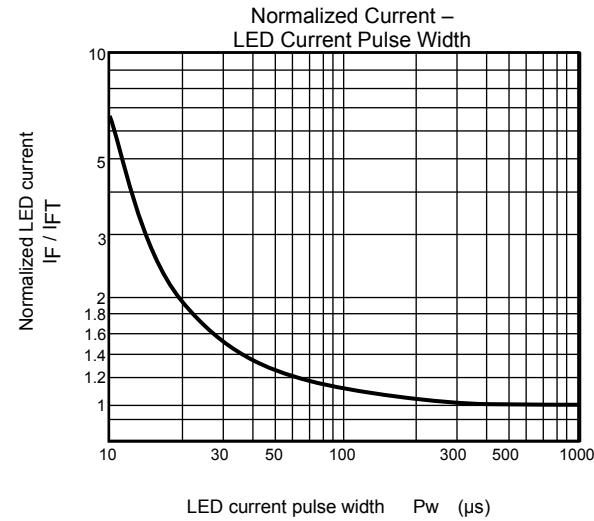
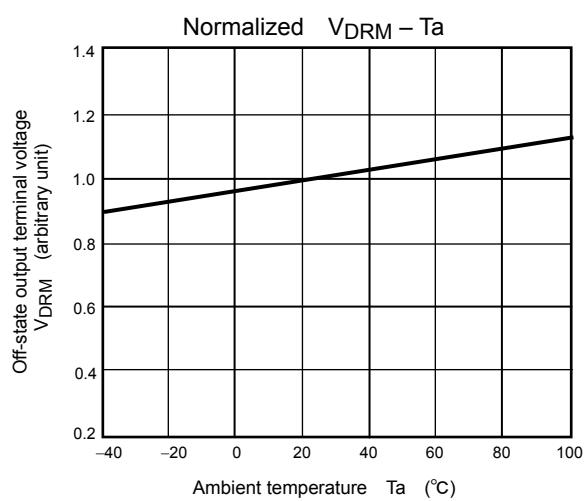
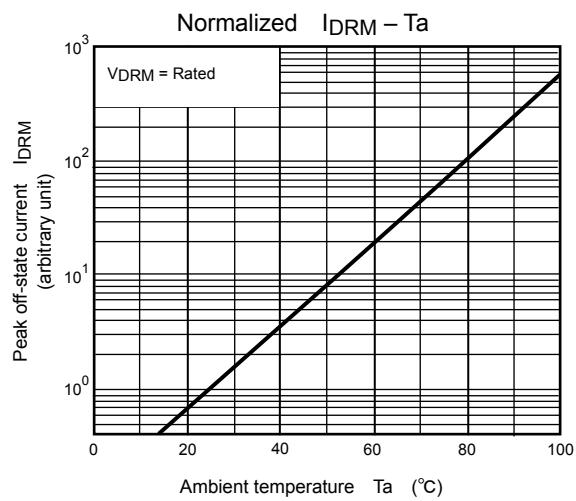
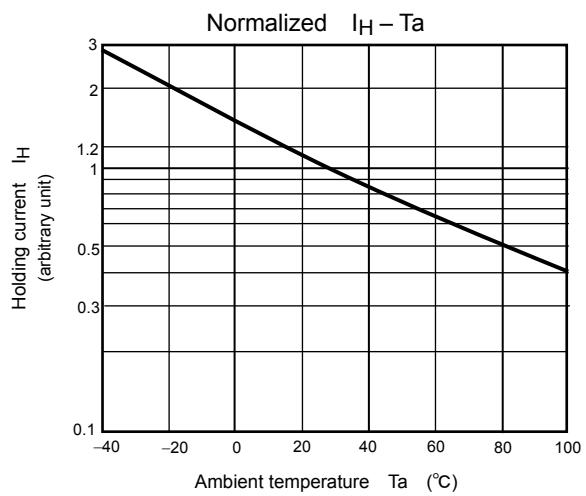
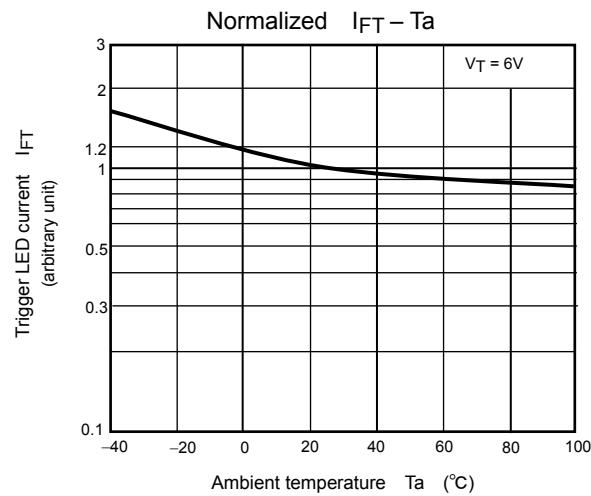
Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
LED	Forward voltage	V_F	$I_F=10\text{mA}$	1.0	1.15	1.3	V
	Reverse current	I_R	$V_R=5\text{V}$	—	—	10	μA
	Capacitance	C_T	$V=0, f=1\text{MHz}$	—	10	—	pF
Detector	Peak off-state current	I_{DRM}	$V_{DRM}=600\text{V}$	—	10	1000	nA
	Peak on-state voltage	V_{TM}	$I_{TM}=100\text{mA}$	—	1.7	3.0	V
	Holding current	I_H	—	—	1.0	—	mA
	Critical rate of rise of off-state voltage	dv / dt	$V_{in}=240\text{V}_{rms}, T_a=85^\circ C$ (fig.1)	—	500	—	$\text{V} / \mu\text{s}$
	Critical rate of rise of commutating voltage	$dv / dt(c)$	$V_{in}=60\text{ V}_{rms}, I_T=15\text{mA}$ (fig.1)	—	0.2	—	$\text{V} / \mu\text{s}$

Coupled Electrical Characteristics ($T_a = 25^\circ C$)

Characteristic		Symbol	Test Condition	Min.	Typ.	Max.	Unit
Trigger LED current	I_{FT}	$V_T=6\text{V}, R_L=100\Omega$	—	5	10	—	mA
Capacitance (input to output)	C_S	$V_S=0, f=1\text{MHz}$	—	0.8	—	—	pF
Isolation resistance	R_S	$V_S=500\text{V}$	5×10^{10}	10^{14}	—	—	Ω
Isolation voltage	BVS	AC, 1 minute	2500	—	—	—	V_{rms}
		AC, 1 second, in oil	—	5000	—	—	
		DC, 1 minute, in oil	—	5000	—	—	V_{dc}

Fig.1: dv / dt test circuit





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20070701-EN

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- Защита от снятия компонента с производства.



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