MOSFETs Silicon N-Channel MOS (U-MOSVII-H)

TPCA8057-H

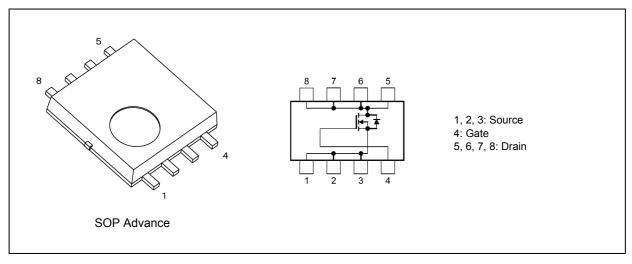
1. Applications

- High-Efficiency DC-DC Converters
- Notebook PCs
- Mobile Handsets

2. Features

- (1) Small footprint due to a small and thin package
- (2) High-speed switching
- (3) Small gate change: $Q_{SW} = 14 \text{ nC}$ (typ.)
- (4) Low drain-source on-resistance: $R_{DS(ON)} = 2.6 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 4.5 \text{ V})$
- (5) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 30 \ V)$
- (6) Enhancement mode: V_{th} = 1.3 to 2.3 V (V_{DS} = 10 V, I_D = 0.5 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteri	Symbol	Rating	Unit		
Drain-source voltage			V _{DSS}	30	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)		(Note 1)	Ι _D	42	Α
Drain current (pulsed)		(Note 1)	I _{DP}	126	
Power dissipation	(T _c = 25°C)		PD	57	W
Power dissipation	(t = 10 s)	(Note 2)	PD	2.8	W
Power dissipation	(t = 10 s)	(Note 3)	PD	1.6	W
Single-pulse avalanche energy		(Note 4)	E _{AS}	229	mJ
Avalanche current			I _{AR}	42	Α
Channel temperature			T _{ch}	150	°C
Storage temperature			T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics				Max	Unit
Channel-to-case thermal resistance	(T _c = 25°C)		R _{th(ch-c)}	2.19	°C/W
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 2)	R _{th(ch-a)}	44.6	
Channel-to-ambient thermal resistance	(t = 10 s)	(Note 3)	R _{th(ch-a)}	78.1	°C/W

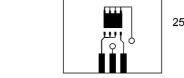
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Device mounted on a glass-epoxy board (a), Figure 5.1

Note 3: Device mounted on a glass-epoxy board (b), Figure 5.2

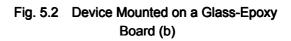
Note 4: V_DD = 24 V, T_ch = 25°C (initial), L = 0.1 mH, R_G = 1 Ω , I_{AR} = 42 A





FR-4 25.4 × 25.4 × 0.8 (Unit: mm)

Fig. 5.1 Device Mounted on a Glass-Epoxy Board (a)



Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

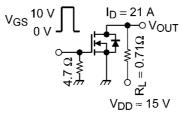
6. Electrical Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

6.1. Static Characteristics

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_	_	±0.1	μA
Drain cut-off current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	30	_	—	V
	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	15	_	_	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 0.5 mA	1.3	_	2.3	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = 4.5 V, I _D = 21 A		2.6	3.2	mΩ
		V _{GS} = 10 V, I _D = 21 A		2.0	2.6	

6.2. Dynamic Characteristics

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	4300	5200	pF
Reverse transfer capacitance	C _{rss}		_	240	370	
Output capacitance	C _{oss}		_	810	—	
Gate resistance	r _g	V _{DS} = 10 V, V _{GS} = 0 V, f = 5 MHz	_	1.4	2.1	Ω
Switching time (rise time)	tr	See Figure 6.2.1.	_	4.3	—	ns
Switching time (turn-on time)	t _{on}		_	14	—	
Switching time (fall time)	t _f		_	6.3	_	
Switching time (turn-off time)	t _{off}		_	52	_	



Duty \leq 1%, t_w = 10 μ s

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics

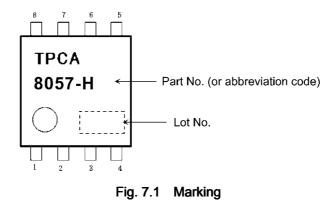
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge	Qg	$V_{DD} \approx 24$ V, V_{GS} = 10 V, I_D = 42 A	_	61	—	nC
(gate-source plus gate-drain)		$V_{DD} \approx 24$ V, V_{GS} = 5 V, I_D = 42 A	_	31	—	
Gate-source charge 1	Q _{gs1}	$V_{DD} \approx 24$ V, V_{GS} = 10 V, I_D = 42 A	_	13	_	
Gate-drain charge	Q _{gd}		_	7.7	_	
Gate switch charge	Q _{SW}			14	_	

6.4. Source-Drain Characteristics

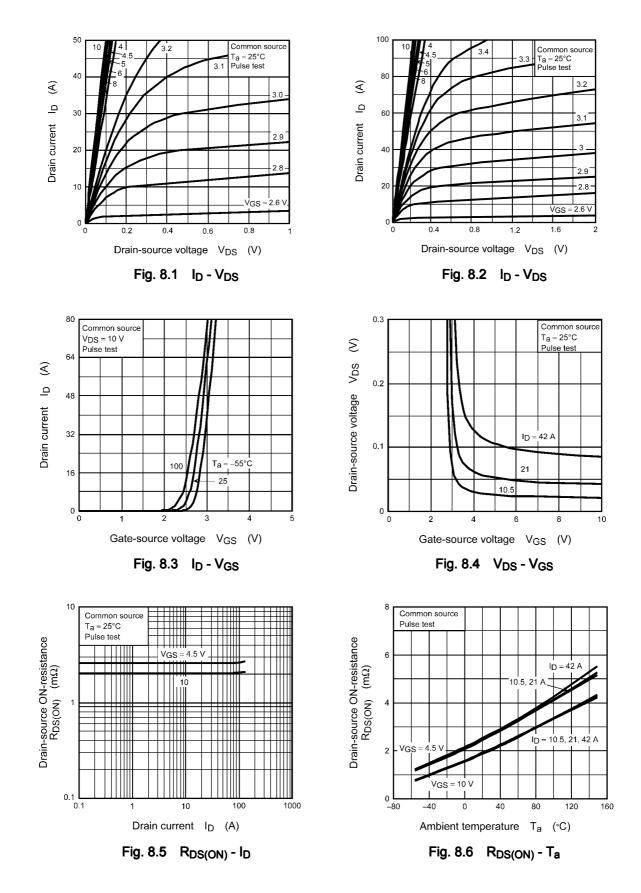
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Pulsed reverse drain current ((Note 5)	I _{DRP}	—	_	_	126	Α
Diode forward voltage		V_{DSF}	I _{DR} = 42 A, V _{GS} = 0 V	—		-1.2	V

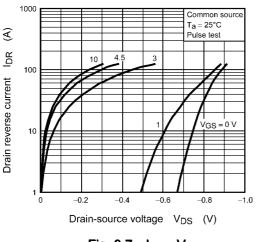
Note 5: Ensure that the channel temperature does not exceed 150°C.

7. Marking

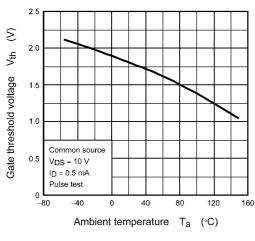


8. Characteristics Curves (Note)

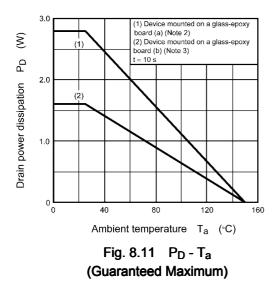












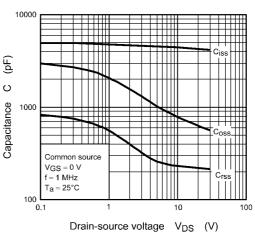


Fig. 8.8 Capacitance - V_{DS}

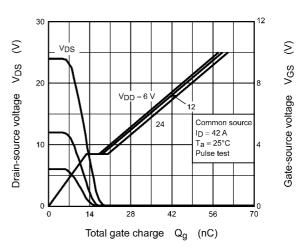
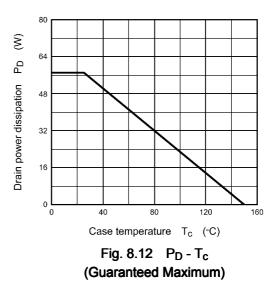
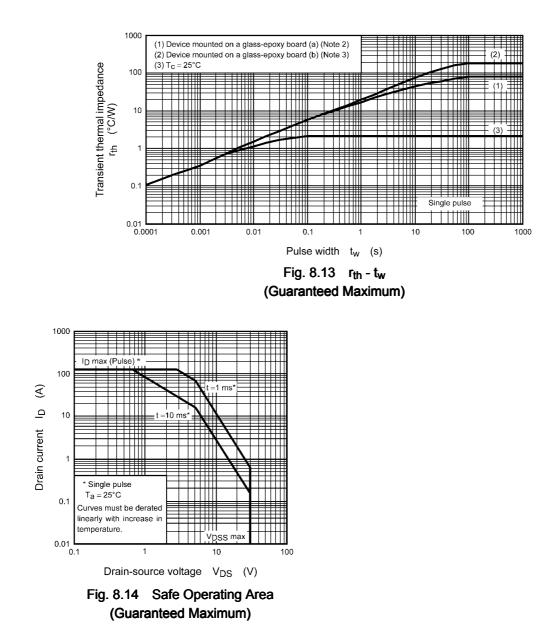


Fig. 8.10 Dynamic Input/Output Characteristics





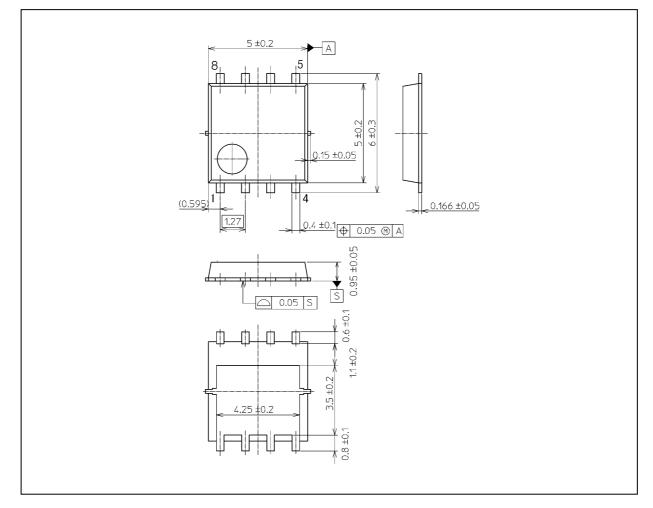


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

TPCA8057-H

Package Dimensions

Unit: mm



Weight: 0.069 g (typ.)

Package Name(s)

TOSHIBA: 2-5Q1S

Nickname: SOP Advance

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