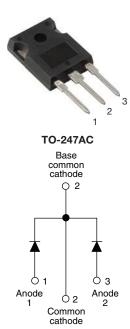




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Vishay Semiconductors

# HEXFRED® Ultrafast Soft Recovery Diode, 2 x 8 A



PRODUCT SUMMARY							
Package	TO-247AC						
I <sub>F(AV)</sub>	2 x 8 A						
$V_{R}$	1200 V						
V <sub>F</sub> at I <sub>F</sub>	3.3 V						
t <sub>rr</sub> typ.	28 ns						
T <sub>J</sub> max.	150 °C						
Diode variation	Single die						

### **FEATURES**

- Ultrafast and ultrasoft recovery
- Very low I<sub>RRM</sub> and Q<sub>rr</sub>
- Designed and qualified according to JEDEC-JESD47
- Material categorization:
   For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>





ROHS
COMPLIANT
HALOGEN
FREE
Available

#### **BENEFITS**

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- Higher frequency operation
- · Reduced snubbing
- Reduced parts count

#### **DESCRIPTION**

VS-HFA16PA120C... is a state of the art ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 1200 V and 8 A per leg continuous current, the VS-HFA16PA120C... is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I<sub>RRM</sub>) and does not exhibit any tendency to "snap-off" during the tb portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED VS-HFA16PA120C... is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Cathode to anode voltage	V <sub>R</sub>		1200	V					
Maximum continuous forward current	1	T <sub>C</sub> = 100 °C	8						
per device	l <sub>F</sub>		16	^					
Single pulse forward current	I <sub>FSM</sub>		130	А					
Maximum repetitive forward current	I <sub>FRM</sub>		32						
Maximum naviar discination	0	T <sub>C</sub> = 25 °C	73.5	10/					
Maximum power dissipation	$P_{D}$	T <sub>C</sub> = 100 °C	29	W					
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to + 150	°C					



# VS-HFA16PA120CPbF, VS-HFA16PA120C-N3

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<b>ELECTRICAL SPECIFICATIONS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Cathode to anode breakdown voltage	$V_{BR}$	Ι <sub>R</sub> = 100 μΑ	1200	-	-				
Maximum forward voltage		I <sub>F</sub> = 8.0 A	-	2.6	3.3	V			
	$V_{FM}$	I <sub>F</sub> = 16 A -		3.4	4.3				
		I <sub>F</sub> = 8.0 A, T <sub>J</sub> = 125 °C	-	2.4	3.1				
Maximum reverse		V <sub>R</sub> = V <sub>R</sub> rated	-	0.31	10				
leakage current	I <sub>RM</sub>	$T_J = 125$ °C, $V_R = 0.8 \times V_R$ rated	-	135	1000	μΑ			
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	-	11	20	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package	-	8.0	-	nH			

<b>DYNAMIC RECOVERY CHARACTERISTICS PER LEG</b> (T <sub>J</sub> = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS			
	t <sub>rr</sub>	$I_F = 1.0 \text{ A}, dI_F/dt = 200$	$I_F = 1.0 \text{ A}, dI_F/dt = 200 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		28	-			
Reverse recovery time	t <sub>rr1</sub>	T <sub>J</sub> = 25 °C		-	63	95	ns		
	t <sub>rr2</sub>	T <sub>J</sub> = 125 °C		-	106	160			
Peak recovery current	I <sub>RRM1</sub>	T <sub>J</sub> = 25 °C		-	4.5	8.0	A nC		
	I <sub>RRM2</sub>	T <sub>J</sub> = 125 °C	$I_F = 8.0 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$	-	6.2	11			
Dovorno rocoveni chargo	Q <sub>rr1</sub>	T <sub>J</sub> = 25 °C	$V_{\rm R} = 200 \text{ V}$	-	140	380			
Reverse recovery charge	Q <sub>rr2</sub>	T <sub>J</sub> = 125 °C	-11 -22 -	-	335	880	IIC		
Peak rate of recovery current during t <sub>b</sub>	dI <sub>(rec)M</sub> /dt1	T <sub>J</sub> = 25 °C		-	133	-	- A/μs		
	dI <sub>(rec)M</sub> /dt2	T <sub>J</sub> = 125 °C		-	85	-			

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Lead temperature	T <sub>lead</sub>	0.063" from case (1.6 mm) for 10 s	-	-	300	°C			
Thermal resistance, junction to case	R <sub>thJC</sub>		-	-	1.7				
Thermal resistance, junction to ambient	R <sub>thJA</sub>	Typical socket mount	-	-	40	K/W			
Thermal resistance, case to heatsink	R <sub>thCS</sub>	R <sub>thCS</sub> Mounting surface, flat, smooth and greased		0.25	-				
Weight			-	6.0	-	g			
vveignt			-	0.21	-	OZ.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-247AC (JEDEC)	HFA16PA120C						

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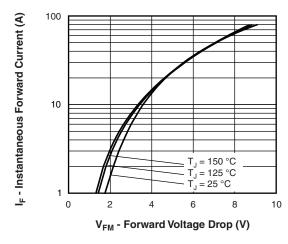


Fig. 1 - Maximum Forward Voltage Drop Characteristics

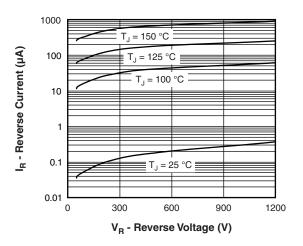


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

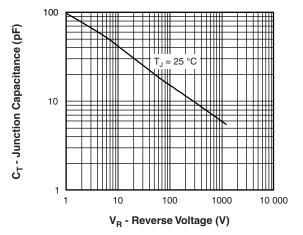


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

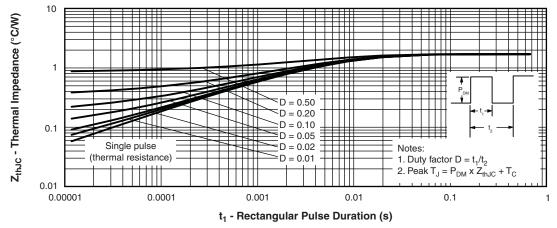


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics



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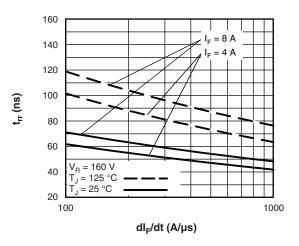


Fig. 5 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

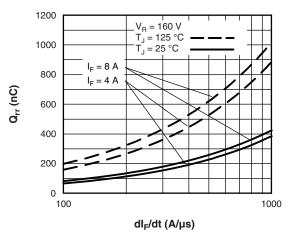


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

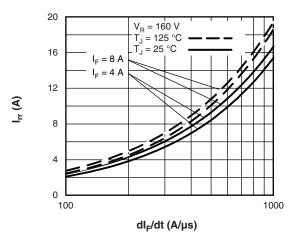


Fig. 6 - Typical Recovery Current vs.  $dI_F/dt$ 

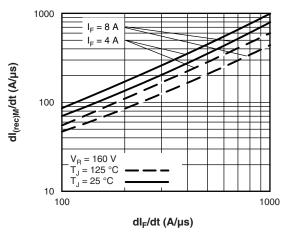


Fig. 8 - Typical  $dI_{(rec)M}/dt$  vs.  $dI_F/dt$ 

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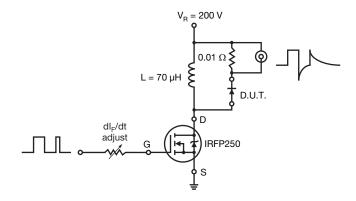
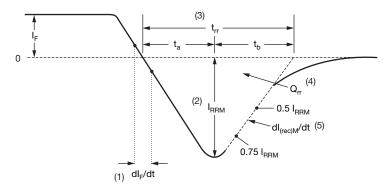


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_F$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{rr}$  area under curve defined by  $\mathbf{t}_{rr}$  and  $\mathbf{I}_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $dl_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

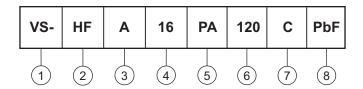
Fig. 10 - Reverse Recovery Waveform and Definitions

# VS-HFA16PA120CPbF, VS-HFA16PA120C-N3

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#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

- HEXFRED® family

- Electron irradiated

Current rating (16 = 16 A)

**5** - PA = TO-247AC

6 - Voltage rating: (120 = 1200 V)

7 - Circuit configuration

C = Common cathode

7 - Environmental digit:

PbF = Lead (Pb)-free and RoHS compliant

-N3 = Halogen-free, RoHS compliant and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-HFA16PA120CPbF	25	500	Antistatic plastic tube						
VS-HFA16PA120C-N3	25	500	Antistatic plastic tube						

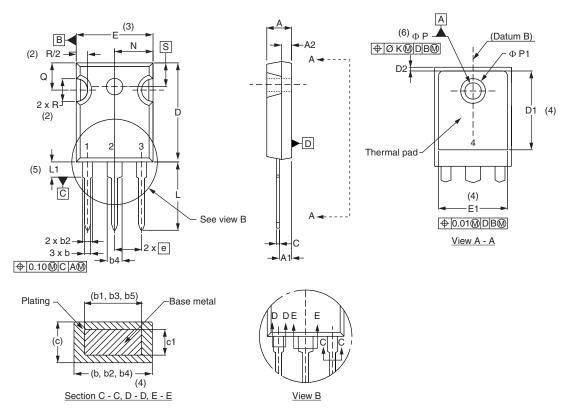
LINKS TO RELATED DOCUMENTS							
Dimensions		www.vishay.com/doc?95542					
Part marking information	TO-247ACPbF	www.vishay.com/doc?95226					
	TO-247AC-N3	www.vishay.com/doc?95007					



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### TO-247 - 50 mils L/F

### **DIMENSIONS** in millimeters and inches



CVMPOL	MILLIMETERS		INC	INCHES NOTES	CVMPOL	MILLIM	IETERS	INC	HES	NOTEC		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØK	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			N	7.62 BSC 0.3		.3		
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000



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- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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