

The Future of Analog IC Technology

# DESCRIPTION

The MP2014 is a low power linear regulator that supplies power to systems with high voltage batteries. It includes a wide 3V to 40V input range, low dropout voltage and low quiescent supply current. The low guiescent current and low dropout voltage allow operations at extremely low power levels. Therefore, the MP2014 is ideal for the low power microcontrollers and the battery-powered equipments.

The MP2014 provides wide variety of fixed output voltage options (if request): 1.8V, 1.9V, 2.3V, 2.5V, 3.0V, 3.3V, 3.45V, 5.0V.

The regulator output current is internally limited and the device is protected against short-circuit, over-load and over-temperature conditions.

The MP2014 also includes thermal shutdown and current limiting fault protection, and is available in TO252-5 package.

# FEATURES

- 3V to 40V Input Range
- 10µA Quiescent Supply Current
- Stable with Low-Value Output Ceramic Capacitor (>0.47µF)
- 500mA Specified Current
- Fixed Output Voltage
- Output ±2% Accuracy
- Specified Current Limit
- Power Good •
- Programmable Power Good Delay
- Thermal Shutdown and Short-Circuit Protection
- -40°C to +150°C Specified Junction **Temperature Range**
- Available in TO252-5 Package

### APPLICATIONS

- Industrial/Automotive Applications
- Portable/Battery-Powered Equipment •
- **Ultra Low Power Microcontrollers** •
- **Cellular Handsets**
- Medical Imaging

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### TYPICAL APPLICATION





### **ORDERING INFORMATION**

Part Number*	Package	Top Marking		
MP2014GZD	TO252-5	See Below		
MP2014GZD-33	TO252-5	See Below		

\* For Tape & Reel, add suffix –Z (e.g. MP2014GZD–Z)

### TOP MARKING (MP2014GZD)

# MPSYYWW MP2014

#### LLLLLLLL

MPS: MPS Prefix YY: Year Code WW: Week Code MP2014: Product Code of MP2014GZD LLLLLLLL: Lot Number

### TOP MARKING (MP2014GZD-33)

MPS YYWW MP2014-33 LLLLLLLL

MPS: MPS Prefix YY: Year Code WW: Week Code MP2014-33: Product Code of MP2014GZD-33 LLLLLLLL: Lot Number

### PACKAGE REFERENCE





### ABSOLUTE MAXIMUM RATINGS (1)

OUT0.3V to +17V
PG0.3V to +15V
PGDL0.3V to +6V
Lead Temperature
Storage Temperature65°C to +150°C
Continuous Power Dissipation $(T_A = +25^{\circ}C)^{(2)}$
TO252-5

#### ESD SUSCEPTIBILITY<sup>(3)</sup>

HBM (Human Body M	/lode)4kV
MM (Machine Mode)	

## Recommended Operating Conditions <sup>(4)</sup>

## Thermal Resistance $^{(5)}$ $\theta_{JA}$ $\theta_{JC}$

TO252-5 ...... 55 ...... 3.... °C/W

#### Notes:

- 1) Exceeding these ratings may damage the device.
- 2) The maximum allowable power dissipation is a function of the maximum junction temperature T<sub>J</sub> (MAX), the junction-toambient thermal resistance  $\theta_{JA}$ , and the ambient temperature T<sub>A</sub>. The maximum allowable continuous power dissipation at any ambient temperature is calculated by P<sub>D</sub> (MAX) = (T<sub>J</sub> (MAX)-T<sub>A</sub>)/ $\theta_{JA}$ . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
- 3) Devices are ESD sensitive. Handling precaution recommended.
- The device is not guaranteed to function outside of its operating conditions.
- 5) Measured on JESD51-7, 4-layer PCB.

# **ELECTRICAL CHARACTERISTICS**

 $V_{IN}$  =13.5V,  $T_J$  = +25°C, unless otherwise noted.

Parameter	Symbol	Condition	Min	Тур	Max	Units
		0 <i<sub>OUT&lt;1mA</i<sub>		12	17	
GND Pin Current	I <sub>GND</sub>	1mA <i<sub>OUT&lt;30mA</i<sub>		16	22	μA
		30mA <i<sub>OUT&lt;500mA</i<sub>		105	150	1
Load Current Limit	I <sub>LIMIT</sub>	V <sub>OUT</sub> = 0V	550	900	1450	mA
Output Voltage accuracy		MP2014GZD, $V_{IN}$ =6V to 40V, $I_{LOAD}$ = 5mA	4.9	5	5.1	V
		MP2014GZD-33, V <sub>IN</sub> =4.3V to 40V, $I_{LOAD}$ = 5mA	3.234	3.3	3.366	
Dropout Voltage <sup>(6)</sup>	V <sub>DROPOUT</sub>	$\begin{array}{llllllllllllllllllllllllllllllllllll$		400	650	- mV
		$\begin{array}{llllllllllllllllllllllllllllllllllll$		500	700	
		$\begin{array}{llllllllllllllllllllllllllllllllllll$		750	1000	
		$\begin{array}{llllllllllllllllllllllllllllllllllll$		1000	1300	
Line Regulation		$V_{IN}$ = 8V to 40V, $I_{LOAD}$ = 5mA	-10	1	10	mV
Load Regulation		$I_{LOAD}$ = 5mA to 500mA		1	15	mV
		100Hz, C <sub>OUT</sub> = 10µF, I <sub>LOAD</sub> =10mA		57		dB
Output Voltage PSRR <sup>(7)</sup>		$1$ kHz, C <sub>OUT</sub> = $10\mu$ F, I <sub>LOAD</sub> = $10$ mA		45		dB
		100kHz, $C_{OUT}$ = 10µF, $I_{LOAD}$ =10mA		51		dB
Startup Response Time		$\begin{array}{llllllllllllllllllllllllllllllllllll$		1	2	ms
		$\begin{array}{llllllllllllllllllllllllllllllllllll$		0.6	1.5	
PG Rising Threshold			90%	93%	96%	V <sub>OUT</sub>
PG Rising Threshold Hysteresis				5%		$V_{\text{OUT}}$
PG Low Voltage		Sink 1mA current		0.1	0.4	V
PG Leakage Current		V <sub>PG</sub> =5V			1	μA
PGDL Charging Current	I <sub>PGDL</sub>	V <sub>PGDL</sub> =1V	3	5.5	9	μA
PGDL Rising Threshold			1.5	1.65	2	V
PGDL Falling Threshold			0.2	0.4	0.7	V
PG Delay Time	t <sub>PGDL</sub>	C <sub>PGDL</sub> =47nF, 10% to 90% PGDL Rising Threshold	6	11	14	ms
PG Reaction Time		C <sub>PGDL</sub> =47nF		0.5	2	μs
Thermal Shutdown <sup>(7)</sup>	$T_{SD}$			165		°C
Thermal Shutdown Hysteresis <sup>(7)</sup>	ΔTSD			30		°C

#### Notes:

6) Dropout voltage: Measured when the output voltage V<sub>OUT</sub> has dropped 100mV from the nominal value obtained at V<sub>IN</sub>=13.5V.

7) Derived from bench characterization. Not tested in production.



# **PIN FUNCTIONS**

Pin #	Name	Description	
1	IN	Input voltage. Connect a 3V to 40V supply to this pin.	
2	PG	Power good.	
3	GND	Ground (internally connected to the exposed pad, GND pin and the exposed pad must be connected to the same ground plane)	
4	PGDL	Programmable power good delay time.	
5	OUT	Regulated output voltage, only low-value ceramic capacitor (≥0.47µF) on output is required for stability.	





# TYPICAL PERFORMANCE CHARACTERISTICS (continued)



# Short-Circuit Steady State $V_{IN}$ =12V





### **OPERATION**

The MP2014 is a linear regulator that supplies power to systems with high voltage batteries. It includes a wide 3V to 40V input range, low dropout voltage and low quiescent supply current.

The MP2014 provides wide variety of fixed output voltage options: 1.8V, 1.9V, 2.3V, 2.5V, 3.0V, 3.3V, 3.45V, 5.0V.

#### **Short-Circuit Protection**

The regulator output current is internally limited and the device is protected against short-circuit, over-load. The peak output current is limited to around 900mA, which exceeds the 500mA recommended continuous output current.

#### **Thermal Shutdown**

When the junction temperature exceeds the upper threshold ( $165^{\circ}$ C), the thermal sensor sends a signal to the control logic to shutdown the IC. The IC will restart when the temperature has sufficiently cooled ( $135^{\circ}$ C).

The maximum power output current is a function of the package's maximum power dissipation for a given temperature.

The maximum power dissipation is dependent on the thermal resistance of the case and the circuit board, the temperature difference between the die junction and the ambient air, and the rate of air flow. The GND pin and Exposed Pad must be connected to the ground plane for proper dissipation.

#### **Power Good Output**

MP2014 has one power good (PG) pin. The PG pin is the open drain of an internal MOSFET. It should be connected to  $V_{OUT}$  or external voltage source(<15V) through a resistor (i.e. 100kohm). After the  $V_{OUT}$  reaches 93% of nominal value, the MOSFET turns off and PG pin is pulled to high by  $V_{OUT}$  or external voltage source. When the  $V_{OUT}$  drops to 88% of nominal value, the PG voltage is pulled to GND.

There is a delay time when PG asserts high. The delay time can be programmed by adding a capacitor on PGDL. To select a capacitor for PGDL, use below equation:

$$C_{\text{PGDL}}(nF) = \frac{t_{\text{PGDL}}(ms) \times I_{\text{PGDL}}(\mu A)}{V_{\text{th PGDL}}(V)}$$

Where  $t_{PGDL}$  is the desired delay time for PG asserts high,  $I_{PGDL}$  is the PGDL charging current and  $V_{th_PGDL}$  is 1.65V.

Figure 2 shows the power good timing.



Figure 1: Functional Block Diagram







Figure 2: Power Good Timing

# **APPLICATION INFORMATION**

#### **Component Selection**

#### **Input Capacitor**

For proper operation, place a ceramic capacitor (C1) between  $1\mu$ F and  $10\mu$ F of dielectric type X5R or X7R between the input pin and ground. Larger values in this range will help improve line transient response.

#### **Output Capacitor**

For stable operation, use a ceramic capacitor (C2) of type X5R or X7R between  $1\mu$ F and  $22\mu$ F. Larger values in this range will help improve load transient response and reduce noise. Output capacitors of other dielectric types may be used, but are not recommended as their capacitance can deviate greatly from their rated value over temperature.

#### **PCB Layout Guide**

PCB layout is very important to achieve good regulation, ripple rejection, transient response and thermal performance. It is highly recommended to duplicate EVB layout for optimum performance.

If change is necessary, please follow these guidelines and take figure 3 for reference.

- Input and output bypass ceramic capacitors are suggested to be put close to the IN Pin and OUT Pin respectively.
- Connect IN, OUT and especially GND respectively to a large copper area to cool the chip to improve thermal performance and long-term reliability.







Bottom Layer Figure 3: PCB Layout



#### **DESIGN EXAMPLE**

Below is a design example following the application guidelines.



Figure 4: Design Example



# **TYPICAL APPLICATION CIRCUITS**



**Figure 5: Typical Application Circuit** 



# **PACKAGE INFORMATION**



TO252-5

 CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS
PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
PACWING CONFORMS TO JEDEC TO-252.
DRAWING IS NOT TO SCALE.

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- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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