

TC74LCX125F, TC74LCX125FN, TC74LCX125FT, TC74LCX125FK

Low-Voltage Quad Bus Buffer with 5-V Tolerant Inputs and Outputs

The TC74LCX125 is a high-performance CMOS quad bus buffers. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) VCC applications, but it could be used to interface to 5-V supply environment for inputs.

This device requires the 3-state control input \overline{OE} to be set high to place the output into the high impedance state.

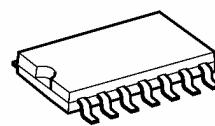
All inputs are equipped with protection circuits against static discharge.

Features

- Low-voltage operation: $V_{CC} = 2.0$ to 3.6 V
- High-speed operation: $t_{pd} = 6.0$ ns (max) ($V_{CC} = 3.0$ to 3.6 V)
- Output current: $|I_{OH}| / I_{OL} = 24$ mA (min) ($V_{CC} = 3.0$ V)
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 125 type

Note: xxxFN (JEDEC SOP) is not available in Japan.

TC74LCX125F



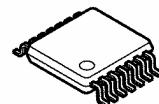
SOP14-P-300-1.27A

TC74LCX125FN



SOL14-P-150-1.27

TC74LCX125FT



TSSOP14-P-0044-0.65A

TC74LCX125FK

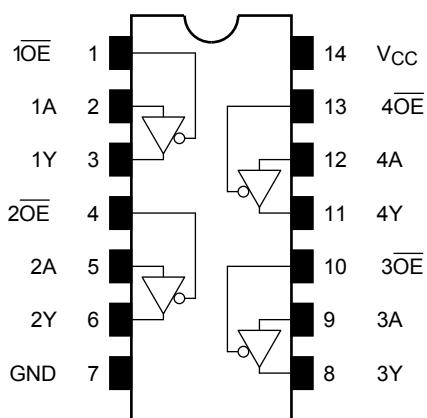


VSSOP14-P-0030-0.50

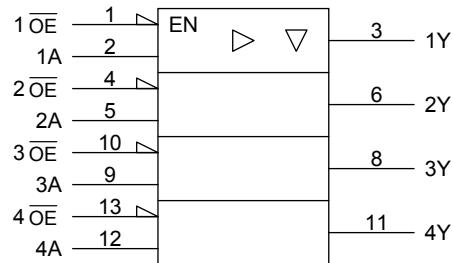
Weight

| | |
|----------------------|-----------------|
| SOP14-P-300-1.27A | : 0.18 g (typ.) |
| SOL14-P-150-1.27 | : 0.12 g (typ.) |
| TSSOP14-P-0044-0.65A | : 0.06 g (typ.) |
| VSSOP14-P-0030-0.50 | : 0.02 g (typ.) |

Pin Assignment (top view)



IEC Logic Symbol



Truth Table

| Inputs | | Outputs |
|-----------------|---|---------|
| \overline{OE} | A | Y |
| H | X | Z |
| L | L | L |
| L | H | H |

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|------------------|---------------------------------|------|
| Power supply voltage | V_{CC} | -0.5 to 7.0 | V |
| DC input voltage | V_{IN} | -0.5 to 7.0 | V |
| DC output voltage | V_{OUT} | -0.5 to 7.0 (Note 2) | V |
| | | -0.5 to $V_{CC} + 0.5$ (Note 3) | |
| Input diode current | I_{IK} | -50 | mA |
| Output diode current | I_{OK} | ± 50 (Note 4) | mA |
| DC output current | I_{OUT} | ± 50 | mA |
| Power dissipation | P_D | 180 | mW |
| DC V_{CC} /ground current | I_{CC}/I_{GND} | ± 100 | mA |
| Storage temperature | T_{STG} | -65 to 150 | °C |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating range (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 2: Output in OFF state

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

| Characteristics | Symbol | Rating | Unit |
|--------------------------|----------------------------------|-------------------------------|------|
| Power supply voltage | V _{CC} | 2.0 to 3.6 | V |
| | | 1.5 to 3.6 (Note 2) | |
| Input voltage | V _{IN} | 0 to 5.5 | V |
| Output voltage | V _{OUT} | 0 to 5.5 (Note 3) | V |
| | | 0 to V _{CC} (Note 4) | |
| Output current | I _{OH} /I _{OL} | ±24 (Note 5) | mA |
| | | ±12 (Note 6) | |
| Operating temperature | T _{opr} | -40 to 85 | °C |
| Input rise and fall time | d _t /d _v | 0 to 10 (Note 7) | ns/V |

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either V_{CC} or GND.

Note 2: Data retention only

Note 3: Output in OFF state

Note 4: High or low state

Note 5: V_{CC} = 3.0 to 3.6 V

Note 6: V_{CC} = 2.7 to 3.0 V

Note 7: V_{IN} = 0.8 to 2.0 V, V_{CC} = 3.0 V

Electrical Characteristics**DC Characteristics (Ta = -40 to 85°C)**

| Characteristics | | Symbol | Test Condition | V _{CC} (V) | Min | Max | Unit | |
|---------------------------------------|------------------|---|--|---------------------------|------------|----------------------|-------|--|
| Input voltage | H-level | V _{IH} | — | | 2.7 to 3.6 | 2.0 | — | |
| | L-level | V _{IL} | — | | 2.7 to 3.6 | — | 0.8 | |
| Output voltage | H-level | V _{OH} | V _{IN} = V _{IH} or V _{IL} | I _{OH} = -100 µA | 2.7 to 3.6 | V _{CC} -0.2 | — | |
| | | | | I _{OH} = -12 mA | 2.7 | 2.2 | — | |
| | | | | I _{OH} = -18 mA | 3.0 | 2.4 | — | |
| | | | | I _{OH} = -24 mA | 3.0 | 2.2 | — | |
| | L-level | V _{OL} | V _{IN} = V _{IH} or V _{IL} | I _{OL} = 100 µA | 2.7 to 3.6 | — | 0.2 | |
| | | | | I _{OL} = 12 mA | 2.7 | — | 0.4 | |
| | | | | I _{OL} = 16 mA | 3.0 | — | 0.4 | |
| | | | | I _{OL} = 24 mA | 3.0 | — | 0.55 | |
| Input leakage current | I _{IN} | V _{IN} = 0 to 5.5 V | | | 2.7 to 3.6 | — | ±5.0 | |
| 3-state output OFF state current | I _{OZ} | V _{IN} = V _{IH} or V _{IL} V _{OUT} = 0 to 5.5 V | | | 2.7 to 3.6 | — | ±5.0 | |
| Power-off leakage current | I _{OFF} | V _{IN} /V _{OUT} = 5.5 V | | | 0 | — | 10.0 | |
| Quiescent supply current | I _{CC} | V _{IN} = V _{CC} or GND | | | 2.7 to 3.6 | — | 10.0 | |
| | | V _{IN} /V _{OUT} = 3.6 to 5.5 V | | | 2.7 to 3.6 | — | ±10.0 | |
| Increase in I _{CC} per input | ΔI _{CC} | V _{IN} = V _{CC} - 0.6 V | | | 2.7 to 3.6 | — | 500 | |

AC Characteristics ($T_a = -40$ to 85°C)

| Characteristics | Symbol | Test Condition | V_{CC} (V) | Min | Max | Unit |
|------------------------|------------|--------------------|---------------|-----|-----|------|
| | | | 2.7 | — | 6.5 | |
| Propagation delay time | t_{PLH} | Figure 1, Figure 2 | 2.7 | — | 6.5 | ns |
| | t_{PHL} | | 3.3 ± 0.3 | 1.5 | 6.0 | |
| Output enable time | t_{PZL} | Figure 1, Figure 3 | 2.7 | — | 8.0 | ns |
| | t_{PZH} | | 3.3 ± 0.3 | 1.5 | 7.0 | |
| Output disable time | t_{PLZ} | Figure 1, Figure 3 | 2.7 | — | 7.0 | ns |
| | t_{PHZ} | | 3.3 ± 0.3 | 1.5 | 6.0 | |
| Output to output skew | t_{osLH} | (Note) | 2.7 | — | — | ns |
| | t_{osHL} | | 3.3 ± 0.3 | — | 1.0 | |

Note: Parameter guaranteed by design.

$$(t_{osLH} = |t_{PLHm} - t_{PLHn}|, t_{osHL} = |t_{PHLm} - t_{PHLn}|)$$

Dynamic Switching Characteristics ($T_a = 25^\circ\text{C}$, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500$ Ω)

| Characteristics | Symbol | Test Condition | V_{CC} (V) | Typ. | Unit |
|---------------------------------------|-------------|----------------------------------|--------------|------|------|
| | | | 2.7 | | |
| Quiet output maximum dynamic V_{OL} | V_{OLP} | $V_{IH} = 3.3$ V, $V_{IL} = 0$ V | 3.3 | 0.8 | V |
| Quiet output minimum dynamic V_{OL} | $ V_{OLV} $ | $V_{IH} = 3.3$ V, $V_{IL} = 0$ V | 3.3 | 0.8 | V |

Capacitive Characteristics ($T_a = 25^\circ\text{C}$)

| Characteristics | Symbol | Test Condition | V_{CC} (V) | Typ. | Unit | |
|-------------------------------|-----------|-------------------|--------------|------|------|----|
| | | | 2.7 | | | |
| Input capacitance | C_{IN} | — | 3.3 | 7 | pF | |
| Output capacitance | C_{OUT} | — | 3.3 | 8 | pF | |
| Power dissipation capacitance | C_{PD} | $f_{IN} = 10$ MHz | (Note) | 3.3 | 25 | pF |

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC(\text{opr})} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$$

AC Test Circuit

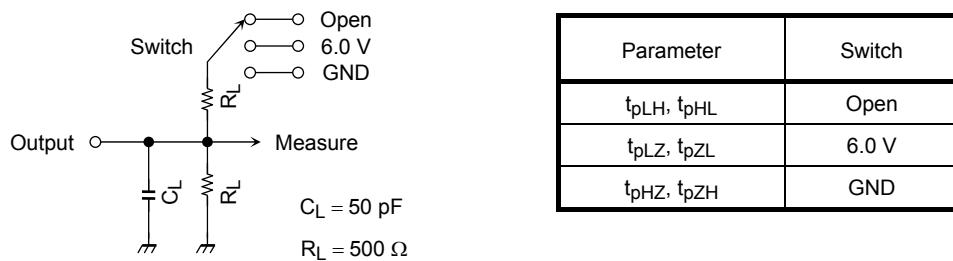
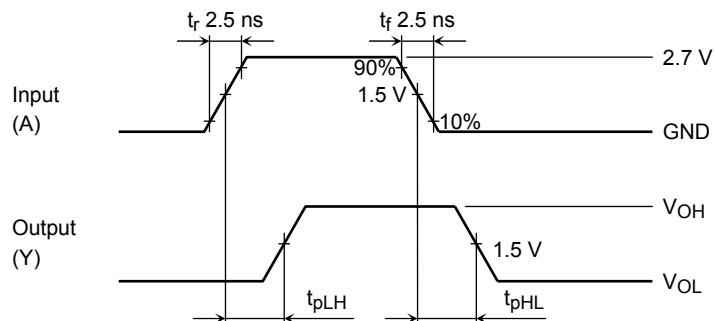
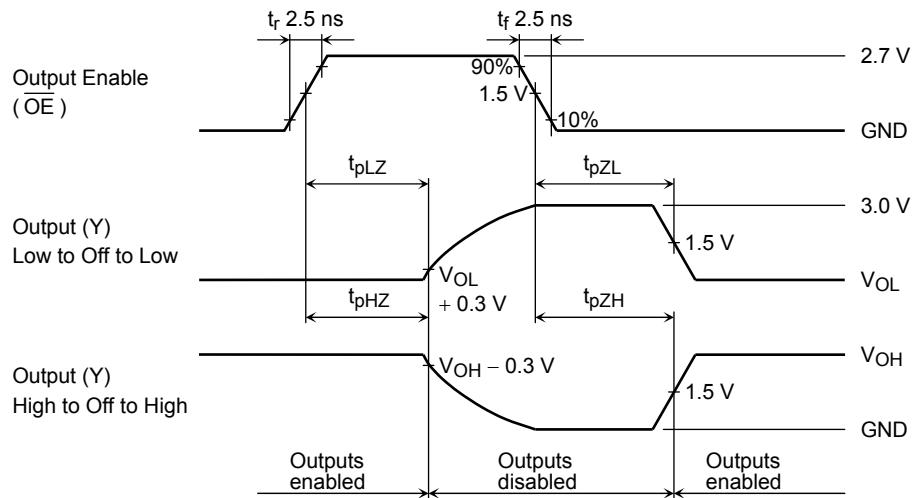


Figure 1

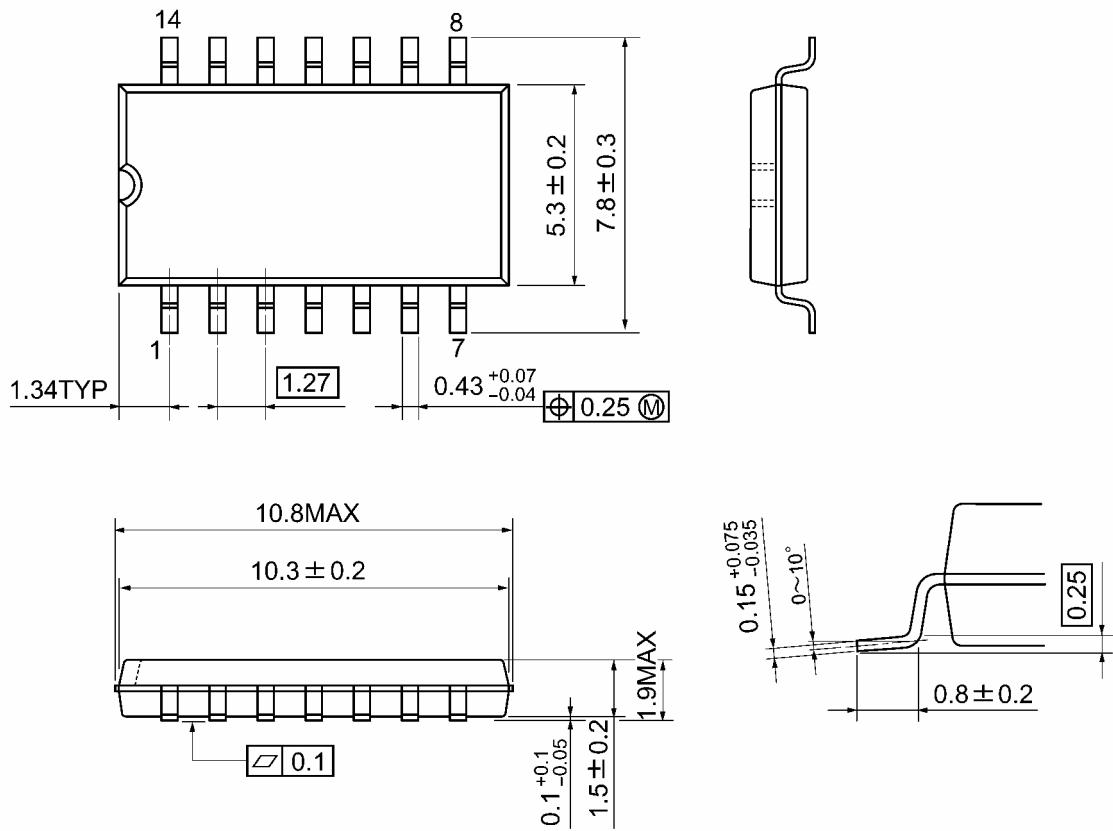
AC Waveform

Figure 2 t_{pLH}, t_{pHL} Figure 3 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$

Package Dimensions

SOP14-P-300-1.27A

Unit: mm

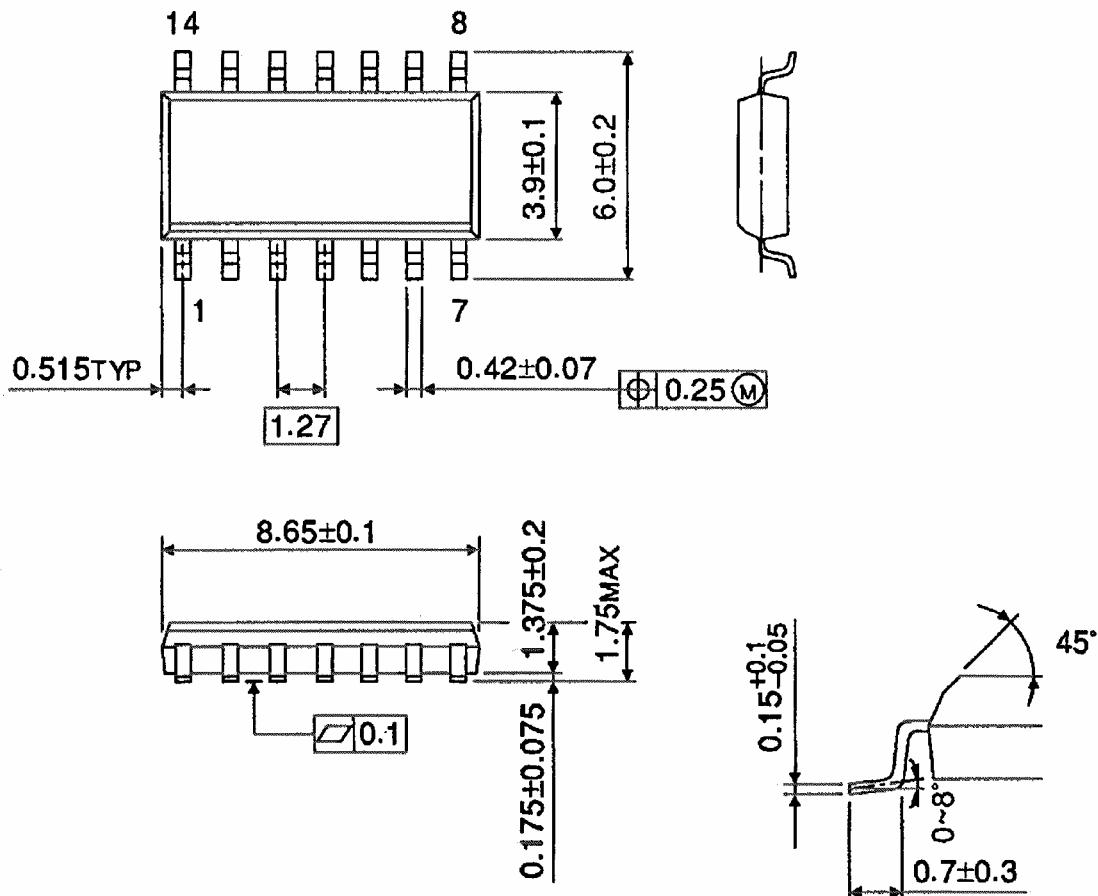


Weight: 0.18 g (typ.)

Package Dimensions (Note)

SOL14-P-150-1.27

Unit : mm



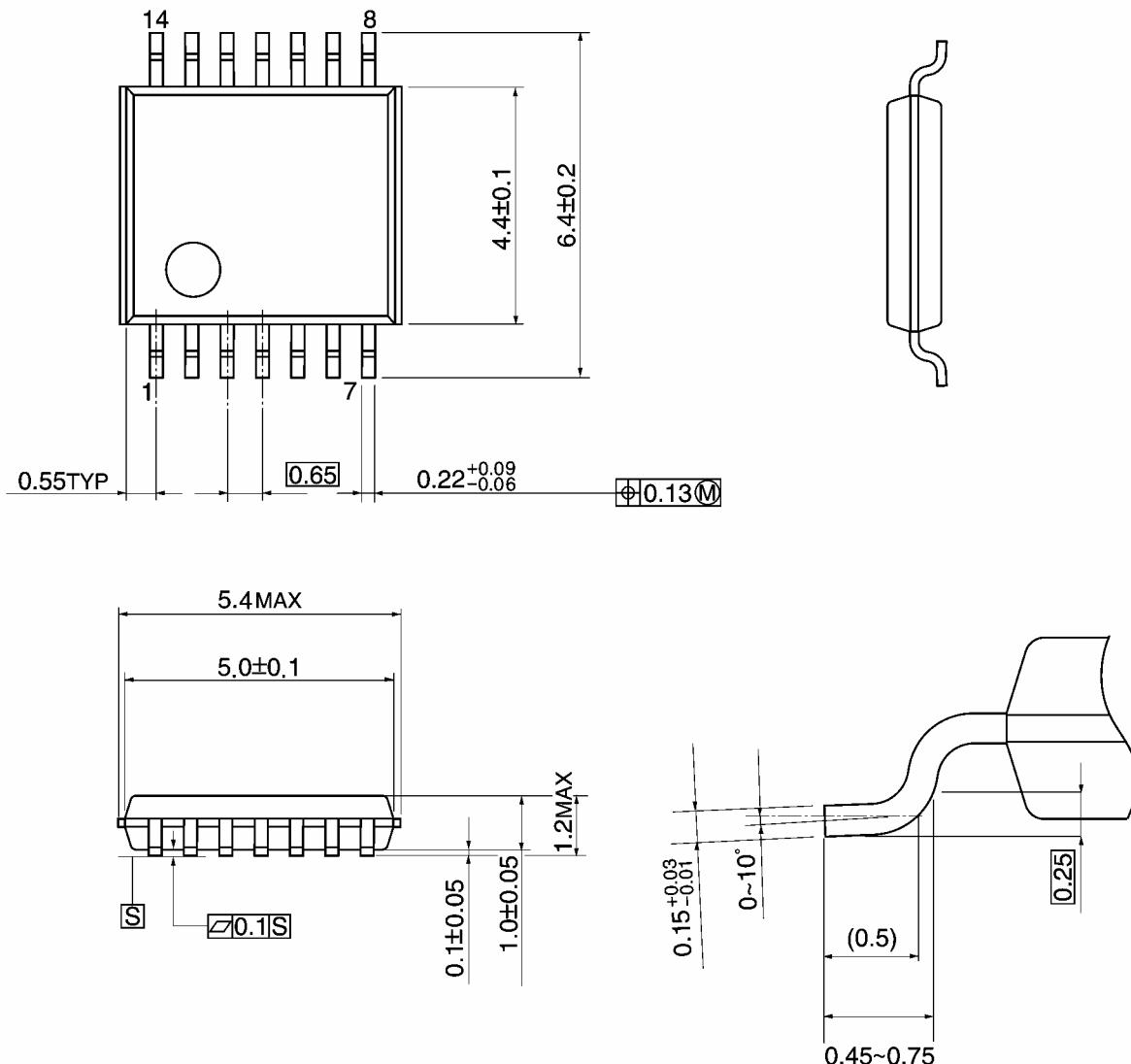
Note: This package is not available in japan.

Weight: 0.12 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm

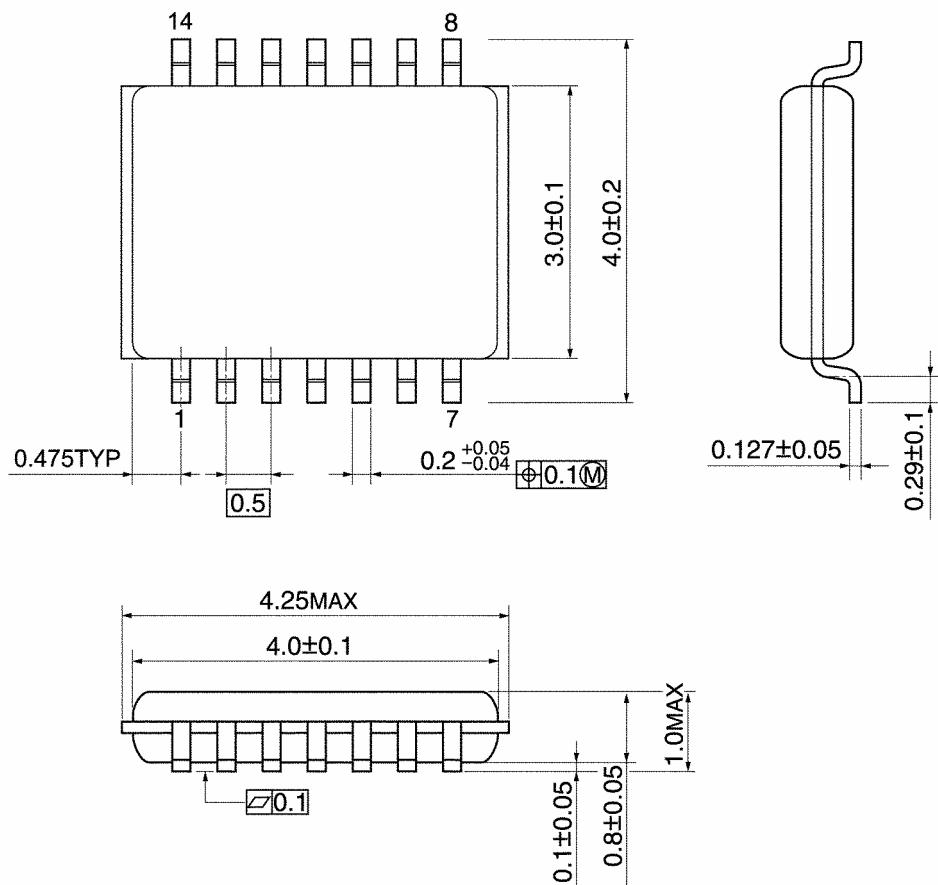


Weight: 0.06 g (typ.)

Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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20070701-EN GENERAL

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- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
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