

Automotive P-Channel 12 V (D-S) 175 °C MOSFET

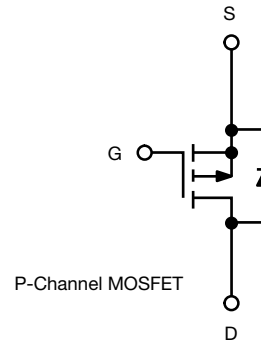
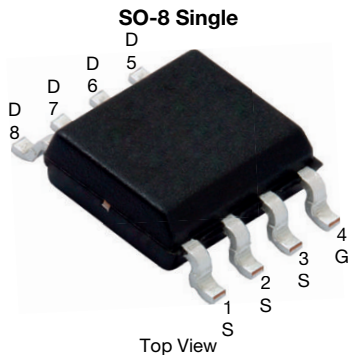
 AUTOMOTIVE
GRADE

RoHS
COMPLIANT
HALOGEN
FREE

PRODUCT SUMMARY	
V_{DS} (V)	-12
$R_{DS(on)}$ (Ω) at $V_{GS} = -4.5$ V	0.016
$R_{DS(on)}$ (Ω) at $V_{GS} = -2.5$ V	0.022
I_D (A)	-15
Configuration	Single
Package	SO-8

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified ^d
- 100 % R_g and UIS tested
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912


Marking Code: Q4005

ABSOLUTE MAXIMUM RATINGS ($T_C = 25$ °C, unless otherwise noted)			
PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DS}	-12	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current ^a	I_D	$T_C = 25$ °C	-15
		$T_C = 125$ °C	-8.7
Continuous Source Current (Diode Conduction) ^a	I_S	-5.4	A
Pulsed Drain Current ^b	I_{DM}	-60	mJ
Single Pulse Avalanche Current	I_{AS}	-20	
Single Pulse Avalanche Energy	E_{AS}	20	
Maximum Power Dissipation ^b	P_D	$T_C = 25$ °C	6
		$T_C = 125$ °C	2
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +175	°C
Soldering Recommendations (Peak Temperature)		260	

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient	R_{thJA}	92	°C/W
Junction-to-Foot (Drain)	R_{thJF}	25	

Notes

- Package limited.
- Pulse test; pulse width ≤ 300 μ s, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR4 material).
- Parametric verification ongoing

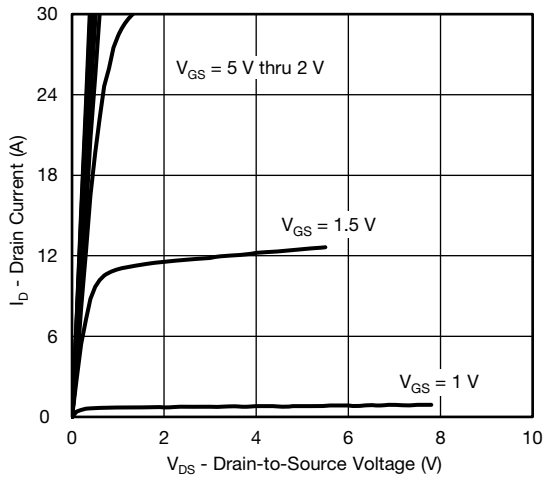
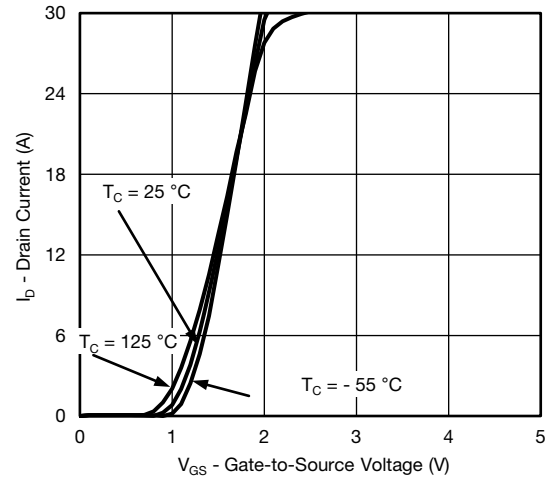
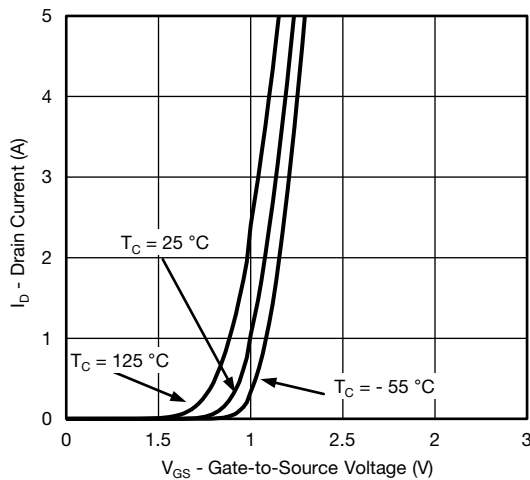
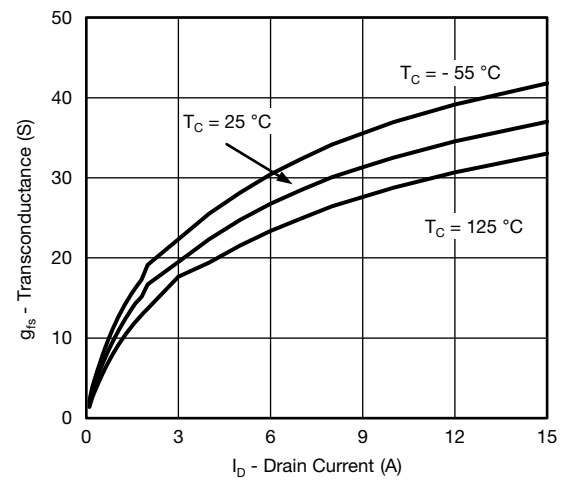
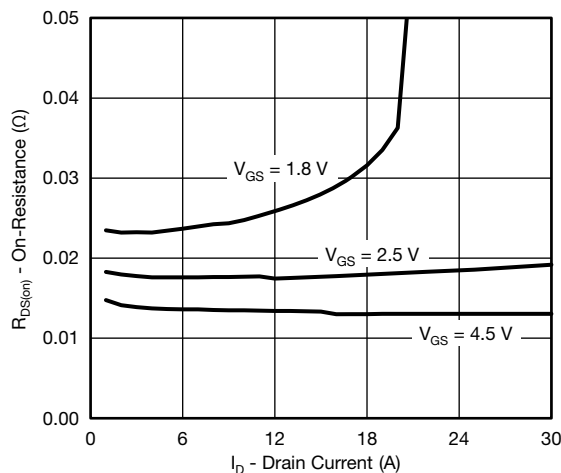
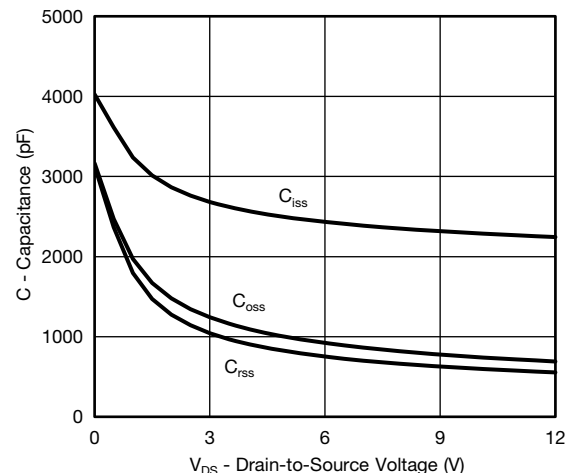


SPECIFICATIONS ($T_C = 25\text{ }^\circ\text{C}$, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static							
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0, I_D = -250\text{ }\mu\text{A}$	-12	-	-	V	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$	-0.45	-0.6	-1		
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8\text{ V}$	-	-	± 100	nA	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{GS} = 0\text{ V}$	$V_{DS} = -12\text{ V}$	-	-	-1	μA
		$V_{GS} = 0\text{ V}$	$V_{DS} = -12\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	-50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = -12\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	-150	
On-State Drain Current ^a	$I_{D(on)}$	$V_{GS} = -4.5\text{ V}$	$V_{DS} \leq -5\text{ V}$	-20	-	A	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = -4.5\text{ V}$	$I_D = -13.5\text{ A}$	-	0.013	0.016	Ω
		$V_{GS} = -4.5\text{ V}$	$I_D = -13.5\text{ A}$	-	-	0.020	
		$V_{GS} = -4.5\text{ V}$	$I_D = -13.5\text{ A}$	-	-	0.022	
		$V_{GS} = -2.5\text{ V}$	$I_D = -12\text{ A}$	-	0.018	0.022	
Forward Transconductance ^b	g_{fs}	$V_{DS} = -6\text{ V}, I_D = -12\text{ A}$		-	34	S	
Dynamic ^b							
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$	$V_{DS} = -6\text{ V}, f = 1\text{ MHz}$	-	2433	3600	μF
Output Capacitance	C_{oss}			-	922	1380	
Reverse Transfer Capacitance	C_{rss}			-	752	1120	
Total Gate Charge ^c	Q_g	$V_{GS} = -4.5\text{ V}$	$V_{DS} = -6\text{ V}, I_D = -10\text{ A}$	-	29	38	nC
Gate-Source Charge ^c	Q_{gs}			-	4.2	-	
Gate-Drain Charge ^c	Q_{gd}			-	8.4	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$		1.3	2.7	4	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = -6\text{ V}, R_L = 0.6\text{ }\Omega$ $I_D \cong -10\text{ A}, V_{GEN} = -4.5\text{ V}, R_g = 1\text{ }\Omega$	-	19	26	ns	
Rise Time ^c	t_r		-	33	44		
Turn-Off Delay Time ^c	$t_{d(off)}$		-	73	97		
Fall Time ^c	t_f		-	30	40		
Source-Drain Diode Ratings and Characteristics ^b							
Pulsed Current ^a	I_{SM}			-	-	-60	A
Forward Voltage	V_{SD}	$I_F = -10\text{ A}, V_{GS} = 0\text{ V}$		-	-0.8	-1.1	V

Notes

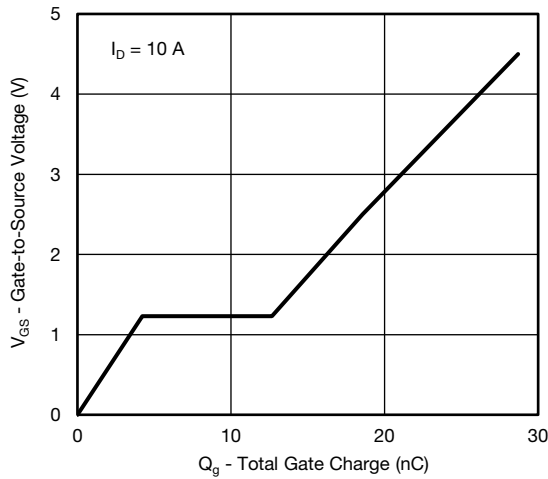
- Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.
- Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

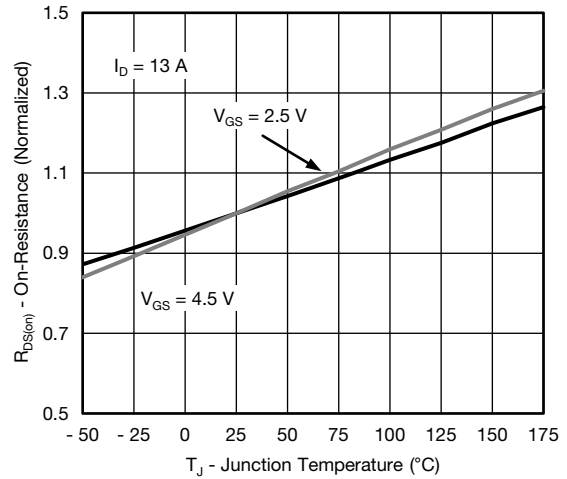
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)

Output Characteristics

Transfer Characteristics

Transfer Characteristics

Transconductance

On-Resistance vs. Drain Current

Capacitance



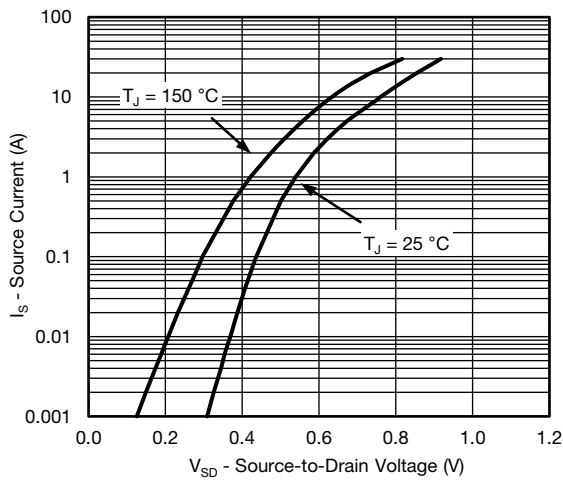
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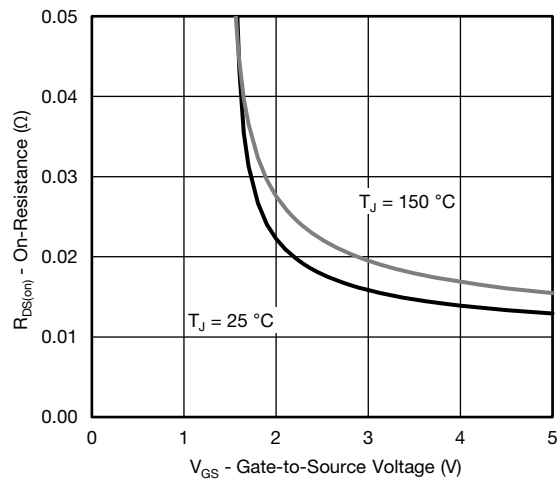
Gate Charge



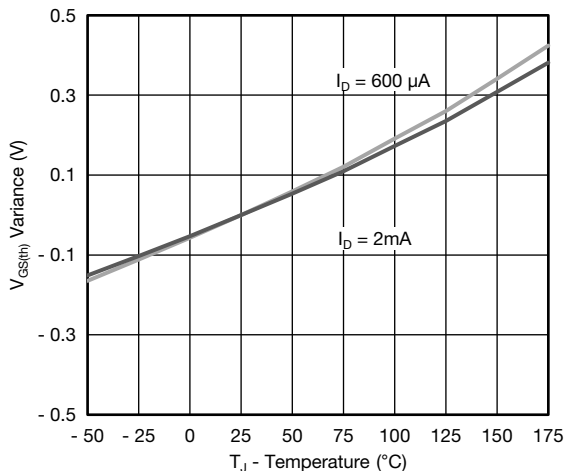
On-Resistance vs. Junction Temperature



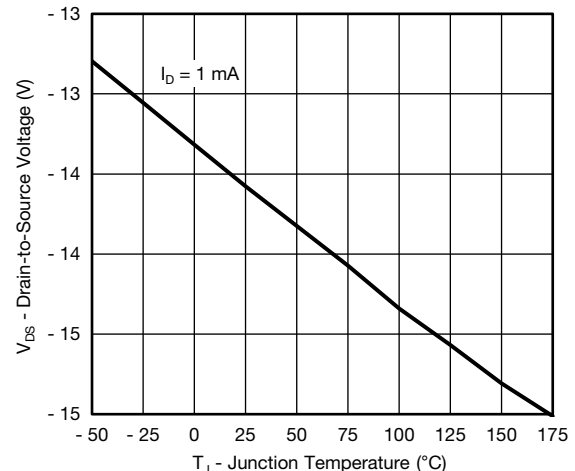
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



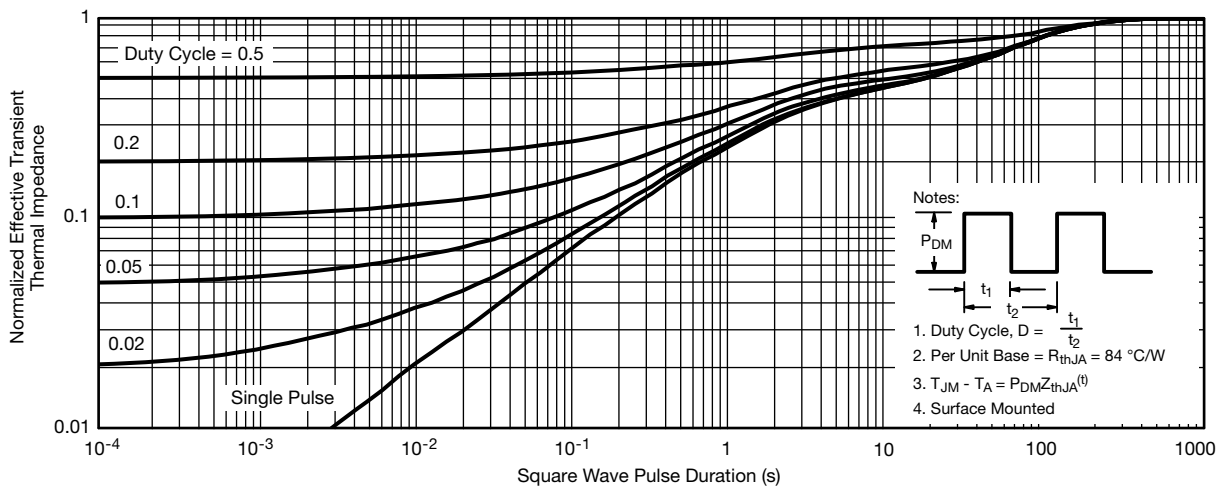
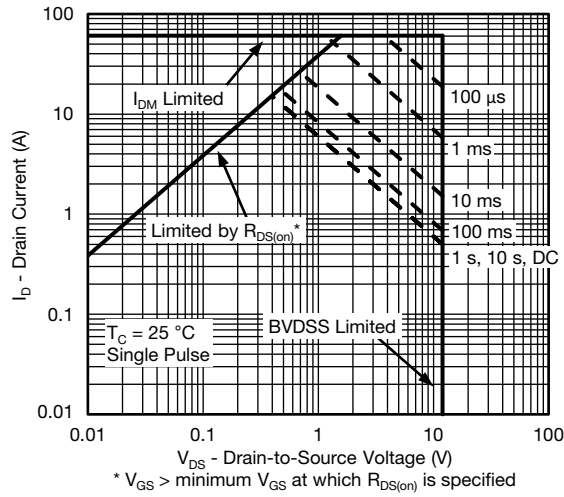
Threshold Voltage



Breakdown Voltage vs. Junction Temperature

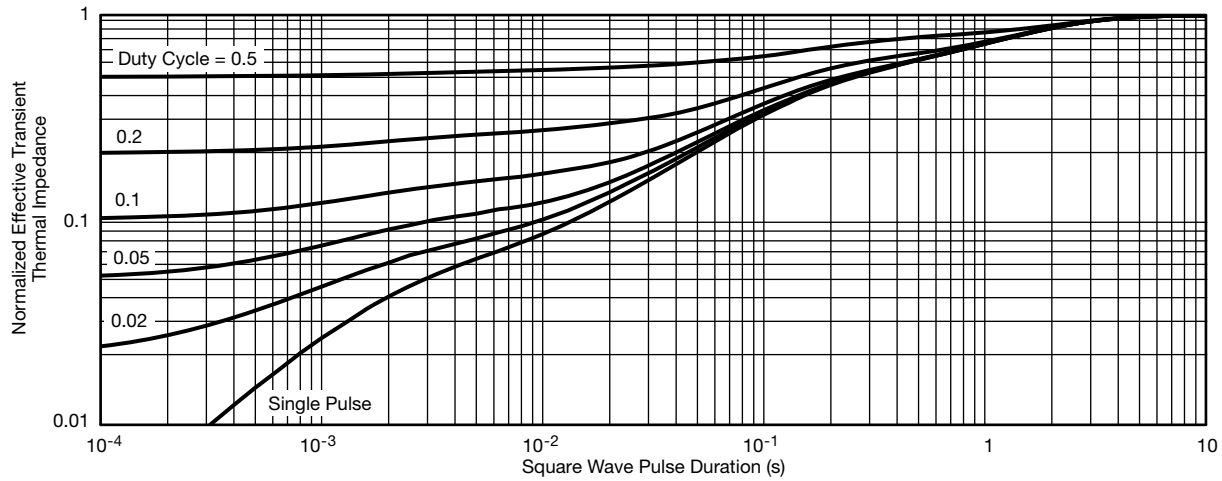


THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)





THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?64454.

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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- Техническая поддержка проекта;
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Как с нами связаться

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