





100V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{(BR)DSS}	Max R _{DS(on)}	Max I _D T _A = 25°C (Note 5)
100V	250mΩ @ $V_{GS} = 10V$	1.9A
	300mΩ @ V _{GS} = 6V	1.68A

Description and Applications

This new generation trench MOSFET from Zetex features a unique structure combining the benefits of low on-resistance and fast switching, making it ideal for high efficiency power management applications.

- DC DC converters
- Power management functions
- Disconnect switches
- Motor control

Features and Benefits

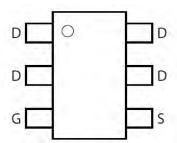
- Low on-resistance
- Fast switching speed
- Totally Lead-Free & Fully RoHS compliant (Note 1)
- Halogen and Antimony Free. "Green" Device (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

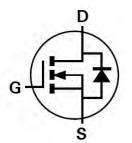
- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.015 grams (approximate)







Pinout Top-view



Device symbol

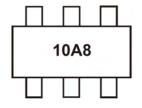
Ordering Information (Note 3)

Part Number	Reel Size (inch)	Tape Width (mm)	Quantity Per Reel
ZXMN10A08E6TA	7	8	3000
ZXMN10A08E6TC	13	8	10,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 3. For packaging details, go to our website at http://www.diodes.com.

Marking Information



10A8 = Product Type Marking Code



Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic			Symbol	Value	Unit V V
Drain-Source voltage Gate-Source voltage		V _{DSS}	100		
		V _{GS}	±20		
Continuous Drain current		Note 5)	l _D	1.9	
	V _{GS} = 10V	T _A =70°C (Note 5)		1.5	٨
	VGS = 10V	(Note 4)		1.5	A
		(Note 7)		3.5	
Pulsed Drain current		(Note 6)	I _{DM}	8.6	Α
Continuous Source Current (Body Diode)		(Note 5)	Is	2.5	Α
Pulsed Source Current (Body Diode) (I		(Note 6)	I _{SM}	8.6	Α

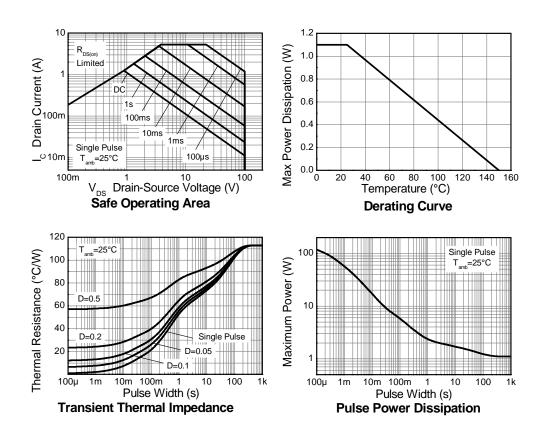
Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit		
	(Note 4)		1.1		
Power Dissipation	(Note 5)	P_{D}	1.7	W	
	(Note 7)		6.3		
The arms of Decistance American to American	(Note 4)		114	°C/W	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{ heta JA}$	73.5		
Thermal Resistance, Junction to Leads	(Note 7)	$R_{ heta JL}$	19.7	°C/W	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

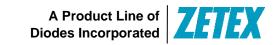
Notes:

- 4. For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- 5. For a device surface mounted on FR4 PCB measured at t ≤ 5 secs.
- 6. Repetitive rating 25mm x 25mm FR4 PCB, D = 0.02, pulse width 300µs pulse width limited by maximum junction temperature.
- 7. Thermal resistance from junction to solder-point (at the end of the drain lead).

Thermal Characteristics







Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$I_D = 250 \mu A, V_{GS} = 0 V$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	0.5	μΑ	$V_{DS} = 100V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS					-		
Gate Threshold Voltage	V _{GS(th)}	2.0	_	4.0	V	$I_D = 250\mu A$, $V_{DS} = V_{GS}$	
Static Drain-Source On-Resistance (Note 8)	D			0.25	Ω	$V_{GS} = 10V, I_D = 3.2A$	
Static Drain-Source On-Resistance (Note 6)	R _{DS} (ON)	_	_	0.30	12	$V_{GS} = 6V, I_D = 2.6A$	
Forward Transconductance (Notes 8 & 10)	g _{fs}	_	5.0	_	S	$V_{DS} = 15V, I_D = 3.2A$	
Diode Forward Voltage (Note 8)	V _{SD}	_	0.87	0.95	V	$I_S = 3.2A, V_{GS} = 0V$	
Reverse recovery time (Note 10)	t _{rr}		27	_	ns		
Reverse recovery charge (Note 10)	Q _{rr}	_	32	_	nC	$I_S = 1.2A$, di/dt = 100A/ μ s	
DYNAMIC CHARACTERISTICS (Note 10)							
Input Capacitance	C _{iss}		405	_	pF	1/ 501/1/ 01/	
Output Capacitance	Coss		28.2	_	pF	$V_{DS} = 50V, V_{GS} = 0V$ - f = 1MHz	
Reverse Transfer Capacitance	C _{rss}		14.2	_	pF		
Gate Charge (Note 9)	Qg		4.2	_	nC	$V_{GS} = 5V, V_{DS} = 50V$ $I_D = 1.2A$	
Total Gate Charge (Note 9)	Qq	_	7.7	_	nC	V _{GS} = 10V, V _{DS} = 50V I _D = 1.2A	
Gate-Source Charge (Note 9)	Qgs	_	1.8	_	nC		
Gate-Drain Charge (Note 9)	Q _{qd}	_	2.1	_	nC		
Turn-On Delay Time (Note 9)	t _{d(on)}		3.4	_	ns		
Turn-On Rise Time (Note 9)	t _r	_	2.2	_	ns	V _{DD} = 30V, V _{GS} = 10V	
Turn-Off Delay Time (Note 9)	t _{d(off)}	_	8	_	ns	$I_D = 1.2A, R_G \cong 6.0\Omega$	
Turn-Off Fall Time (Note 9)	t _f	_	3.2	_	ns		

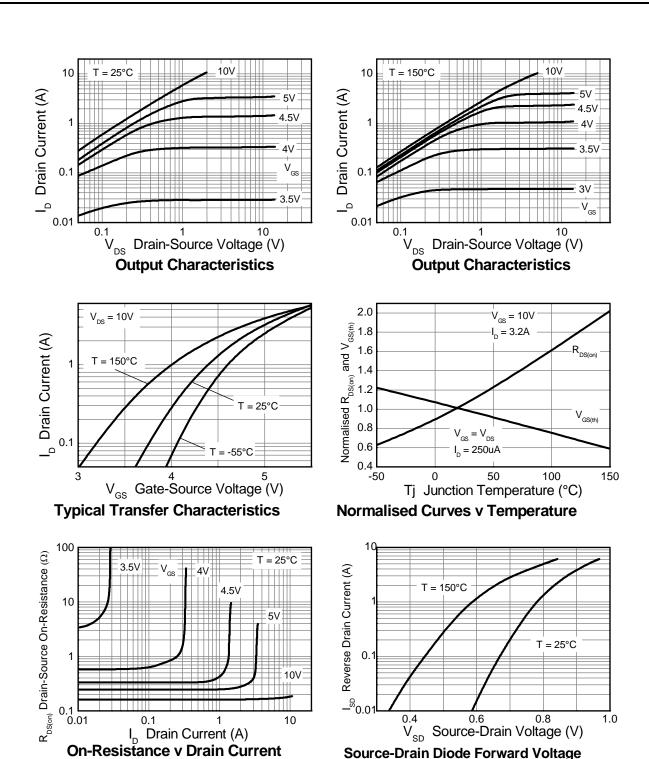
Notes:

- 8. Measured under pulsed conditions. Width 300µs. Duty cycle 2%.9. Switching characteristics are independent of operating junction temperature.10. For design aid only, not subject to production testing.





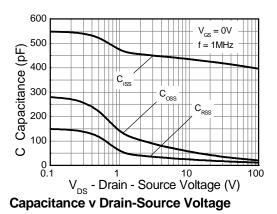
Typical characteristics

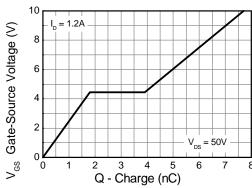






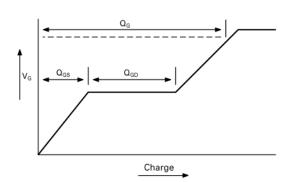
Typical characteristics - Continued





Gate-Source Voltage v Gate Charge

Test Circuits

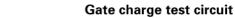


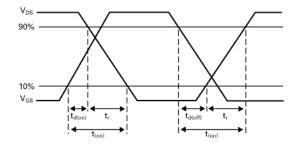
Current regulator

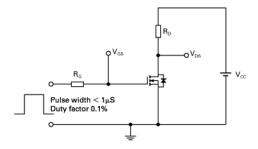
12V 0.2µF 50k Same as D.U.T

V_{os}

Basic gate charge waveform





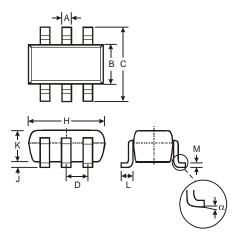


Switching time waveforms

Switching time test circuit

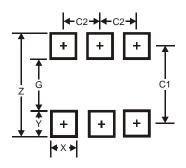


Package Outline Dimensions



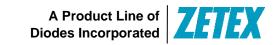
SOT26				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
O	2.70	3.00	2.80	
D	_	_	0.95	
Н	2.90	3.10	3.00	
7	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
M	0.10	0.20	0.15	
α	0°	8°	_	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95





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