

## Features

- Epitaxial Planar Die Construction
- Complementary NPN Type Available (MMBT3904)
- Ideal for Medium Power Amplification and Switching
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP capable (Note 4)

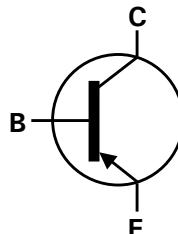
## Mechanical Data

- Case: SOT23
- Case Material: molded plastic, "Green" molding compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.008 grams (approximate)

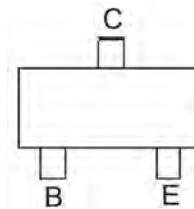
SOT23



Top View



Device Symbol


 Top View  
 Pin-Out

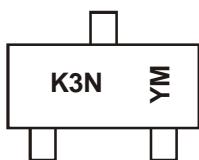
## Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMBT3906-7-F	AEC-Q101	K3N / C3N	7	8	3,000
MMBT3906Q-7-F	Automotive	K3N	7	8	3,000
MMBT3906-13-F	AEC-Q101	K3N / C3N	13	8	10,000

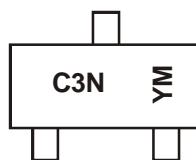
Notes:

- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified.
- For packaging details, go to our website at <http://www.diodes.com>.

## Marking Information



K = SAT (Shanghai Assembly / Test site)  
 3N = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: Y = 2011)  
 M = Month (ex: 9 = September)



C = CAT (Chengdu Assembly / Test site)  
 3N = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: Y = 2011)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2010	2011	2012	2013	2014	2015	2016	2017				
Code	X	Y	Z	A	B	C	D	E				
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

**Maximum Ratings** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	-40	V
Collector-Emitter Voltage	$V_{CEO}$	-40	V
Emitter-Base Voltage	$V_{EBO}$	-6.0	V
Continuous Collector Current	$I_C$	-200	mA

**Thermal Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	$P_D$	310	mW
		350	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	403	°C/W
		357	
Thermal Resistance, Junction to Leads	$R_{\theta JL}$	350	°C/W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	°C

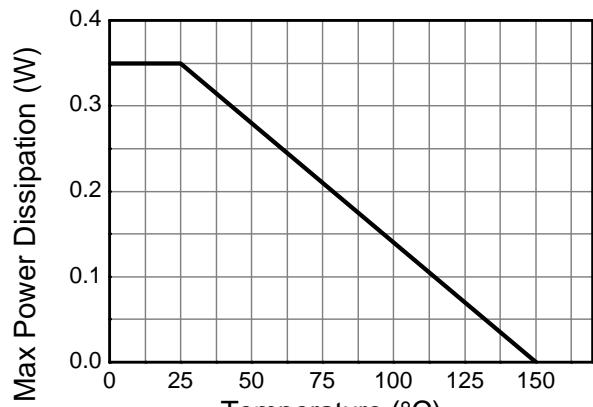
**ESD Ratings** (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	6,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	$\geq 400$	V	C

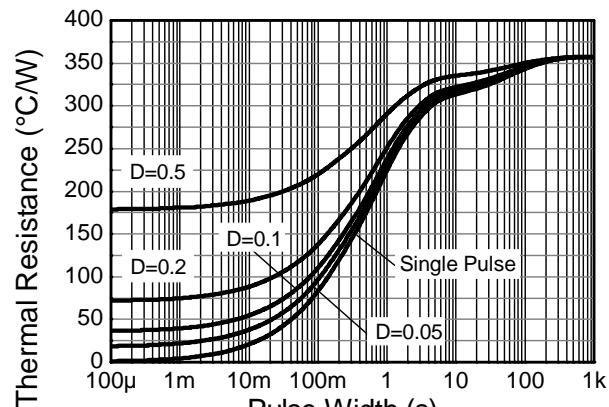
Notes:

6. For the device mounted on minimum recommended pad layout FR4 PCB with high coverage of single sided 1oz copper in still air condition;
7. Same as Note 6, expect the device is mounted on 15mm X 15mm X 1.6mm FR4 PCB
8. Thermal resistance from junction to solder-point (at the end of the leads).
9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

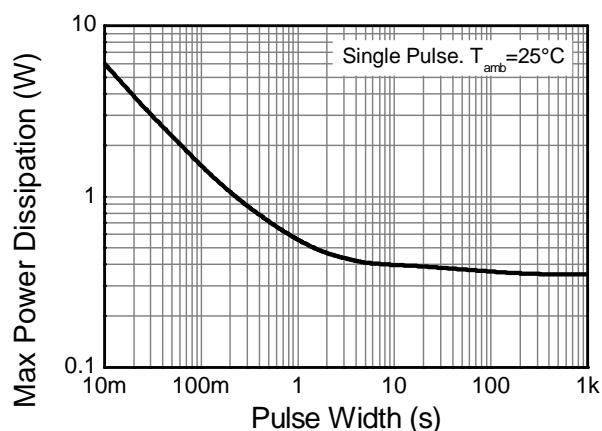
## Thermal Characteristics



**Derating Curve**



**Transient Thermal Impedance**



**Pulse Power Dissipation**

**Electrical Characteristics** (@ $T_A = +25^\circ\text{C}$ , unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS</b>					
Collector-Base Breakdown Voltage	$\text{BV}_{\text{CBO}}$	-40	—	V	$I_C = -100\mu\text{A}, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 10)	$\text{BV}_{\text{CEO}}$	-40	—	V	$I_C = -10\text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	$\text{BV}_{\text{EBO}}$	-6.0	—	V	$I_E = -100\mu\text{A}, I_C = 0$
Collector Cutoff Current	$I_{\text{CEV}}$	—	-50	nA	$V_{\text{CE}} = -30\text{V}, V_{\text{BE}} = -3.0\text{V}$
Emitter-Base Cutoff Current		—	-50	nA	$V_{\text{CE}} = -30\text{V}, V_{\text{BE}} = 0.25\text{V}$
Emitter-Base Cutoff Current	$I_{\text{EBO}}$	—	-50	nA	$V_{\text{EB}} = -5\text{V}$
<b>ON CHARACTERISTICS</b> (Note 10)					
DC Current Gain	$h_{\text{FE}}$	60	—	—	$I_C = -100\mu\text{A}, V_{\text{CE}} = -1.0\text{V}$
		80	—	—	$I_C = -1.0\text{mA}, V_{\text{CE}} = -1.0\text{V}$
		100	300	—	$I_C = -10\text{mA}, V_{\text{CE}} = -1.0\text{V}$
		60	—	—	$I_C = -50\text{mA}, V_{\text{CE}} = -1.0\text{V}$
		30	—	—	$I_C = -100\text{mA}, V_{\text{CE}} = -1.0\text{V}$
Collector-Emitter Saturation Voltage	$V_{\text{CE}(\text{sat})}$	—	-0.25 -0.40	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$ $I_C = -50\text{mA}, I_B = -5.0\text{mA}$
Base-Emitter Saturation Voltage	$V_{\text{BE}(\text{sat})}$	-0.65 —	-0.85 -0.95	V	$I_C = -10\text{mA}, I_B = -1.0\text{mA}$ $I_C = -50\text{mA}, I_B = -5.0\text{mA}$
<b>SMALL SIGNAL CHARACTERISTICS</b>					
Output Capacitance	$C_{\text{obo}}$	—	4.5	pF	$V_{\text{CB}} = -5.0\text{V}, f = 1.0\text{MHz}, I_E = 0$
Input Capacitance	$C_{\text{ibo}}$	—	10	pF	$V_{\text{EB}} = -0.5\text{V}, f = 1.0\text{MHz}, I_C = 0$
Input Impedance	$h_{\text{ie}}$	2.0	12	k $\Omega$	$V_{\text{CE}} = 10\text{V}, I_C = 1.0\text{mA}, f = 1.0\text{kHz}$
Voltage Feedback Ratio	$h_{\text{re}}$	0.1	10	$\times 10^{-4}$	
Small Signal Current Gain	$h_{\text{fe}}$	100	400	—	
Output Admittance	$h_{\text{oe}}$	3.0	60	$\mu\text{S}$	
Current Gain-Bandwidth Product	$f_T$	250	—	MHz	
Noise Figure	NF	—	4.0	dB	$V_{\text{CE}} = -5.0\text{V}, I_C = -100\mu\text{A}, R_S = 1.0\text{k}\Omega, f = 1.0\text{kHz}$
<b>SWITCHING CHARACTERISTICS</b>					
Delay Time	$t_d$	—	35	ns	$V_{\text{CC}} = -3.0\text{V}, I_C = -10\text{mA},$
Rise Time	$t_r$	—	35	ns	$V_{\text{BE}(\text{off})} = 0.5\text{V}, I_{B1} = -1.0\text{mA}$
Storage Time	$t_s$	—	225	ns	$V_{\text{CC}} = -3.0\text{V}, I_C = -10\text{mA},$
Fall Time	$t_f$	—	75	ns	$I_{B1} = I_{B2} = -1.0\text{mA}$

Notes: 10. Measured under pulsed conditions. Pulse width  $\leq 300\mu\text{s}$ . Duty cycle  $\leq 2\%$ .

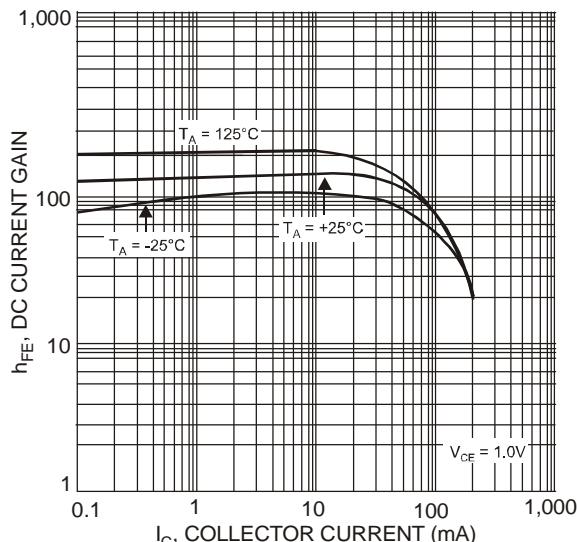


Figure 1 Typical DC Current Gain  
vs. Collector Current

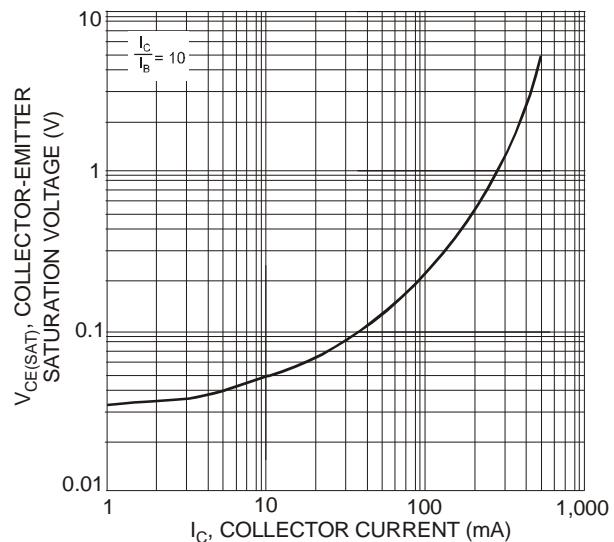


Figure 2 Typical Collector-Emitter Saturation Voltage  
vs. Collector Current

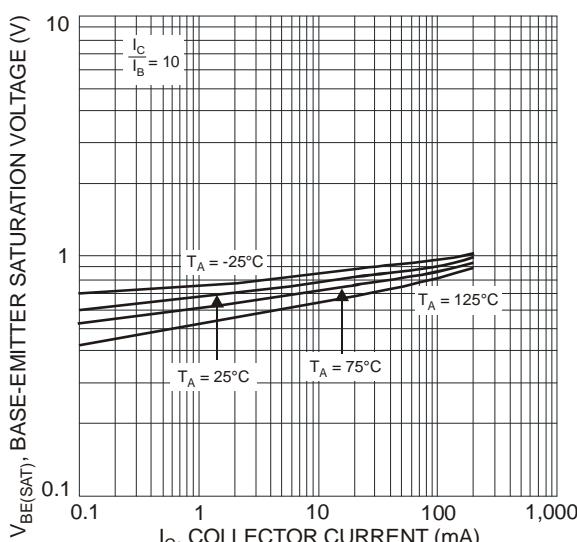


Figure 3 Typical Base-Emitter Saturation Voltage  
vs. Collector Current

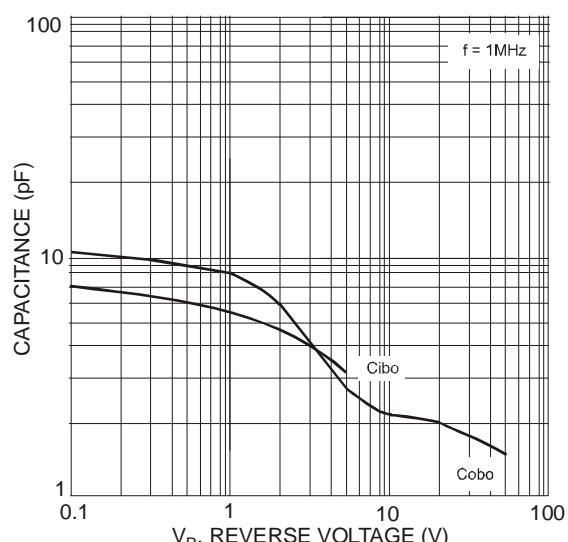
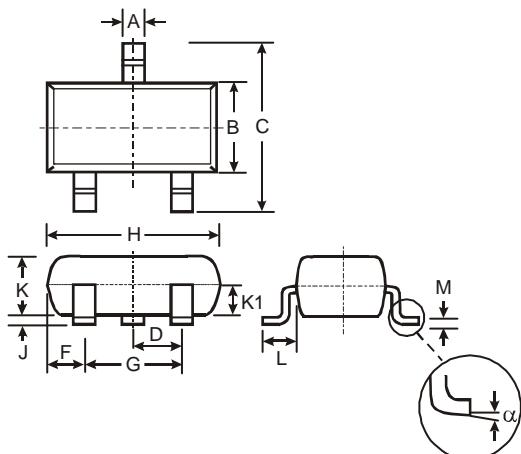


Figure 4 Typical Capacitance Characteristics

## Package Outline Dimensions

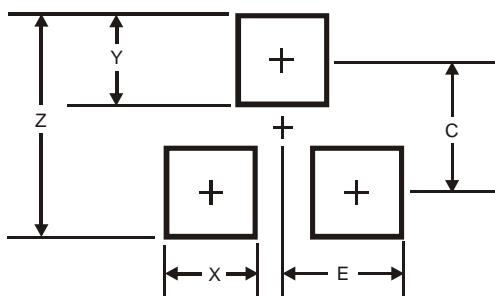
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT23			
Dim	Min	Max	Typ
<b>A</b>	0.37	0.51	0.40
<b>B</b>	1.20	1.40	1.30
<b>C</b>	2.30	2.50	2.40
<b>D</b>	0.89	1.03	0.915
<b>F</b>	0.45	0.60	0.535
<b>G</b>	1.78	2.05	1.83
<b>H</b>	2.80	3.00	2.90
<b>J</b>	0.013	0.10	0.05
<b>K</b>	0.903	1.10	1.00
<b>K1</b>	-	-	0.400
<b>L</b>	0.45	0.61	0.55
<b>M</b>	0.085	0.18	0.11
$\alpha$	0°	8°	-

## **Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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