

Features and Benefits

- Each half-bridge circuit consists of a pre-driver circuit that is completely independent from the others
- 10 to 30 A continuous output IPM in a small-footprint package
- · Protection against simultaneous high- and low-side turning on
- Bootstrap diodes with series resistors for suppressing inrush current are incorporated
- CMOS compatible input (3.3 to 5 V)
- Designed to minimize simultaneous current through both high- and low-side IGBTs by optimizing gate drive resistors
- Integrated Fast Recovery Diode (FRD) as freewheeling diode for each IGBT
- UVLO protection with auto restart
- Thermal shutdown (TSD) with auto restart
- Fault (FO indicator) signal output at protection activation: UVLO (low side only), OCP, STP, and TSD
- Proprietary power DIP package
- UL Recognized Component (File No.: E118037)

Packages: Power DIP



Description

The SCM1240M inverter power module (IPM) devices provide a robust, highly-integrated solution for optimally controlling 3-phase motor power inverter systems and variable speed control systems used in energy-conserving designs to drive motors of residential and commercial appliances. These ICs take 85 to 253 VAC input voltage, and 10 to 30 A (continuous) output current. They can withstand voltages of up to 600 V (IGBT breakdown voltage).

The high current SCM1240M employs a new, small-footprint proprietary DIP package. The IC itself consists of all of the necessary power elements (six IGBTs), pre-drive ICs (three), and freewheeling diodes (six), needed to configure the main circuit of an inverter, as well as a bootstrap circuit (three bootstrap diodes and three boot resistors) as a high-side drive power supply. This enables the main circuit of the inverter to be configured with fewer external components than traditional designs.

Applications include residential white goods (home appliances) and commercial appliance motor control, such as:

- Air conditioner compressor motor
- Air conditioner fan motor
- Refrigerator compressor motor
- Washing machine main motor

Functional Block Diagram



Figure 1. Diagram of one of three phases in the device.

Selection Guide

				ICBT Seturation	Output Current			
Part Number	Package	Packing	Voltage, V _{CES} (min) (V)	Voltage, V _{CE(sat)} (typ) (V)	Continuous, I _o (max) (A)	Pulsed, I _{OP} (max) (A)		
SCM1241M	Fully Molded	10 pieces per tube	600	1.7	10	20		
SCM1243MF	Heatsink Pad	10 pieces per tube	600	1.7	15	30		
SCM1245MF	Heatsink Pad	10 pieces per tube	600	1.7	20	30		
SCM1246MF	Heatsink Pad	10 pieces per tube	600	1.7	30	45		

Absolute Maximum Ratings, valid at $T_A = 25^{\circ}C$

Characteristic	Symbol	Remarks		Rating	Units
Supply Voltage	V _{DC}	Between VBB an	d LS1, LS2, and LS3	450	V
Supply Voltage (Surge)	V _{DC(surge)}	Between VBB an	d LS1, LS2, and LS3	500	V
IGBT Breakdown Voltage	V _{CES}	V_{CC} = 15 V, I _C = -	1 mA, V _{IN} = 0 V	600	V
Logic Supply Voltage	V _{CC}	Between VCC ar	nd COM	20	V
Boot-strap Voltage	V _{BS}	Between VB and	HS (U,V,W)	20	V
		SCM1241M	T _{Case} = 25°C	10	Adc
Output Current Continuous		SCM1243MF	T _{Case} = 25°C	15	Adc
	10	SCM1245MF	T _{Case} = 25°C	20	Adc
		SCM1246MF	T _{Case} = 25°C	30	Adc
		SCM1241M	Pulse Width ≤1 ms	20	Α
Output Current Duland		SCM1243MF	Pulse Width ≤1 ms	30	Α
Output Current, Puised	IOP	SCM1245MF	Pulse Width ≤1 ms	30	Α
		SCM1246MF	Pulse Width ≤1 ms	45	А
Input Voltage	V _{IN}	HIN, LIN, and OC	HIN, LIN, and OCP		V
FO Terminal Voltage	V _{FO}	Between FO and	1 COM	7	V
		SCM1241M		6.0	°C/W
Thermal Desistance, Junction to Coop	R _{(j-c)Q}	SCM1243MF SCM1245MF SCM1246MF	1 element operation (IGBT)	3.0	°C/W
Thermal Resistance, Junction-to-Case		SCM1241M		6.5	°C/W
	R _{(j-c)F}	SCM1243MF SCM1245MF SCM1246M	1 element operation (FRD)	4.0	°C/W
Case Operating Temperature	T _{OPC}			-20 to 100	°C
Junction Temperature (IGBT)	TJ			150	°C
Storage Temperature	T _{stg}			-40 to 150	°C
Isolation Voltage	V _{iso}	Between expose	d thermal pad and each pin; 1 minute, ac	2000	V _{rms}

All performance characteristics given are typical values for circuit or system baseline design only and are at the nominal operating voltage and an ambient temperature, T_A , of 25°C, unless otherwise stated.

Recommended Operating Conditions

Characteristic	Symbol		Remarks	Min.	Тур.	Max.	Units
Main Supply Voltage	V _{DC}	Between VBB and LS		-	300	400	V
Logic Supply Voltage	V _{CC}	Between VCC	and COM	13.5	_	16.5	V
Logic Supply Voltage	V _{BS}	Between VB a	nd HS	13.5	-	16.5	V
Minimum Input Duloo Width	t _{INmin(on)}	On pulse		0.5	-	_	μs
	t _{INmin(off)}	Off pulse		0.5	-	-	μs
Dead Time*	t _{dead}			1.0	_	-	μs
FO Pull-up Resistor	R _{FO}				_	22	kΩ
FO Pull-up Voltage	V _{FO}			3.0	-	5.5	V
Bootstrap Capacitor	C _{BOOT}			10	-	220	μF
		SCM1241M	For I _P ≤ 20 A	27	_	_	mΩ
Shunt Resistor	Rs	SCM1243MF SCM1245MF SCM1246MF	For I _P ≤ 30 A	15	_	_	mΩ
RC Filter Resistor	Ro			-	-	100	Ω
RC Filter Capacitor	Co			-	-	2200	pF
PWM Carrier Frequency f _C				-	_	20	kHz
Junction Temperature	TJ			-	_	125	°C

*Dead Time must be controlled from an external source.



Typical Application Diagram

Typical application utilizing one shunt resistor, RS

See application notes on next page.

Notes for Typical Application Diagram

- To use the OCP circuit shown in the Typical Application drawing, an external shunt resistor, RS, is needed. The RS value depends on how many low-side IGBTs turn on simultaneously.
- To avoid malfunction, the wiring between the LS and COM pins should be as short as possible.
- To prevent surge destruction, put a 0.01 to 1 µF snubber capacitor, CS, in parallel with the electrolytic capacitor.
- To prevent surge destruction, put a 18 to 20 V Zener diode between the VCC and COM pins.
- To prevent surge malfunction, put a 0.01 to 0.1 µF ceramic capacitor between the VCC and COM pins and the VB and HS pins.
- To add an external low pass filter, place RO and CO between the A to D pin of the application controller and shunt resistor RS, ensuring that the time constant of this low pass filter is less than 0.2 µs. The internal OCP circuit has a 1.5 µs (min) filtering delay.

However, the low pass filter causes an additional delay to detecting an overcurrent condition of the SCM1240M, and if the total delay exceeds 1.7 μ s, permanent damage to the SCM1240M may result.

- To suppress noise, connect a resistor, RF, and capacitor, CF, to the \overline{FOx} pins. Also, when an overcurrent condition is detected, the \overline{FOx} pin goes low for 25 µs (typ) as an active low fault flag, and then returns up to its normal state. It is recommended to connect the \overline{FOx} pin directly to an interrupt pin of the external controller, to ensure the 25 µs interval is detected.
- To provide the best thermal shutdown protection (TSD), it is strongly recommended that the three FOx pins be tied together. In this way an overtemperature event (internal temperature rise above 150°C) in any of the three phases will cause all three phases to shut down. If the FOx pins are not tied together, but are independently pulled up to +5 V, the TSD function for each phase shuts down only that phase.

ELECTRICAL CHARACTERISTICS, valid at T_A=25°C, unless otherwise noted

Characteristics	Symbol	Conditions	Min	Тур	Max	Units
Logic Supply Voltage	V _{CC}	Between VCC and COM	13.5	-	16.5	V
Logic Supply Current	I _{CC}	V_{CC} = 15 V, 3 phases operating	_	3	-	mA
Bootstrap Supply Current	I _{BS}	VB – HS = 15 V, one phase operating	-	140	-	μA
Input Voltago	VIH	V_{CC} = 15 V, output on	-	2.0	2.5	V
	V _{IL}	V_{CC} = 15 V, output off	1.0	1.5	-	V
Input Voltage Hysteresis	V _{lhys}	V _{CC} = 15 V	-	0.5	-	V
Input Current	I _{IHH}	V_{CC} = 15 V, V_{IN} = 5 V	-	230	500	μA
	I _{ILH}	V _{CC} = 15 V, V _{IN} = 0 V	-	-	2	μA
Lindervoltage Lock Out (High Side)	U_{VHL}	$V_{ee} = 15 V$	10.0	11.0	12.0	V
	U_{VHH}		10.5	11.5	12.5	V
Lindervoltage Lock Out (Low Side)	U _{VLL}	$V_{ee} = 15 V$	10.0	11.0	12.0	V
	U _{VLH}		10.5	11.5	12.5	V
	V _{FOL}	$V_{ro} = 15 V V_{ro} = 5 V B_{ro} = 10 kO$	_	-	0.5	V
	V _{FOH}			_	_	V
Overcurrent Protection Trip Voltage	V _{TRIP}	V _{CC} = 15 V	0.46	0.50	0.54	V
Overcurrent Protection Hold Time	t _P	V _{CC} = 15 V	20	25	-	μs
Overtemperature Protection Activation	T _{DH}	V _{CC} = 15 V		150	165	°C
and Releasing Temperature	T _{DL}			120	135	°C
Blanking Time	t _{blank}	V _{CC} = 15 V	-	1.65	-	μs
IGBT Breakdown Voltage	V _{CES}	V _{CC} = 15 V, I _C = 1 mA, V _{IN} = 0 V	600	-	-	V
IGBT Leakage Current	I _{CES}	$V_{CC} = 15 \text{ V}, V_{CE} = 600 \text{ V}, V_{IN} = 0 \text{ V}$	-	_	1	mA
		SCM1241M V _{CC} = 15 V, I _C = 10 A, V _{IN} = 5 V	_	1.7	2.2	V
IGBT Saturation Voltage	Varia	SCM1243MF V _{CC} = 15 V, I _C = 15 A, V _{IN} = 5 V	_	1.7	2.2	V
	CE(sat)	SCM1245MF V _{CC} = 15 V, I _C = 20 A, V _{IN} = 5 V	_	1.7	2.2	V
		SCM1246MF V _{CC} = 15 V, I _C = 30 A, V _{IN} = 5 V	_	1.7	2.2	V
		SCM1241M V _{CC} = 15 V, I _F = 10 A, V _{IN} = 0 V	_	1.9	2.4	V
Diode Forward Voltage	Ve	SCM1243MF V _{CC} = 15 V, I _F = 15 A, V _{IN} = 0 V	-	1.75	2.2	V
Diode i orward voltage	٧F	SCM1245MF V _{CC} = 15 V, I _F = 20 A, V _{IN} = 0 V	-	1.9	2.4	V
		SCM1246MF V _{CC} = 15 V, I _F = 30 A, V _{IN} = 0 V	-	1.5	2.0	V
Diode Recovery Time (Bootstrap)	t _{rr}	I _F = I _{RECOVERY(PEAK)} = 100 mA		70	-	ns
Diode Leakage Current (Boot Strap)	I _{IB}	V _R = 600 V	-	-	10	μA
Diode Forward Voltage (Boot Strap)	V_{FB}	I _F = 0.15 A	-	1.1	1.3	V
Diode Series Resistor (Boot Strap)	R _B			22.0	26.4	Ω

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ELECTRICAL CHARACTERISTICS, valid at T_A=25°C, unless otherwise noted

Characteristics	Symbol		Conditions			Max	Units
	t _{dH(on)}			-	590	-	ns
	t _{rH}			-	80	-	ns
	t _{rrH}	SCM1241M	$V_{DC} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_C = 10 \text{ A}, \text{ inductive}$	-	60	-	ns
	t _{dH(off)}			-	550	-	ns
	t _{fH}			-	90	-	ns
	t _{dH(on)}			-	600	-	ns
	t _{rH}			-	70	-	ns
	t _{rrH}	SCM1243MF	V_{DC} = 300 V, V_{CC} = 15 V, I _C = 15 A, inductive load; HIN = 0 \rightarrow 5 V or 5 \rightarrow 0 V	-	70	-	ns
	t _{dH(off)}			-	620	-	ns
High Sido Switching Time	t _{fH}			-	60	-	ns
	t _{dH(on)}	SCM1245MF	V_{DC} = 300 V, V_{CC} = 15 V, I_C = 20 A, inductive load; HIN = 0 \rightarrow 5 V or 5 \rightarrow 0 V	-	695	-	ns
	t _{rH}			-	95	-	ns
	t _{rrH}			-	75	-	ns
	t _{dH(off)}			-	675	-	ns
	t _{fH}			-	55	-	ns
	t _{dH(on)}				660	-	ns
	t _{rH}			-	110	-	ns
	t _{rrH}	SCM1246MF	$V_{DC} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_C = 30 \text{ A}, \text{ inductive}$ load: HIN = 0 \rightarrow 5 V or 5 \rightarrow 0 V	-	60	-	ns
	t _{dH(off)}]	$\log u$, $1 \lim v = 0 \rightarrow 3 \ v \ 01 \ 3 \rightarrow 0 \ v$	-	700	-	ns
	t _{fH}				50	-	ns

Continued on the next page...

ELECTRICAL CHARACTERISTICS, valid at $T_A=25^{\circ}C$, unless otherwise noted

Characteristics	Symbol		Conditions			Max	Units
	t _{dL(on)}			-	570	-	ns
	t _{rL}			-	100	-	ns
	t _{rrL}	SCM1241M	$V_{DC} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_C = 10 \text{ A}, \text{ inductive}$	-	70	-	ns
	t _{dL(off)}			-	560	-	ns
	t _{fL}			-	100	-	ns
	t _{dL(on)}			-	600	-	ns
	t _{rL}			-	100	-	ns
	t _{rrL}	SCM1243MF	V_{DC} = 300 V, V_{CC} = 15 V, I _C = 15 A, inductive load; LIN = 0 \rightarrow 5 V or 5 \rightarrow 0 V	_	80	-	ns
	t _{dL(off)}			-	600	-	ns
Low Cide Cwitching Time	t _{fL}			-	70	-	ns
Low Side Switching Time	t _{dL(on)}	SCM1245MF	V_{DC} = 300 V, V_{CC} = 15 V, I_C = 20 A, inductive load; LIN = 0 \rightarrow 5 V or 5 \rightarrow 0 V	-	715	-	ns
	t _{rL}			-	135	-	ns
	t _{rrL}			-	115	-	ns
	t _{dL(off)}			-	670	-	ns
	t _{fL}			-	50	-	ns
	t _{dL(on)}			-	660	-	ns
	t _{rL}]		-	150	-	ns
	t _{rrL}	SCM1246MF	$V_{DC} = 300 \text{ V}, V_{CC} = 15 \text{ V}, I_C = 30 \text{ A}, \text{ inductive}$	_	70	_	ns
	t _{dL(off)}			-	690	-	ns
	t _{fL}			_	50	-	ns



Switching Timing Definitions

Mode	HINx	LINx	High-side MOSFET	Low-side MOSFET
	L	L	Off	Off
Nerro el1	Н	L	On	Off
Normal	L	Н	Off	On
	Н	Н	Off	Off
	L	L	Off	Off
TOD	Н	L	Off	Off
150	L	Н	Off	Off
	Н	н	Off	Off
	L	L	Off	Off
000	Н	L	Off	Off
OCP	L	Н	Off	Off
	Н	Н	Off	Off
	L	L	Off	Off
	Н	L	Off	Off
VCCX UVLO ²	L	Н	Off	Off
	Н	Н	Off	Off
	L	L	Off	Off
	Н	L	Off	Off
VBX UVLU ³	L	Н	Off	On
	Н	Н	Off	Off
	L	L	Off	Off
FO x Low	н	L	Off	Off
FUXLOW	L	Н	Off	Off
	Н	н	Off	Off

Input-Output Truth Table

¹In the case where a pair of HINx and LINx signals are asserted at the same time, the shoot-through prevention feature sets both the high-side and the low-side IGBTs off.

²After the VCCx power rail recovers from a UVLO condition, a rising edge of HINx starts driving the highside IGBT (edge trigger). On the other hand, after the UVLO condition is released, the input level of the LINx pins reflects the state of the low-side IGBTs (level trigger).

³After the VBx power rail recovers from a UVLO condition, a rising edge of HINx starts driving the highside IGBT (edge trigger).

(Bottom View)

Terminal List Table

Name	Number	Function
1	FO1	U phase fault output for overcurrent and UVLO detected
2	OCP1	Input for U phase overcurrent protection
3	LIN1	Signal input for low-side U phase (active high)
4	COM1	Supply ground for U phase IC
5	HIN1	Signal input for high-side U phase (active high)
6	VCC1	Supply voltage for U phase IC
7	VB1	High-side floating supply voltage for U phase
8	HS1	High-side floating supply ground for U phase
9	FO2	V phase fault output for overcurrent and UVLO detected
10	OCP2	Input for V phase overcurrent protection
11	LIN2	Signal input for low-side V phase (active high)
12	COM2	Supply ground for V phase IC
13	HIN2	Signal input for high-side V phase (active high)
14	VCC2	Supply voltage for V phase IC
15	VB2	High-side floating supply voltage for V phase
16	HS2	High-side floating supply ground for V phase
17	FO3	W phase fault output for overcurrent and UVLO detected
18	OCP3	Input for W phase overcurrent protection
19	LIN3	Signal input for low-side W phase (active high)
20	COM3	Supply ground for W phase IC
21	HIN3	Signal input for high-side W phase (active high)
22	VCC3	Supply voltage for W phase IC
23	VB3	High-side floating supply voltage for W phase
24	HS3	High-side floating supply ground for W phase
25	VBB	Positive DC bus supply voltage
26	W	Output for W phase
27	LS3	Negative DC bus supply ground for W phase
28	VBB	(Pin trimmed) positive DC bus supply voltage
29	V	Output for V phase
30	LS2	Negative DC bus supply ground for V phase
31	VBB	(Pin trimmed) positive DC bus supply voltage
32	U	Output for U phase
33	LS1	Negative DC bus supply ground for U phase

SCM1240M

High-Side Driver Input/Output and UVLO Protection

High Voltage, High Current 3-Phase Motor Drivers

Low-Side Driver Input/Output and UVLO Protection

V_{CC} = 15 V VB – HS = 15 V HIN ни LIN LIN UVHH UVLH UVHL UVLL VB-HS vcc но но LO FO *No FO output at H-side UVLO FO *HO starts from positive edge after UVLO release *LO starts after UVLO release **Shoot-Through Prevention Thermal Shutdown** V_{CC} , VB - HS = 15 VHIN HIN-LIN LIN TDH Tmic VCC VB HO но LO FO FO *Tmic is the temperature of the predriver IC (MIC) * While both HIN and LIN are in high state HO and LO turn off and FO signals out. *HO and LO start after TSD release

Timing Diagrams

(one phase of three phases)

Overcurrent Protection



PACKAGE OUTLINE DRAWING



Device composition complies with the RoHS directive.

SANKEN ELECTRIC CO., LTD.



Device composition complies with the RoHS directive.

SANKEN ELECTRIC CO., LTD.



Device composition complies with the RoHS directive.

SANKEN ELECTRIC CO., LTD.

MECHANICAL CHARACTERISTICS

Characteristic	Remarks	Min.	Тур.	Max.	Units
Hostoink Mounting Sorow Torque	Lies and M2 agrow each and	58.8	-	78.4	N•cm
Heatsink Mounting Screw Torque	Use one was screw each end	6.0	Ι	8.0	kgf•cm
Flatness of Heatsink Attachment Area	Refer to figure below	0	Ι	200	μm
Paakaga Waight	SCM1241M	Ι	10.8	Ι	g
	SCM124xMF	_	11.8	_	g





SCM1240M

High Voltage, High Current 3-Phase Motor Drivers





WARNING — These devices are designed to be operated at lethal voltages and energy levels. Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

The use of an isolation transformer is recommended during circuit development and breadboarding.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5°C to 35°C) and the standard relative humidity (around 40 to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of product that has been stored for a long time.

Cautions for Testing and Handling

- When tests are carried out during inspection testing and other standard test periods, protect the product from power surges from the testing device, shorts between adjacent product packages, and shorts to the heatsink.
- The screwing torque for attaching a heatsink shall be 58.8 to 78.4 N•cm (6.0 to 8.0 Kgf•cm).
- Ensure there are no foreign objects between the heatsink and thermal pad; only silicone thermal grease is allowed.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting this product with a heatsink, grease shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce stress.
- Volatile-type silicone greases may permeate the product and produce cracks after long periods of time, resulting in reduced heat radiation effect, and possibly shortening the lifetime of the product.
- Hard silicone greases may cause cracks in the product when screwing the product to a heatsink.

 Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Туре	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	Momentive Performance Materials Holding, Inc.
SC102	Dow Corning Toray Silicone Co., Ltd.

Soldering

 When soldering the product, please be sure to minimize the working time, within the following limits: 260±5°C 10 s

380±5°C 5 s

 Soldering iron should be at a distance of at least 1.5 mm from the body of the product

Electrostatic Discharge

- When handling the product, operator must be grounded. Grounded wrist straps worn should have at least 1 MΩ of resistance to ground to prevent shock hazard.
- Workbenches where the product is handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the product, the head of soldering irons or the solder bath must be grounded in other to prevent leak voltages generated by them from being applied to the product.
- The product should always be stored and transported in our shipping containers or conductive containers, or be wrapped in aluminum foil.

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- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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