

SLTS286F-MAY 2007-REVISED FEBRUARY 2010

20-A, 4.75-V to 14-V INPUT, NON-ISOLATED, WIDE-OUTPUT, DIGITAL POWERTRAIN™ MODULE

Check for Samples: PTD08A020W

FEATURES

- Up to 20-A Output Current
- 4.75-V to 14-V Input Voltage
- **Programmable Wide-Output Voltage** (0.7 V to 3.6 V)
- Efficiencies up to 96%
- **Digital I/O**
 - PWM signal
 - INHIBIT
 - Current limit flag (FAULT)
 - Sychronous Rectifier Enable (SRE)
- Analog I/O
 - Temperature
 - Output currrent
- Safety Agency Approvals: (Pending)
 - UL/IEC/CSA-C22.2 60950-1
- Operating Temperature: -40°C to 85°C

DESCRIPTION

APPLICATIONS

Digital Power Systems using UCD92XX Digital Controllers



The PTD08A020W is a high-performance 20-A rated, non-isolated digital PowerTrain module. This module is the power conversion section of a digital power system which incorporates TI's UCD7230 MOSFET driver IC. The PTD08A020W must be used in conjunction with a digital power controller such as the UCD9240 or UCD9110 family. The PTD08A020W receives control signals from the digital controller and provides parametric and status information back to the digital controller. Together, PowerTrain modules and a digital power controller form a sophisticated, robust, and easily configured power management solution.

Operating from an input voltage range of 4.75 V to 14 V, the PTD08A020W provides step-down power conversion to a wide range of output voltages from, 0.7 V to 3.6 V. The wide input voltage range makes the PTD08A020W particularly suitable for advanced computing and server applications that utilize a loosely regulated 8-V, 9.6-V or 12-V intermediate distribution bus. Additionally, the wide input voltage range increases design flexibility by supporting operation with tightly regulated 5-V or 12-V intermediate bus architectures.

The module incorporates output over-current and temperature monitoring which protects against most load faults. Output current and module temperature signals are provided for the digital controller to permit user defined over-current and over-temperature warning and fault scerarios.

The module uses double-sided surface mount construction to provide a low profile and compact footprint. Package options include both through-hole and surface mount configurations that are lead (Pb) - free and RoHS compatible.



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PTD08A020W



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This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

Standard PTD08A020W Application





PTD08A020W

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ORDERING INFORMATION

For the most current package and ordering information, see the Package Option Addendum at the end of this datasheet, or see the TI website at www.ti.com.

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ENVIRONMENTAL AND ABSOLUTE MAXIMUM RATINGS

(Voltages are with respect to GND)

					UNIT
VI	Input voltage			16	V
VB	Bias voltage			16	V
T _A	Operating temperature range	Over V _I range		-40 to 85	
T _{wave}	Wave soldering temperature	Surface temperature of module body or pins for 5 seconds maximum.	260	°C	
T _{stg}	Storage temperature	Storage temperature of module removed from shipp	-55 to 125		
T _{pkg}	Packaging temperature	Shipping Tray or Tape and Reel storage or bake ter	45		
	Mechanical shock	Per Mil-STD-883D, Method 2002.3, 1 msec, 1/2 sine, mounted	suffix AD	250	G
	Mechanical vibration	Mil-STD-883D, Method 2007.2, 20-2000 Hz		15	
	Weight			7.4	grams
MTBF	Reliability	Per Telcordia SR-332, 50% stress, T _A = 40°C, grou	nd benign	9.26	10 ⁶ Hr
	Flammability	Meets UL94V-O			



ELECTRICAL CHARACTERISTICS

PTD08A020W

 T_A = 25°C, F_{SW} = 350kHz, V_I = 12 V, V_O = 3.3 V, V_B = V_I , C_{I1} = 330 µF, C_{I2} = 22 µF ceramic, C_{O1} = 47 µF ceramic, C_{O2} = 330 µF, and I_O = $I_{O(max)}$ (unless otherwise stated)

	PARAMETER		TEST CONDITIONS				W	UNIT
					MIN	ТҮР	MAX	
lo	Output current	Over V _O range	0		20	А		
VI	Input voltage range	Over I _O range			4.75		14 ⁽¹⁾	V
V _{OADJ}	Output voltage adjust range	Over I _O range			0.7 ⁽¹⁾		3.6	V
				V _O = 3.3 V		91%		
				$V_{0} = 2.5 V$		89%		
22	Efficiency	I _O = 20 A,		V _O = 1.8 V		86%		
η	Enciency	<i>f</i> s = 350 kHz		V _O = 1.5 V		84%		
				V ₀ = 1.2 V		83%		
				V _O = 1.0 V		80%		
V _{OPP}	V _O Ripple (peak-to-peak)	20-MHz bandwid	th			20		mV_{PP}
VB	Bias voltage				4.75		14	V
VB	Bias voltage under voltage			V _B increasing	4.25	4.5	4.75	V
UVLO	lockout			V _B decreasing	4.0	4.25	4.5	V
	Diana	Inhibit (pin 8) to A	AGND	Standby		5		
IB	Bias current			Switching		42		mA
VIH	High-level input voltage			2.0		5.5	V	
V _{IL}	Low-level input voltage	SRE, INH, & PWM input levels					0.8	v
	DW/M instat	Frequency range	300		500	kHz		
	PWM input	Pulse width limits	130			ns		
		Range	-40		125	°C		
		Accuracy, -40°C	-4.0		+6.0	°C		
	TEMP output	Slope				10		mV/°C
		Offset, $T_A = 0^{\circ}C$		500		mV		
V _{OH}		High-level output	voltage, I _{FAULT} = 4mA		2.7	3.3		V
V _{OL}	FAULT output	Low-level output	voltage, I _{FAULT} = 4mA			0	0.6	v
I _{LIM}		Overcurrent three	Overcurrent threshold; Reset, followed by auto-recovery					А
		Range			0.15		3.5	V
		Gain			46	66	86	mV/A
	IOUT output	Offset			0.44	0.6	0.76	V
		Output Impedance	e		10	15	20	kΩ
C	External input consoltance		Nonceramic			330 (2)		_
CI	External input capacitance			Ceramic		22 (2)		μF
				Nonceramic		330 ⁽³⁾	10,000 ⁽⁴⁾	
Co	External output capacitance	Capacitance Valu	Capacitance Value			47 ⁽³⁾	(3)	μF
		Equivalent series resistance (non-ceramic)						mΩ

(1) The maximum input voltage is duty cycle limited to ($V_O/(130$ ns × F_{SW})) or 14 V, whichever is less. The maximum allowable input voltage is a function of switching frequency.

(2) A 22 µF ceramic input capacitor is recommended for operation. An additional 330 µF bulk capacitor rated for a minimum of 500mA rms of ripple current is recommended. Refer to the UCD9240 controller datasheet and user interface for application specific capacitor specifications.

(3) A 47 µF ceramic output capacitor is recommended for operation. An additional 330 µF bulk capacitor is recommended for improved transient response. See the related Application Information section for further guidance. Refer to the UCD9240 controller datasheet and user interface for application specific capacitor specifications.

(4) 10,000 μF is the calculated maximum output capacitance given a 1V/msec output voltage rise time. Additional capacitance or increasing the output voltage rise rate may trigger the overcurrent threshold at start-up.

(5) This is the minimum ESR for all non-ceramic output capacitance. Refer to the UCD9240 controller datasheet and user interface for application specific capacitor specifications.

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EXAS

INSTRUMENTS

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TERMINAL FUNCTIONS	
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TERMINAL		DESCRIPTION					
NAME	NO.	DESCRIPTION					
VI	1	The positive input voltage power node to the module, which is referenced to common GND.					
GND	2	This is the common ground connection for the V and V power connections					
GND	3	This is the common ground connection for the V_I and V_O power connections.					
Vo	4	The regulated positive power output with respect to GND.					
TEMP	5	Temperature sense output. The voltage level on this pin represents the temperature of the module.					
IOUT	6	Current sense output. The voltage level on this pin represents the average output current of the module.					
AGND	7	Analog ground return. It is the 0 V_{dc} reference for the control inputs.					
INH ⁽¹⁾	8	The inhibit pin is a negative logic input that is referenced to AGND. Applying a low-level signal to this pin disables the module and turns off the output voltage. A 10 k Ω pull-up to 3.3 V or 5 V is required if the INH signal is not used.					
FAULT	9	Current limit flag. The Fault signal is a 3.3 V digital output which is latched high after an over-current condition. The Fault is reset after two complete PWM cycles without an over-current condition (third rising edge of the PWM).					
SRE	10	Synchronized Rectifier Enable. This pin is a high impedance digital input. A 3.3 V or 5 V logic level signals is used to enable the synchronous rectifier switch. When this signal is high, the module will source and sink output current. When this signal is low, the module will only source current.					
PWM	11	This is the PWM input pin. It is a high impedance digital input that accepts 3.3 V or 5 V logic level signals up to 500 MHz.					
VBIAS	12	Bias voltage supply required to power internal circuitry. For optimal performance connect VBIAS to V _I .					

(1) Denotes negative logic: Open = Normal operation, Ground = Function active



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TYPICAL CHARACTERISTICS⁽¹⁾ ⁽²⁾



 The electrical characteristic data has been developed from actual products tested at 25°C. This data is considered typical for the converter. Applies to Figure 1 thru Figure 5.

(2) The temperature derating curves represent the conditions at which internal components are at or below the manufacturer's maximum operating temperatures. Derating limits apply to modules soldered directly to a 100 mm x 100 mm double-sided PCB with 2 oz. copper. Please refer to the mechanical specification for more information. Applies to Figure 6 thru Figure 9.

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TYPICAL CHARACTERISTICS⁽¹⁾ ⁽²⁾

CHARACTERISTIC DATA ($V_1 = 5 V$)



(1) The electrical characteristic data has been developed from actual products tested at 25°C. This data is considered typical for the converter. Applies to Figure 10 thru Figure 14.

(2) The temperature derating curves represent the conditions at which internal components are at or below the manufacturer's maximum operating temperatures. Derating limits apply to modules soldered directly to a 100 mm x 100 mm double-sided PCB with 2 oz. copper. Please refer to the mechanical specification for more information. Applies to Figure 15 thru Figure 18.

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Figure 16. Safe Operating Area



Figure 18. Safe Operating Area

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APPLICATION INFORMATION

Digital Power





Figure 19 shows the UCD9240 power supply controller working in a system which requires the regulation of four independent power supplies. The loop for each power supply is created by the respective voltage outputs feeding into the Error ADC differential inputs, and completed by DPWM outputs feeding into the UCD7230 drivers which are shown on the PTD08A010W and PTD08A020W modules.



UCD9240 Graphical User Interface (GUI)

When using the UCD9240 digital controller along with digital PowerTrain modules to design a digital power system, several internal parameters of the modules are required to run the Fusion Digital Power Designer GUI. See the plant parameters below for the PTD08A010W and PTD08A020W digital PowerTrain modules.

Table 1. PTD08A010W Plant Parameters

PTD08A010W Plant Parameters							
L (μH) DCR (mΩ) Rds-on-hi (mΩ) Rds-on-lo (mΩ)							
0.90	2.2	3.6	3.6				

Table 2. PTD08A020W Plant Parameters

PTD08A020W Plant Parameters							
L (μH) DCR (mΩ) Rds-on-hi (mΩ) Rds-on-lo (mΩ)							
1.0	1.5	5.0	2.5				

Internal output capacitance is present on the digital PowerTrain modules themselves. When using the GUI interface this capacitance information must be included along with any additional external capacitance. See the capacitor parameters below for the PTD08A010W and PTD08A020W digital PowerTrain modules.

Table 3. PTD08A010W Capacitor Parameters

PTD08A010W Capacitor Parameters						
C (μF) ESR (mΩ) ESL (nH) Quantity						
47	1.5	2.5	1			

Table 4. PTD08A020W Capacitor Parameters

PTD08A020W Capacitor Parameters						
C (μF) ESR (mΩ) ESL (nH) Quantity						
47	1.5	2.5	2			

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/ Ball Finish	MSL Peak Temp ⁽³⁾	Samples (Requires Login)
PTD08A020WAD	ACTIVE	Through- Hole Module	EGP	12	42	Pb-Free (RoHS)	SN	Level-1-235C-UNLIM/ Level-3-260C-168HRS	Request Free Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

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⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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