

## 16 x 8 x 1 BiMOS-E Crosspoint Switch

The Intersil CD22M3494 is an array of 128 analog switches capable of handling signals from DC to video. Because of the switch structure, input signals may swing through the total supply voltage range,  $V_{DD}$  to  $V_{EE}$ . Each of the 128 switches may be addressed via the ADDRESS input to the 7 to 128 line decoder. The state of the addressed switch is established by the signal to the DATA input. A low or zero input will open the switch, while a high logic level or a one will result in closure of the addressed switch when the STROBE input goes high from its normally low state. Any number or combination of connections may be active at one time. Each connection, however, must be made or broken individually in the manner previously described. All switches may be reset by taking the RESET input from a zero state to a one state and then returning it to its normal low state.

CS allows crosspoint array to be cascaded for matrix expansion.

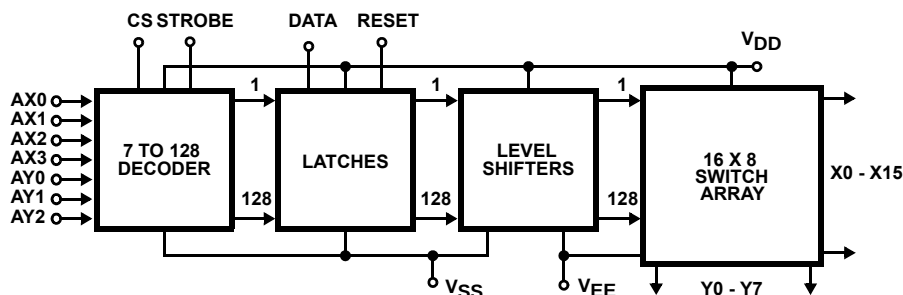
## Features

- 128 Analog Switches
- Low  $r_{ON}$
- Guaranteed  $r_{ON}$  Matching
- Analog Signal Input Voltage Equal to the Supply Voltage
- Wide Operating Voltage . . . . . 4V to 15V
- Parallel Input Addressing
- High Latch Up Current . . . . . 50mA (Min)
- Very Low Crosstalk
- Pin and Functionally Compatible with the Following Types: SGS M3494 and Mitel MT8816
- Pb-Free Plus Anneal Available (RoHS Compliant)

## Applications

- PBX Systems
- Instrumentation
- Analog and Digital Multiplexers
- Video Switching Networks

## Block Diagram



## Ordering Information

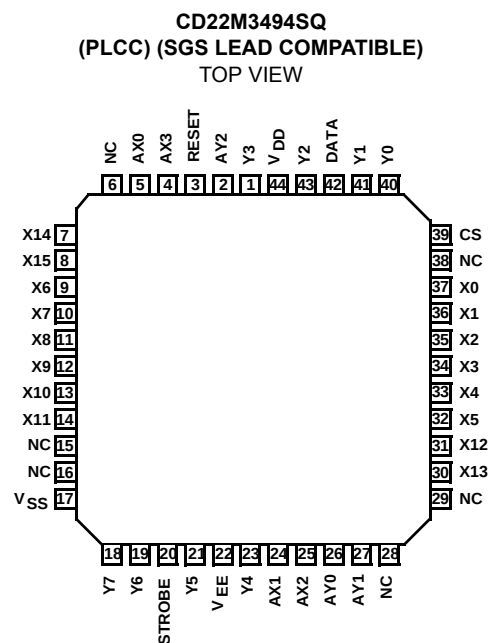
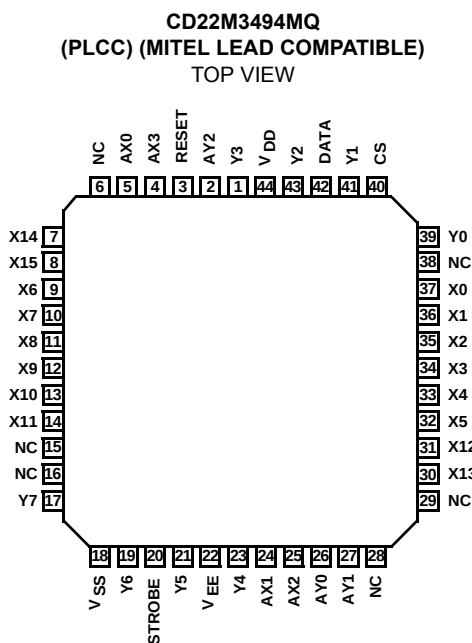
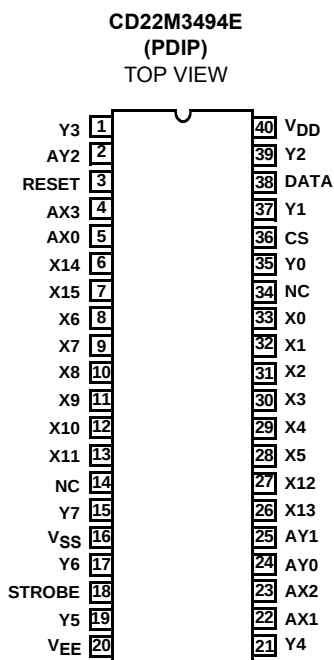
| PART NUMBER               | PART MARKING  | TEMP. RANGE (°C) | PACKAGE                                    | PKG. DWG. # |
|---------------------------|---------------|------------------|--------------------------------------------|-------------|
| CD22M3494E                | CD22M3494E    | -40 to 85        | 40 Ld PDIP                                 | E40.6       |
| CD22M3494EZ (See Note)    | CD22M3494EZ   | -40 to 85        | 40 Ld PDIP** (Pb-free)                     | E40.6       |
| CD22M3494MQ*              | CD22M3494MQ   | -40 to 85        | 44 Ld PLCC (Mitel Ld Compatible)           | N44.65      |
| CD22M3494MQZ* (See Note)  | CD22M3494MQZ  | -40 to 85        | 44 Ld PLCC (Mitel Ld Compatible) (Pb-free) | N44.65      |
| CD22M3494MQA*             | CD22M3494MQA  | -40 to 85        | 44 Ld PLCC (Mitel Ld Compatible)           | N44.65      |
| CD22M3494MQAZ* (See Note) | CD22M3494MQAZ | -40 to 85        | 44 Ld PLCC (Mitel Ld Compatible) (Pb-free) | N44.65      |
| CD22M3494SQ               | CD22M3494SQ   | -40 to 85        | 44 Ld PLCC (SGS Ld Compatible)             | N44.65      |
| CD22M3494SQZ (See Note)   | CD22M3494SQZ  | -40 to 85        | 44 Ld PLCC (SGS Ld Compatible) (Pb-free)   | N44.65      |

\*Add "96" suffix for tape and reel.

\*\*Pb-free PDIPs can be used for through hole wave solder processing only. They are not intended for use in Reflow solder processing. applications.

NOTE: Intersil Pb-free plus anneal products employ special Pb-free material sets; molding compounds/die attach materials and 100% matte tin plate termination finish, which are RoHS compliant and compatible with both SnPb and Pb-free soldering operations. Intersil Pb-free products are MSL classified at Pb-free peak reflow temperatures that meet or exceed the Pb-free requirements of IPC/JEDEC J STD-020.

## Pinouts



**Absolute Maximum Ratings**

|                                                                          |                                 |
|--------------------------------------------------------------------------|---------------------------------|
| DC Supply Voltage ( $V_{DD}$ )                                           |                                 |
| Voltages Referenced to $V_{EE}$ .....                                    | -0.5 to 16V                     |
| DC Supply Voltage ( $V_{DD}$ )                                           |                                 |
| Voltages Referenced to $V_{SS}$ .....                                    | -0.5, 16V                       |
| DC Input Diode Current, $I_{IN}$                                         |                                 |
| For $V_I$ , Digital < $V_{SS}$ -0.5V or $V_I$ ,                          |                                 |
| Analog < $V_{EE}$ -0.5V or $V_I$ > $V_{DD}$ 0.5V .....                   | $\pm 20$ mA                     |
| DC Output Diode Current, $I_{OK}$                                        |                                 |
| For $V_O$ , Digital < $V_{SS}$ -0.5V or $V_O$ ,                          |                                 |
| Analog < $V_{EE}$ -0.5V or $V_O$ > $V_{DD}$ 0.5V .....                   | $\pm 20$ mA                     |
| DC Transmission Gate Current .....                                       | $\pm 25$ mA                     |
| Power Dissipation Per Package ( $P_o$ )                                  |                                 |
| For $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ (PDIP) .....         | 500mW                           |
| For $T_A = 60^\circ\text{C}$ to $85^\circ\text{C}$ Derate Linearly ..... | 12mW/ $^\circ\text{C}$ to 200mW |
| For $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ (PLCC) .....         | 600mW                           |

**Thermal Information**

|                                                                                                                                                  |                                              |
|--------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------|
| Thermal Resistance (Typical, Note 1)                                                                                                             | $\theta_{JA}$ ( $^\circ\text{C}/\text{W}$ )  |
| PDIP Package* .....                                                                                                                              | 55                                           |
| PLCC Package .....                                                                                                                               | 43                                           |
| Maximum Junction Temperature Plastic Package .....                                                                                               | 150 $^\circ\text{C}$                         |
| Maximum Storage Temperature Range ( $T_{STG}$ ) .....                                                                                            | -65 $^\circ\text{C}$ to 150 $^\circ\text{C}$ |
| Maximum Lead Temperature (Soldering 10s) .....                                                                                                   | 300 $^\circ\text{C}$                         |
| (PLCC - Lead Tips Only)                                                                                                                          |                                              |
| *Pb-free PDIPs can be used for through hole wave solder processing only. They are not intended for use in Reflow solder processing applications. |                                              |

**Operating Conditions**

|                                                              |                                             |
|--------------------------------------------------------------|---------------------------------------------|
| Operating Temperature Range ( $T_A$ )                        |                                             |
| Package Type E and Q .....                                   | -40 $^\circ\text{C}$ to 85 $^\circ\text{C}$ |
| Supply Voltage Range                                         |                                             |
| For $T_A$ = Full Package Temperature Range                   |                                             |
| $V_{SS} = 0\text{V}$ , $V_{EE} = 0\text{V}$ , $V_{DD}$ ..... | 4V to 15V                                   |
| DC Input or Output Voltage $V_I$ or $V_O$ .....              | $V_{EE}$ to $V_{DD}$                        |
| Digital Input Voltage .....                                  | $V_{SS}$ to $V_{DD}$                        |

**CAUTION:** Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

**NOTE:**

1.  $\theta_{JA}$  is measured with the component mounted on an evaluation PC board in free air.

**Electrical Specifications**  $T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$ ,  $V_{DD} = 5\text{V}$ ,  $V_{SS} = 0\text{V}$ ,  $V_{EE} = 0\text{V}$ , Unless Otherwise Specified

| PARAMETER                      | SYMBOL   | TEST CONDITIONS                                 | MIN             | TYP | MAX                  | UNITS         |
|--------------------------------|----------|-------------------------------------------------|-----------------|-----|----------------------|---------------|
| <b>STATIC CONTROLS</b>         |          |                                                 |                 |     |                      |               |
| Supply Current                 | $I_{DD}$ | $V_{DD} = 5\text{V}$ , Logic Inputs = $V_{DD}$  | -               | -   | 2                    | mA            |
|                                |          | $V_{DD} = 15\text{V}$ , Logic Inputs = $V_{DD}$ | -               | -   | 5                    | mA            |
| High-Level Input Voltage       | $V_{IH}$ | $V_{DD} = 5\text{V}$                            | 2.4<br>(Note 2) | -   | -                    | V             |
| Low-Level Input Voltage        | $V_{IL}$ |                                                 | -               | -   | 0.8<br>(Note 2)      | V             |
| Input Leakage Current, Digital | $I_{IN}$ | Reset = Low (Note 3)                            | -               | -   | $\pm 10$<br>(Note 4) | $\mu\text{A}$ |

**Electrical Specifications**  $T_A = -40^\circ\text{C}$  to  $85^\circ\text{C}$ ,  $V_{DD} = 12\text{V}$ ,  $V_{SS} = 0\text{V}$ ,  $V_{EE} = 0\text{V}$ , Unless Otherwise Specified.

| PARAMETER                                            | SYMBOL          | TEST CONDITIONS                                                                                                                           | MIN                   | TYP | MAX | UNITS       |
|------------------------------------------------------|-----------------|-------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|-----|-----|-------------|
| <b>STATIC CROSSPOINTS</b>                            |                 |                                                                                                                                           |                       |     |     |             |
| ON Resistance                                        | $r_{ON}$        | $V_{SS} = V_{EE} = 0\text{V}$ ,<br>$T_A = 25^\circ\text{C}$ , $V_{IN} = V_{DD}/2$ , $V_X$<br>- $V_Y = 0.2\text{V}$                        | $V_{DD} = 10\text{V}$ | -   | 40  | 75 $\Omega$ |
|                                                      |                 |                                                                                                                                           | $V_{DD} = 12\text{V}$ | -   | 36  | 65 $\Omega$ |
| ON Resistance                                        | $r_{ON}$        | $T_A = -40^\circ\text{C}$ to $85^\circ\text{C}$ ,<br>$V_{IN} = V_{DD}/2$ , $V_X$ - $V_Y = 0.2\text{V}$ ,<br>$V_{SS} = V_{EE} = 0\text{V}$ | $V_{DD} = 10\text{V}$ | -   | 50  | 75 $\Omega$ |
|                                                      |                 |                                                                                                                                           | $V_{DD} = 12\text{V}$ | -   | 45  | 65 $\Omega$ |
| Difference in ON Resistance Between Any Two Switches | $\Delta r_{ON}$ | $T_A = 25^\circ\text{C}$ , $V_{IN} = V_{DD}/2$ , $V_X$ - $V_Y = 0.2\text{V}$ ,<br>$V_{SS} = V_{EE} = 0\text{V}$ , $V_{DD} = 12\text{V}$   | -                     | 6   | 10  | $\Omega$    |

**Electrical Specifications**  $T_A = -40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ ,  $V_{DD} = 12\text{V}$ ,  $V_{SS} = 0\text{V}$ ,  $V_{EE} = 0\text{V}$ , Unless Otherwise Specified. (Continued)

| PARAMETER                                            | SYMBOL          | TEST CONDITIONS                                                                                                                                                                           | MIN | TYP | MAX                  | UNITS         |
|------------------------------------------------------|-----------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|-----|----------------------|---------------|
| Difference in ON Resistance Between Any Two Switches | $\Delta r_{ON}$ | $T_A = -40^{\circ}\text{C}$ to $85^{\circ}\text{C}$ , $V_{IN} = V_{DD}/2$ ,<br>$V_X - V_Y = 0.2\text{V}$ , $V_{DD} = 12\text{V}$<br>$V_{SS} = V_{EE} = 0\text{V}$ , $V_{DD} = 12\text{V}$ | -   | -   | 10                   | $\Omega$      |
| OFF-State Leakage Current                            | $I_L$           | $ V_X - V_Y  = 12\text{V}$                                                                                                                                                                | -   | -   | $\pm 10$<br>(Note 4) | $\mu\text{A}$ |

**Electrical Specifications**  $T_A = 25^{\circ}\text{C}$ ,  $V_{SS} = 0\text{V}$ ,  $V_{EE} = 0\text{V}$ ,  $V_{DD} = 14\text{V}$ ,  $C_L = 50\text{pF}$ , Unless Otherwise Specified.

| PARAMETER                                                                                          |       | TEST CONDITIONS                                                                                                                                   | MIN | TYP  | MAX | UNITS              |
|----------------------------------------------------------------------------------------------------|-------|---------------------------------------------------------------------------------------------------------------------------------------------------|-----|------|-----|--------------------|
| DYNAMIC CROSSPOINTS                                                                                |       |                                                                                                                                                   |     |      |     |                    |
| Switch I/O Capacitance                                                                             |       | V <sub>IN</sub> = V <sub>DD</sub> /2, f = 1MHz                                                                                                    | -   | -    | 20  | pF                 |
| Switch Feedthrough Capacitance                                                                     |       | V <sub>IN</sub> = V <sub>DD</sub> /2, f = 1MHz                                                                                                    | -   | 0.3  | -   | pF                 |
| Propagation Delay Time (Switch ON)<br>Signal Input to Output, t <sub>PHL</sub> or t <sub>PLH</sub> |       |                                                                                                                                                   | -   | 5    | 30  | ns                 |
| Frequency Response Channel ON<br>f = 20log (V <sub>X</sub> /V <sub>Y</sub> ) = -3dB                |       | C <sub>L</sub> = 3pF, R <sub>L</sub> = 75Ω, V <sub>IN</sub> = 2V <sub>P-P</sub>                                                                   | -   | 50   | -   | MHz                |
| Total Harmonic, THD                                                                                |       | V <sub>IN</sub> = 2V <sub>P-P</sub> , f = 1kHz                                                                                                    | -   | 0.01 | -   | %                  |
| Feedthrough Channel OFF<br>Feedthrough = 20log (V <sub>X</sub> /V <sub>Y</sub> ) = F <sub>DT</sub> |       | V <sub>IN</sub> = 2V <sub>P-P</sub> , f = 1kHz                                                                                                    | -   | -95  | -   | dB                 |
| Frequency for Signal Crosstalk, f <sub>CT</sub><br>Attenuation of:                                 | 40dB  | V <sub>IN</sub> = 2V <sub>P-P</sub> , R <sub>L</sub> = 75Ω                                                                                        | -   | 10   | -   | MHz                |
|                                                                                                    | 110dB | V <sub>IN</sub> = 2V <sub>P-P</sub> , R <sub>L</sub> = 1kΩ    10pF                                                                                | -   | 5    | -   | kHz                |
| Control Crosstalk<br>DATA-Input, ADDRESS,<br>or STROBE to Output                                   |       | Control Input = 3V <sub>P-P</sub><br>Square Wave, t <sub>R</sub> = t <sub>F</sub> = 10ns<br>R <sub>IN</sub> = 1K, R <sub>OUT</sub> = 10kΩ    10pF | -   | 75   | -   | mV <sub>PEAK</sub> |

**Electrical Specifications**  $T_A = 25^{\circ}\text{C}$ ,  $V_{SS} = 0\text{V}$ ,  $V_{EE} = 0\text{V}$ ,  $V_{DD} = 14\text{V}$ ,  $R_L = 1\text{k}\Omega \parallel 50\text{pF}$ , Unless Otherwise Specified.

| PARAMETER                                  | SYMBOL           | TEST CONDITIONS                | MIN | TYP | MAX | UNITS |
|--------------------------------------------|------------------|--------------------------------|-----|-----|-----|-------|
| DYNAMIC CONTROLS                           |                  |                                |     |     |     |       |
| Digital Input Capacitance                  | C <sub>IN</sub>  | V <sub>IN</sub> = 5V, f = 1MHz | -   | 5   | -   | pF    |
| Propagation Delay Time<br>STROBE to Output |                  |                                |     |     |     |       |
| Switch Turn-ON                             | t <sub>PSN</sub> |                                | -   | 50  | 100 | ns    |
| Switch Turn-OFF                            | t <sub>PSF</sub> |                                | -   | 50  | 100 | ns    |
| DATA-IN to Output                          |                  |                                |     |     |     |       |
| Turn-ON to High Level                      | t <sub>PZH</sub> |                                | -   | 60  | 100 | ns    |
| Turn-ON to Low Level                       | t <sub>PZL</sub> |                                | -   | 70  | 100 | ns    |
| ADDRESS to Output                          |                  |                                |     |     |     |       |
| Turn-ON to High Level                      | t <sub>PAN</sub> |                                | -   | 70  | -   | ns    |
| Turn-OFF to Low Level                      | t <sub>PAF</sub> |                                | -   | 70  | -   | ns    |

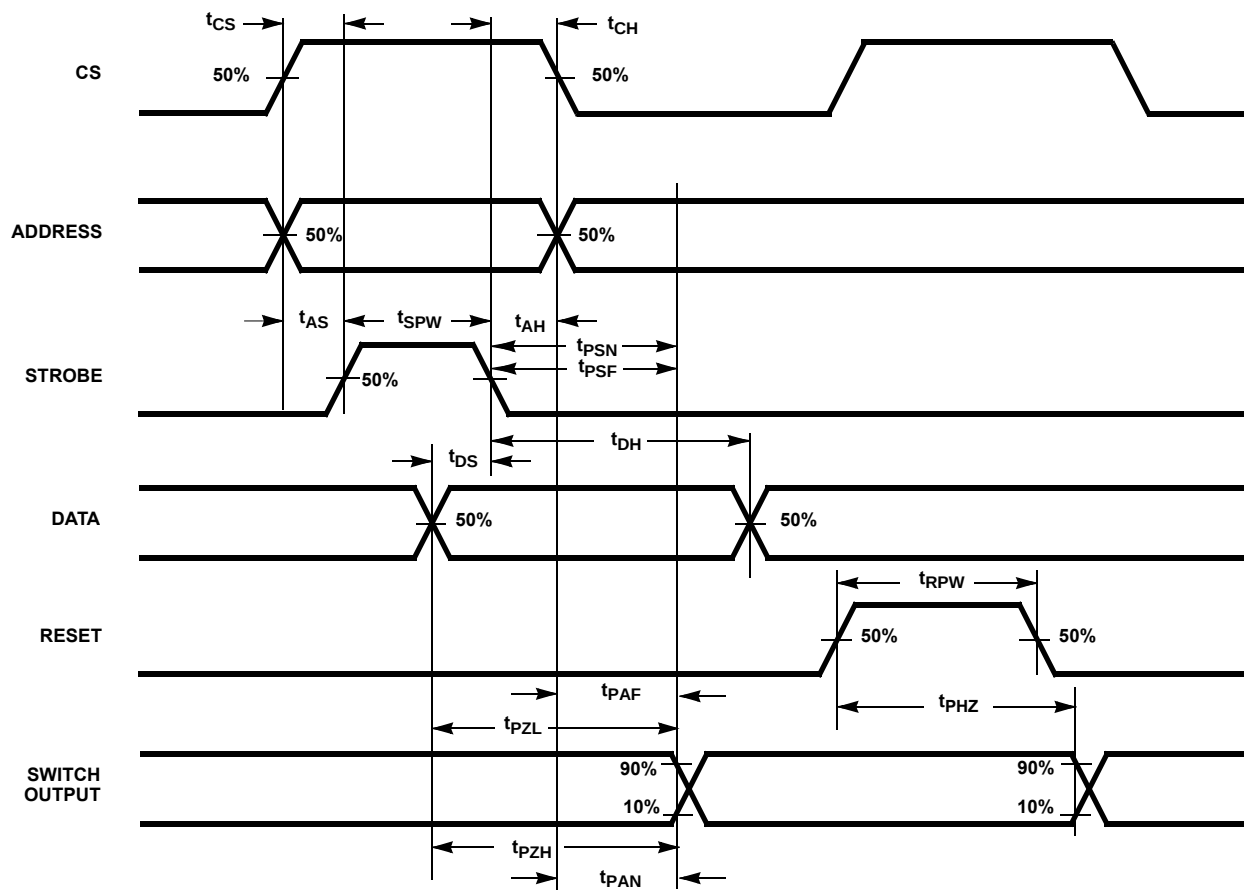
**Electrical Specifications**  $T_A = 25^\circ\text{C}$ ,  $V_{SS} = 0\text{V}$ ,  $V_{EE} = 0\text{V}$ ,  $V_{DD} = 14\text{V}$ ,  $R_L = 1\text{k}\Omega \parallel 50\text{pF}$ , Unless Otherwise Specified. (Continued)

| PARAMETER                      | SYMBOL    | TEST CONDITIONS | MIN | TYP | MAX | UNITS |
|--------------------------------|-----------|-----------------|-----|-----|-----|-------|
| Setup Time                     |           |                 |     |     |     |       |
| CS to STROBE                   | $t_{CS}$  |                 | 10  | -   | -   | ns    |
| DATA-IN to STROBE              | $t_{DS}$  |                 | 10  | -   | -   | ns    |
| ADDRESS to STROBE              | $t_{AS}$  |                 | 10  | -   | -   | ns    |
| Hold Time                      |           |                 |     |     |     |       |
| STROBE to CS                   | $t_{CH}$  |                 | 10  | -   | -   | ns    |
| ADDRESS to CS                  |           |                 | 10  | -   | -   | ns    |
| STROBE to DATA-IN              | $t_{DH}$  |                 | 20  | -   | -   | ns    |
| STROBE to ADDRESS              | $t_{AH}$  |                 | 10  | -   | -   | ns    |
| DATA-IN to CS                  |           |                 | 20  | -   | -   | ns    |
| Pulse Width                    |           |                 |     |     |     |       |
| STROBE                         | $t_{SPW}$ |                 | 20  | -   | -   | ns    |
| RESET                          | $t_{RPW}$ |                 | 20  | -   | -   | ns    |
| RESET Turn-OFF to Output Delay | $t_{PHZ}$ |                 | -   | 70  | 100 | ns    |

NOTES:

- Operation of  $V_{IH}$  at 2.4V or  $V_{IL}$  at 0.8V will result in much higher supply current ( $I_{DD}$ ) than for logic inputs equal to  $V_{DD}$  or  $V_{SS}$  respectively.
- Reset  $I_{IH} < 20\mu\text{A}$ , Reset =  $V_{IH}$ .
- At  $25^\circ\text{C}$  Limit is  $\pm 100\text{nA}$ .

## Timing Diagram



TRUTH TABLE X AXIS

| X ADDRESS |     |     |     |          |
|-----------|-----|-----|-----|----------|
| AX3       | AX2 | AX1 | AX0 | X SWITCH |
| 0         | 0   | 0   | 0   | X0       |
| 0         | 0   | 0   | 1   | X1       |
| 0         | 0   | 1   | 0   | X2       |
| 0         | 0   | 1   | 1   | X3       |
| 0         | 1   | 0   | 0   | X4       |
| 0         | 1   | 0   | 1   | X5       |
| 0         | 1   | 1   | 0   | X12      |
| 0         | 1   | 1   | 1   | X13      |
| 1         | 0   | 0   | 0   | X6       |
| 1         | 0   | 0   | 1   | X7       |
| 1         | 0   | 1   | 0   | X8       |
| 1         | 0   | 1   | 1   | X9       |
| 1         | 1   | 0   | 0   | X10      |
| 1         | 1   | 0   | 1   | X11      |
| 1         | 1   | 1   | 0   | X14      |
| 1         | 1   | 1   | 1   | X15      |

TRUTH TABLE Y AXIS

| Y ADDRESS |     |     |          |
|-----------|-----|-----|----------|
| AY2       | AY1 | AY0 | Y SWITCH |
| 0         | 0   | 0   | Y0       |
| 0         | 0   | 1   | Y1       |
| 0         | 1   | 0   | Y2       |
| 0         | 1   | 1   | Y3       |
| 1         | 0   | 0   | Y4       |
| 1         | 0   | 1   | Y5       |
| 1         | 1   | 0   | Y6       |
| 1         | 1   | 1   | Y7       |

To make a connection (close switch) between any two points, specify an "X" address, a "Y" address, set "DATA" high, and switch "STROBE" from low to high. To break a connection, follow this same procedure with "DATA" low.

Example:

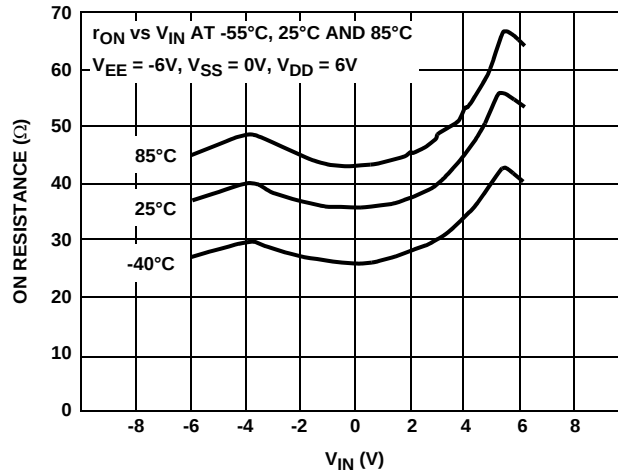
To connect switch X3 to switch Y4:

To connect switch X6 to switch Y7:

To break connection from X3 to Y4:

| DATA | X ADDRESS |     |     |     | Y ADDRESS |     |     |
|------|-----------|-----|-----|-----|-----------|-----|-----|
|      | AX3       | AX2 | AX1 | AX0 | AY2       | AY1 | AY0 |
| 1    | 0         | 0   | 1   | 1   | 1         | 0   | 0   |
| 1    | 1         | 0   | 0   | 0   | 1         | 1   | 1   |
| 0    | 0         | 0   | 1   | 1   | 1         | 0   | 0   |

## Typical Performance Curve



## Pin Descriptions

| SYMBOL          | 40 LD PDIP<br>PIN NO. | 44 LD PLCC<br>PIN NO. |    | DESCRIPTION                                                                                                                                                                                                                                                          |
|-----------------|-----------------------|-----------------------|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|                 |                       | MQ                    | SQ |                                                                                                                                                                                                                                                                      |
| POWER SUPPLIES  |                       |                       |    |                                                                                                                                                                                                                                                                      |
| V <sub>DD</sub> | 40                    | 44                    | 44 | Positive Supply.                                                                                                                                                                                                                                                     |
| V <sub>SS</sub> | 16                    | 18                    | 17 | Negative Supply (Digital).                                                                                                                                                                                                                                           |
| V <sub>EE</sub> | 20                    | 22                    | 22 | Negative Supply (Analog).                                                                                                                                                                                                                                            |
| ADDRESS         |                       |                       |    |                                                                                                                                                                                                                                                                      |
| AX0 - AX3       | 5, 22, 23 and 4       | 5, 24, 25 and 4       |    | X Address Lines. These pins select one of the 16 rows of switches. See the Truth Table for the valid addresses.                                                                                                                                                      |
| AY0 - AY2       | 24, 25 and 2          | 26, 27 and 2          |    | Y Address Lines. These pins select one of the 8 columns of switches. See the Truth Table for the valid addresses.                                                                                                                                                    |
| CONTROL         |                       |                       |    |                                                                                                                                                                                                                                                                      |
| DATA            | 38                    | 42                    |    | DATA Input determines the state of the addressed switch. A high or one will close the switch. A low or zero will open the switch.                                                                                                                                    |
| STROBE          | 18                    | 20                    |    | STROBE Input enables the action defined by the DATA and ADDRESS Inputs. A low or zero results in no action. The ADDRESS Input must be stable before the STROBE Input goes to the active high level. The DATA Input must be stable on the falling edge of the STROBE. |
| RESET           | 3                     | 3                     |    | MASTER RESET. A high or one on this line opens all switches.                                                                                                                                                                                                         |
| CS              | 36                    | 40                    | 39 | CHIP SELECT. Device is selected when CS is at a high level, allows the crosspoint array to be cascaded for matrix expansion.                                                                                                                                         |





Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



#### Как с нами связаться

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