# Using the UCD3138CC64EVM-030

# **User's Guide**



Literature Number: SLUU886A February 2012-Revised July 2013



## Control Card for Digitally Controlled Isolated Power Converters

#### 1 Introduction

The UCD3138CC64EVM-030 evaluation module helps evaluate the UCD3138 device made by Texas Instruments and aids in the design of digitally controlled isolated power converters. The EVM can be used either as a stand-alone control card to study the UCD3138 controller device, or as a DPWM controller board working with a power-stage board to implement a fully regulated power converter. To help the targeted off-line isolated power applications, this EVM has been designed to work seamlessly with the UCD3138PFCEVM-026, UCD3138PSFBEVM-027, and UCD3138LLCEVM-028 power-converter EVMs offered by TI.

Alternately the EVM can also be loaded with custom user-developed firmware. In order to communicate with the UCD3138 digital controller in this EVM, a separate USB Interface Adapter EVM from TI known as the USB-TO-GPIO Adapter is required. The USB-TO-GPIO adapter is NOT supplied with UCD3138CC64EVM-030 evaluation module and must be purchased separately. TI also offers a Graphical User Interface (GUI) in order to program the UCD3138 controller and configure parameters when used with the power-converter EVMs mentioned above.

### 2 Description

The UCD3138CC64EVM-030 is an EVM board, functioning as a control card for UCD3138RGC digital power-supply applications. This EVM is used to control a power-converter topology such as PFC preregulator, LLC Resonant Half-Bridge DC converter, and Phase-Shifted Full-Bridge DC converter by downloading the associated firmware and interfacing with an appropriate power stage board. After the UCD3138 is programmed appropriately, the EVM works seamlessly with the following EVM boards from Texas Instruments. Please visit www.ti.com to check EVM status and availability.

- UCD3138PFCEVM-026, a digital controlled PFC pre-regulator evaluation board, Texas Instruments Literature Number, <u>SLUU885</u>
- UCD3138PSFBEVM-027, a digital controlled phase-shift full-bridge DC-to-DC converter evaluation board
- UCD3138LLCEVM-028, a digital controlled LLC resonant half-bridge DC-to-DC converter evaluation board, Texas Instruments Literature Number, <u>SLUU979</u>

## 2.1 Typical Applications

- Isolated Power Supply Applications (such as single-phase, two-phase interleaved or bridgeless PFC, LLC resonant half-bridge DC-to-DC power converter, phase-shifted full-bridge DC-to-DC power converter, and hard switching full-bridge DC-to-DC power converter)
- Server Power Supplies and Telecom Rectifiers
- Isolated DC-to-DC Telecom Modules

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#### 2.2 Features

- 40-Pin Digital Signal Connector to Connect Digital Signals to Power Converters
- 40-Pin Analog Signal Connector to Connect Analog Signals to Power Converters
- JTAG Connector
- LED Indicator
- PMBus Connector to PC Computer Connection Through USB-to-GPIO Adapter
- Rich Test Points to Facilitate the Device Evaluation, System Design, and Circuit and Firmware Debugging
- 12-V Input Capable With Onboard Regulator 3.3 V

## 3 Specifications

| Table 1. UCD3138CC64EVM-030 | Specifications |
|-----------------------------|----------------|
|-----------------------------|----------------|

| PARAMETER                      | TEST CONDITIONS  | MIN      | TYP  | MAX  | UNITS  |
|--------------------------------|--|----------|------|------|--------|
| Connector J1                   |  |          |      | Ļ    |        |
| PMBus connector                | Port of connection to USB-to-GPIO, pin definition refer<br>to TI standard USB-to-GPIO document SLLU093 | Standard |      |      |        |
| Connector J2                   |  |          |      |      |        |
| 3.3-V connection to PMBus      | Port to use on board 3.3 VD to bias PMBus <sup>(1)</sup>   | 3.25     | 3.30 | 3.35 | VDC    |
| Connector J3                   |  |          |      |      |        |
| Analog signal connection       | Pin definition in compliance with UCD3138  | 40-pin   |      |      |        |
| Connector J4                   |  |          |      |      |        |
| Digital signal connection      | Pin definition in compliance with UCD3138  | 40-pin   |      |      |        |
| Pin 39                         | External voltage source input  | 11.5     | 12.0 | 12.5 | VDC    |
| Connector J5                   |  |          |      | Ļ    |        |
| JTAG                           | Standard JTAG communication connection   | Standard |      |      |        |
| Connector J6                   |  |          |      | Ļ    |        |
| 3.3-V on board to external use | Port to use 3.3 V on board to bias external circuit  | 3.27     | 3.30 | 3.32 | VDC    |
| Operation Environment          |  |          |      | Ļ    |        |
| Operating temperature range    | Natural Convection   |          | 25   |      | °C     |
| MECHANICAL CHARACTERS          | TICS   |          |      | Ļ    |        |
|                                | Width  |          | 1.8  |      |        |
| Dimensions                     | Length   |          | 3.4  |      | inches |
|                                | Component height   |          | 0.5  |      |        |

<sup>(1)</sup> Apply jumper to provide a 3.3-V bias to board from USB-to-GPIO adapter.

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## 4 Schematics and Test Points





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## TEXAS INSTRUMENTS

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Figure 2. UCD3138CC64EVM-030 Schematic

11

AGND

5

DGND

DGND

 $\rightarrow$ 

Schematics and Test Points

## 4.1 List of Test Points

The functions of each test point for the UCD3138CC64EVM-030.

| TEST POINTS | NAME               | DESCRIPTION   |  |
|-------------|--------------------|---|--|
| TP1         | 3.3VA              | 3.3-V analog on board   |  |
| TP2         | RC filter 2B       | DPWM2B RC filter  |  |
| TP3         | RC filter 3A       | DPWM3A RC filter  |  |
| TP4         | PWM-0              | PWM0  |  |
| TP5         | AGND               | Analog GND  |  |
| TP6         | DGND               | Digital GND   |  |
| TP7         | PWM-1              | PWM1  |  |
| TP8         | EADC-N0            | EANO  |  |
| TP9         | EADC-P1            | EAP1  |  |
| TP10        | EADC-P0            | EAPO  |  |
| TP11        | EADC-N1            | EAN1  |  |
| TP12        | DPWM-0A            | DPWM0A  |  |
| TP13        | DPWM-0B            | DPWM0B  |  |
| TP14        | DPWM-1A            | DPWM1A  |  |
| TP15        | DPWM-1B            | DPWM1B  |  |
| TP16        | TCAP               | ТСАР  |  |
| TP17        | DPWM-2A            | DPWM2A  |  |
| TP18        | DPWM-2B            | DPWM2B  |  |
| TP19        | DPWM-3A            | DPWM3A  |  |
| TP20        | DPWM-3B            | DPWM3B  |  |
| TP21        | AD-00              | A to D converter channel AD01                                     |  |
| TP22        | EADC-N2            | EAN2  |  |
| TP23        | EADC-P2            | EAP2  |  |
| TP24        | AD-01              | A to D converter channel AD00                                     |  |
| TP25 to 36  | AD-02 to -13       | A to D converter channels AD02 to AD13                            |  |
| TP37        | +12V_EXT           | External 12 V   |  |
| J1          | PMBus Connection   | PMBus connector, 10 pins  |  |
| J2          | +3.3VD             | Jumper header, if jump across, 3.3 V supplied from USB connection |  |
| J3          | Analog Connection  | 40-pin header, analog signals                                     |  |
| J4          | Digital Connection | 40-pin header, digital signals                                    |  |
| J5          | JTAG Connection    | 14-pin header, JTAG connector                                     |  |
| J6          | +3.3VD             | Jumper header, if jump across, 3.3 V supplied to outside need     |  |
| S1          | Reset              | UCD3138 reset, press to reset.                                    |  |

#### **Table 2. Test Point Functions**



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#### Test Equipment

### 5.1 PC Computer

#### 5.1.1 Operating System

Microsoft Windows XP (32 bit), or Vista (32 bit), or Windows 7 (32 bit).

## 5.2 USB-to-GPIO Interface Adapter

This adapter (not included in this EVM, order separately) establishes the communication between the control card UCD3138CC64EVM-030 and the PC computer through the PMBus and the GUI, <u>Texas</u> Instruments Fusion Digital Power Designer.

## 5.2.1 USB-to-GPIO Interface Adapter (HPA172)

Accessories included:

- USB Interface Adapter
- USB Cable, 5-pin B Mini Male to Type A Male
- Ribbon Cable, Socket-to-Socket, 10 Pin, 2 Headers, Polarized



Figure 3. USB-to-GPIO Interface Adapter Outlook

## 5.3 Oscilloscope

An oscilloscope of analog or digital type is capable of 200-MHz bandwidth with Tektronix P6138 or equivalent oscilloscope probe.

#### 6 Equipment Setup

#### 6.1 GUI Setup

#### 6.1.1 File for Installation

The GUI installation file is *TI-Fusion-Digital-Power-Designer-Version-1.8.92.exe* or newer version. To get the latest version of GUI, go to the Fusion Digital Power Designer tool folder (<u>Texas Instruments Fusion</u> Digital Power Designer) on the TI Web site, download, and install on computer.

#### 6.1.2 Installation

Double click and launch the *.exe* file to start the installation. Click *Next* all the way through. When prompted, read through the agreement and click *I accept the agreement* when finished. Then click *install*. After the installation, click *Finish* to exit setup. Then click *Exit Program*.

#### 6.1.3 Launch UCD3138 Device GUI

The GUI for UCD3138CC64EVM-030 board can be launched through the following steps:

- 1. Click the window Start
- 2. Click All Programs
- 3. Click Texas Instruments Fusion Digital Power Designer
- 4. Click Device GUIs
- 5. Click UCD3xxx Device GUI



Figure 4. Device GUI Launch Path

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## 6.2 Hardware Setup

#### 6.2.1 Setup Overview

The connection between UCD3138CC64EVM-030 and the PC computer through USB-to-GPIO Interface Adapter is shown in Figure 5.

For USB adapter connection, complete the following:

- Connect one end of the ribbon cable to the EVM, and connect the other end to the USB interface adapter.
- Connect the mini connector of the USB cable to the USB interface adapter, and connect the other end to the USB port of the PC computer.



Figure 5. UCD3138CC64EVM-030 Test Connections



Test Procedure

#### 7 Test Procedure

#### 7.1 Download Firmware Codes to UCD3138CC64EVM-030

See Figure 5 to set up the EVM connection.

- 1. Set up the EVM connection based on Figure 5. The LED of USB adapter lights up.
- 2. Use the provided jumper jump across J2. The LED of the EVM lights up.
- 3. Launch the UCD31xx device GUI following the steps described in Section 6.1.3. The window shown in Figure 6 appears.
- 4. Click Firmware Download and a new window appears as shown in Figure 7. Click Select File and browse an intended firmware code file with file extension .x0, for example, cyclone.x0. Then click Download. TI recommends not writing the program checksum (stay in ROM) during firmware development or debug state. The firmware of cyclone.x0 downloads to the device UCD3138 on the board of UCD3138CC64EVM-030. When asked, click yes to complete the download. Click Close to exit the download window.
- 5. After the firmware codes download to the UCD3138 device, the intended test can be performed. For example, with the provided firmware *cyclone.x0*, one can observe voltage toggled between 0 V and 3.3 V on test point TP7.

#### 7.2 Erase Firmware Codes from UCD3138CC64EVM-030

Erasing the downloaded firmware codes from UCD3138 flash memory can be made with the steps below based on Figure 6.

- 1. Click Device ID
- 2. Click Command Program to jump to ROM (SendByte.0xD9)
- 3. Click Erase/Set PFlash: 0xFF

### 7.3 Equipment Shutdown

- 1. Exit the GUI
- 2. Disconnect the USB cable and the ribbon cable

| tatus  | Tools  |  |  |  |  |
|--|--|--|--|--|--|
| Attached: ROM UCD31xx 1p0<br>Last ROM Found:<br>IC Tnfo: UCD31xx 1p0<br>ROM Info: ROM v3 IC v2<br>Package ID: 80-pin | Scan Device in ROM Mode           Scan for Device in Program Mode:         DEVICE_ID_PMBUS_REVISION           Image: The state of |  |  |  |  |
| Lest Program Found:<br>Address:<br>DEVICE_JDi<br>MFR_MODEL:<br>MFR_REVISION:   | ROM API         SMBus Debug         USB Adapter (SAA) Settings         Memory Debugger           Memory Peek/Poke         Firmware Download         Erase/Set DFlash:         0xFE 0xAA         Peek/Poke IC Registers and Firmware Variables           Report trim status         Dumo Info Block         Erase/Set PFlash:         0xFE 0xAA         Program Flash Checksum:         Dumo Cakulate         Recreate         Validate         Clear           Export Flash         Compare Flash Files         Dumo Flash File         Flash Test Tool         Full Export Tool         Vol Hex Tool           Device Debug Tool         Tool         Tool         Tool         Tool         Tool         Tool  |  |  |  |  |
| og   |  |  |  |  |  |
| Timestamp Message  | Message  |  |  |  |  |
| 11:26:50.582 Click one of the scan buttons to find a   | device in ROM or program mode  |  |  |  |  |
| 1:27:00.020 Looking for device in ROM mode at ad   | h  |  |  |  |  |
| 11:27:00.020 Reading ROM version   | Jess 110   |  |  |  |  |
| 11:27:00.145 Found ROM v3 IC v2 - UCD31xx 1p0  |  |  |  |  |  |
|  |  |  |  |  |  |
|  | Display all SMBus/I2C activ  |  |  |  |  |

#### Figure 6. UCD31xx Device GUI



| Firmware File:   | C:\CCStudio_v                               | /3.3\MyProjects\Cyclone tra | ining\Training 02 - Open Loop DPWM\cyclone.x0 Select File   |  |  |  |
|--|---|-----------------------------|---|--|--|--|
| Program flash n  | node:                                       | Data flash mode:            | Program flash checksum write mode (Device power up mode):   |  |  |  |
| Download program flash<br>(mass erases first)     Download data flash<br>(mass erases first)   |   | (mass erases first)         | <ul> <li>DO NOT write program checksum (Stay in ROM)</li> <li>Select this option for experimental firmware or if you need to be able to perform low-level<br/>debugging via the ROM. When the UCD3000 is powered on, it will stay in ROM mode.</li> </ul> |  |  |  |
| Erase progr  | am flash                                    | C Erase data flash          | WRITE program checksum (Automatically execute program)  |  |  |  |
| Skip progra  | m flash                                     | O Skip data flash           | Select this option for production devices. When the device is powered on, it will execte<br>program flash.  |  |  |  |
|  |   | Write pattern:              | PASS THRU whatever program checksum is present in the firmware image file   |  |  |  |
|  |   | 0xAA                        | This option can be used to test a firmware image produced by the Fusion GUI "File->Exp<br>tool PFlash+DFlash output or the UCD3000 Device GUI's "Export Flash" output.  |  |  |  |
|  | Task and the second                         |                             |   |  |  |  |
| Contraction of the Contraction o | and the second dates                        | v1.0.11 [PEC; 400 kHz] Fou  | und (Adapter #1)  |  |  |  |
| 11:27:58.536<br>11:27:58.536   | USB Adapter<br>Looking for d                | levice in ROM mode          | und (Adapter #1)  |  |  |  |
| Timestamp<br>11:27:58.536<br>11:27:58.536<br>11:27:58.598<br>11:27:58.598  | USB Adapter<br>Looking for o<br>ROM v3 IC v | levice in ROM mode          | und (Adapter #1)  |  |  |  |

Figure 7. Firmware Code Downloading



EVM Assembly Drawing and PCB layout

## 8 EVM Assembly Drawing and PCB layout

The following figures (Figure 8 through Figure 13) show the design of the UCD3138CC64EVM-030 printed circuit board. PCB dimensions:  $L \times W = 3.4$  in  $\times 1.8$  in, PCB material: FR4 or compatible, four layers and 1-oz copper on each layer.



Figure 8. UCD3138CC64EVM-030 Top Layer Assembly Drawing (top view)



Figure 9. UCD3138CC64EVM-030 Bottom Assembly Drawing (no components on this side)









Figure 11. UCD3138CC64EVM-030 Internal Layer 1 (top view)









Figure 13. UCD3138CC64EVM-030 Bottom Copper (top view)



## 9 List of Materials

Table 3 lists the EVM components according to the schematic shown in Figure 1 and Figure 2.

| QTY | RefDes   | Description  | Part Number    | MFR     |  |
|-----|--|--|----------------|---------|--|
| 3   | C1, C6, C32  | Capacitor, ceramic, 16 V, X7R, ±10%, 1 µF, 0603                    | STD            | STD     |  |
| 1   | C12  | Capacitor, ceramic, 16 V, X5R, ±10%, 2.2 µF, 0603                  | STD            | STD     |  |
| 7   | C2, C3, C4, C5,<br>C11, C13, C34   |  |                |         |  |
| 1   | C33  | Capacitor, ceramic, 10 V, X5R, ±10%, 10 µF, 0805                   | STD            | STD     |  |
| 1   | C7   | Capacitor, ceramic, 50 V, X7R, ±10%, 100 pF, 0603                  | STD            | STD     |  |
| 16  | C8, C14, C18,<br>C19, C20, C21,<br>C22, C23, C24,<br>C25, C26, C27,<br>C28, C29, C30,<br>C31 | Capacitor, ceramic, 50 V, X7R, ±10%, 1000 pF, 0603                 | STD            | STD     |  |
| 5   | C9, C10, C15,<br>C16, C17 Capacitor, ceramic, 50 V, NP0, ±10%, 33 pF, 0603                   |  | STD            | STD     |  |
| 2   | D1, D2   | Diode, dual Schottky, common anode, 300 mA, 30 V, SOT23            | BAT54AFILM     | ST      |  |
| 1   | D3   | Diode, LED, green, 2.1 V, 20 mA, 6 mcd, 0603                       | LTST-C190GKT   | Lite On |  |
| 1   | J1   | Header, 2 x 5 pin, 100-mil spacing                                 | 5103308-1      | Тусо    |  |
| 2   | J2, J6   | Header, male 2 pin, 100-mil spacing                                | PEC02SAAN      | Sullins |  |
| 2   | J3, J4   | Conn header 2-mm dual R/A 40POS                                    | PPPN202FJFN-RC | Sullins |  |
| 1   | J5   | Header, male 2 x 7 pin, 100-mil spacing                            | PEC07DAAN      | Sullins |  |
| 0   | R1, R2, R3, R4   | Resistor, chip, 1/16 W, 1%, open, 0603                             | STD            | STD     |  |
| 6   | R10, R32, R33,<br>R35, R37, R38  | Resistor, chip, 1/16 W, 1%, 10 kΩ, 0603                            | STD            | STD     |  |
| 1   | R11  | Resistor, chip, 1/16 W, 1%, 16 kΩ, 0603                            | STD            | STD     |  |
| 1   | R12  | Resistor, chip, 1/16 W, 1%, 1.65 kΩ, 0603                          | STD            | STD     |  |
| 2   | R13, R14   | Resistor, chip, 1/16 W, 1%, 1.5 kΩ, 0603                           | STD            | STD     |  |
| 1   | R15  | Resistor, chip, 1/16 W, 1%, 100 kΩ, 0603                           | STD            | STD     |  |
| 6   | R21, R22, R24,<br>R27, R28, R29  | Resistor, chip, 1/16 W, 1%, 2 kΩ, 0603                             | STD            | STD     |  |
| 1   | R30  | Resistor, chip, 1/16 W, 1%, 0.5 Ω, 0603                            | STD            | STD     |  |
| 1   | R31  | Resistor, chip, 1/16 W, 1%, 301 Ω, 0603                            | STD            | STD     |  |
| 1   | R39  | Resistor, chip, 1/10 W, 1%, 10 Ω, 0805                             | Std            | Std     |  |
| 12  | R5, R7, R8, R9,<br>R16, R17, R18,<br>R19, R20, R23,<br>R25, R26                              | Resistor, chip, 1/16 W, 1%, 100 Ω, 0603                            | STD            | STD     |  |
| 3   | R6, R34, R36   | Resistor, chip, 1/16 W, 1%, 0 Ω, 0603                              | STD            | STD     |  |
| 1   | S1   | Switch, SPST, PB momentary, sealed washable, 0.245 x 0.251         | KT11P2JM-34LFS | C & K   |  |
| 1   | U1   | Digital Power Controller, PFC-64                                   | UCD3138RGC     | TI      |  |
| 1   | U2   | High Input Voltage, Micro power, 3.2 µA at 80 mA LDO, 3.3 V, QFN-8 | TPS715A33DRBR  | TI      |  |

## Table 3. UCD3138CC64EVM-030 List of Materials



## Appendix A Summary of Using Code Composer Studio v3.3

This appendix describes the basic steps on how to use Code Composer Studio v3.3, or CCS, to compile firmware for UCD3138. The design flow describes detailed steps for firmware code creation and firmware debugging.

## A.1 Set Up Code Composer Studio v3.3 for UCD3138

The recommended version of Code Composer Studio is version 3.3 (v3.3). After completing the CCS v3.3 installation, and when CCS is opened for the first time, the window shown in Figure 14 prompts users to select the required configuration. For UCD3138 device, please select *ARM7 SIMULATOR BIG ENDIAN*. Click *ADD* and then *Save & Quit*.

If CCS has existing configurations in *My System*, click *Launch Setup* under *File* pull-down menu. Select *Remove All* to remove the existing configurations; then select *ARM7 SIMULATOR BIG ENDIAN* as shown in Figure 14, Click *ADD* and then *Save* & *Quit* for UCD3138 device.

| System Configuration    | Available Factory Boards           | Family Platform Endiann |            | Endiann | ha 🔿 | ARM7 Simulator, Big  |  |
|-------------------------|------------------------------------|-------------------------|------------|---------|------|--|--|
|                         |                                    | All 👻                   | All 💌      | All     |      | Endian   |  |
| My System               | ARM11 - VPOM2420 Platform Sim      | ARM11                   | simulator  | *       |      | Configuration File Location:                                 |  |
| - E: ARM7TDMI Simulator | ARM11 - VPOM2430 Platform Sim      | ARM11                   | simulator  | *       | -    |  |  |
| CPU                     | RARM7 - VPOM2420 Platform Simul    | ARM7                    | simulator  | *       |      | C:\CCSTUDIO_V3.3\drivers\imp                                 |  |
|                         | ARM7 Simulator, Big Endian         | ARM7                    | simulator  | big     |      |  |  |
|                         | RARM7 Simulator, Little Endian     | ARM7                    | simulator  | little  |      | Pre-Configured Board Description:                            |  |
|                         | ARM7 XDS510 Emulator               | ARM7                    | xds510     | *       |      | ARM7TDMI CPU Simulator with<br>memory maps to be specified t |  |
|                         | ARM7 XDS560 Emulator               | ARM7                    | xds560     | *       |      |  |  |
|                         | ARM9 XDS510 Emulator               | ARM9                    | xds510     | *       |      |  |  |
|                         | RM9 XDS560 Emulator                | ARM9                    | xds560     | *       |      |  |  |
|                         | ARM926EJ-S Simulator Little Endian | ARM9                    | simulator  | little  |      |  |  |
|                         | F240 XDS510 Emulator               | C24xx                   | xds510     | *       |      |  |  |
|                         | F240 XDS560 Emulator               | C24xx                   | xds560     | *       |      |  |  |
|                         | F2401 XDS510 Emulator              | C24xx                   | xds510     | *       |      |  |  |
|                         | F2401 XDS560 Emulator              | C24xx                   | xds560     | *       |      |  |  |
|                         | F2402 XD5510 Emulator              | C24xx                   | xds510     | *       |      |  |  |
|                         | F2402 XD5560 Emulator              | C24xx                   | xds560     | *       |      |  |  |
|                         | F2403 XD5510 Emulator              | C24xx                   | xds510     | *       |      |  |  |
|                         | F2403 XDS560 Emulator              | C24xx                   | xds560     | *       |      |  |  |
|                         | F2406 XDS510 Emulator              | C24xx                   | xds510     | *       |      |  |  |
|                         | F2406 XDS560 Emulator              | C24xx                   | xds560     | *       |      |  |  |
|                         | F2407 XD5510 Emulator              | C24xx                   | xds510     | *       |      |  |  |
|                         | F2407 XDS560 Emulator              | C24xx                   | xds560     | *       | (70) |  |  |
|                         | Encode Voction                     | CO.4                    | - during   | *       | M    |  |  |
|                         | 🔤 Factory Boards 🔤 Custom Boa      | rds 🧆 Cri               | eate Board |         | _    | <  |  |
|                         | _                                  |                         |            |         |      |  |  |

Figure 14. Set Up Code Composer Studio v3.3 for UCD3138



## A.2 Build and Compile a Project Using Code Composer Studio

After a project is created with all source codes developed, one can compile the project using CCS.

## A.2.1 Creating a Project

The example below describes the typical compile process for UCD3138 firmware. The project file name is *Cyclone.pjt* and it is located in the folder named *Training 02*. The final result of the compile process is a file with the file extension of *.x0*. Because the project name is *Cyclone.pjt*, the final file name of *Cyclone.x0* is naturally chosen. *Cyclone.x0* is the final firmware code downloaded to the UCD3138 device memory for the UCD3138 intended functional operation. The following are the steps for a typical compile process:

- 1. Copy file folder *Training 02* and paste into any desired directory inside the PC.
- 2. Launch CCS and open the CCS project file *Cyclone.pjt* from the directory where *Training 02* was saved. The window shown in Figure 15 appears.
  - Note, because the project *Cyclone.pjt* has been created and designed for UCD3138 functions, CCS can be launched without connecting an emulator.
- 3. From CCS project window, Right click on *Cyclone.pjt (Debug)* and then select *Build Options*. The window shown in Figure 16 appears when the *Linker* tab is selected.
  - Figure 16 shows the project *Build Options* have been selected to create the file *Cyclone.out* from CCS.
- 4. Convert the file Cyclone.out to Cyclone.x0. Cyclone.x0 is the final firmware code downloaded to the UCD3138 device memories. To convert Cyclone.out to Cyclone.x0, first click theGeneral tab under Build Options for Cyclone.pjt (Debug), as shown in Figure 17. Second, under the Build Command, confirm that the file to be converted is Cyclone.out. Third, click ok to close Build Options.

Steps 4, 5, and 6 are only necessary once per project. If using a platform with TI developed firmware, these steps have been set up and there is no need to repeat.

5. Select *Project Rebuild All*, as shown in Figure 18. *Project Rebuild All* generates the file *Cyclone.x0* based on *Cyclone.out*, and saves the file of *Cyclone.x0* inside the folder where *Cyclone.pjt* is saved.



Figure 15. Open a Project File with Example of *Cyclone.pjt* – Initial Open.



| yelone.pit → Debug → 🖉 🕮 🛗  | Build Options for Cyclone.pjt (Debug) ?<br>General Compiler Linker Link Order  |
|---|--|
| a 66°   | -c-heap10-m".\Debug\cyclone.map"-o".\Debug\cyclone.out"-stack200<br>-w -x  |
| Files Files GEL files Dependent Projects Documents DSP/BIOS Config Generated Files Include Ubraries Source Cyclone_global_variables_defs.c Interrupts.c Interru | Category:       Basic         ABI [abi=):       None, default to ARM9 •         Libraries       Suppress Banner [-q]         Output Module:       •         Output Filename [-o]:       \Debug\cyclone.out         Map Filename [-m]:       \Debug\cyclone.map         Autoinit Modet       Run-Time Autoinitialization [-c] •         Heap Size [-heap]:       10         Stack Size [-stack]:       200         Fill Value [4]:       Code Entry Point [-e]: |
| File View ABookmarks  | OK Cancel Help   |

Figure 16. Open a Project File with Example of Cyclone.pjt – Build Options and Linker Tab

| Cyclone.pit 💽 Debug 💽 🥙 🕮 🛗   | General Compiler Linker Link Order<br>Initial build steps:<br>Build Command      | Bun           |
|---|--|---------------|
| Files Files GEL files Projects Dependent Projects DSP/BIOS Config Generated Files Include Include Cyclone_global_variables_defs.c finterrupts.c Isource cyclone_global_variables_defs.c finterrupts.c Isource cyclone_mode cyclone_mode file View File View File View | Final build steps:<br>Build Command<br>hex470 -x -memwidth 8 .\debug\cyclone.out | Run<br>Always |

Figure 17. Open a Project File With Example of Cyclone.pjt - Build Options and General Tab





Figure 18. Opened Project File With Example of Cyclone.pjt - Rebuild All

## A.3 References

- 1. UCD3138 Datasheet, *Highly Integrated Digital Controller for Isolated Power*, Texas Instruments Literature Number, <u>SLUSAP2</u>
- Code Composer Studio Development Tools v3.3 Getting Started Guide, Texas Instruments Literature Number, <u>SPRU509</u>
- Reference Guide, UCD3138 Digital Power Peripherals Programmer's Manual, Texas Instruments Literature Number, <u>SLUU995</u>
- Reference Guide, UCD3138 Monitoring and Communications Programmer's Manual, Texas Instruments Literature Number, <u>SLUU996</u>
- 5. Reference Guide, UCD3138 ARM and Digital System Programmer's Manual, Texas Instruments Literature Number, <u>SLUU994</u>
- 6. User's Guide, UCD3138 Isolated Power Fusion GUI, (please contact TI).

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#### General Statement for EVMs including a radio

User Power/Frequency Use Obligations: This radio is intended for development/professional use only in legally allocated frequency and power limits. Any use of radio frequencies and/or power availability of this EVM and its development application(s) must comply with local laws governing radio spectrum allocation and power limits for this evaluation module. It is the user's sole responsibility to only operate this radio in legally acceptable frequency space and within legally mandated power limitations. Any exceptions to this are strictly prohibited and unauthorized by Texas Instruments unless user has obtained appropriate experimental/development licenses from local regulatory authorities, which is responsibility of user including its acceptable authorization.

#### For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

#### Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

#### FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- · Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

#### For EVMs annotated as IC – INDUSTRY CANADA Compliant

This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

#### Concerning EVMs including radio transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

#### Concerning EVMs including detachable antennas

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

#### Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

#### Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

#### [Important Notice for Users of EVMs for RF Products in Japan]

#### This development kit is NOT certified as Confirming to Technical Regulations of Radio Law of Japan

If you use this product in Japan, you are required by Radio Law of Japan to follow the instructions below with respect to this product:

- Use this product in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
- 2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
- 3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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