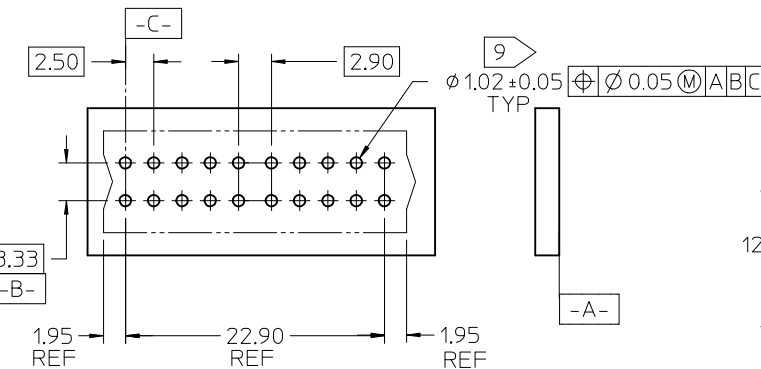
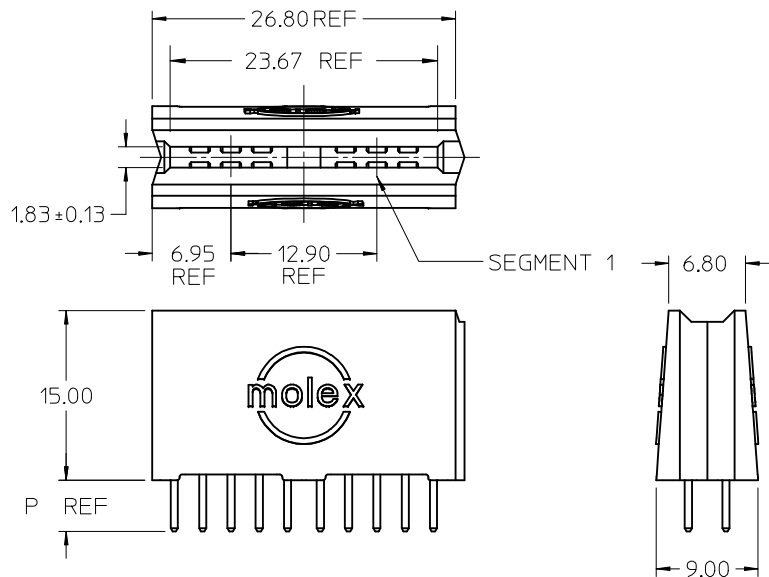
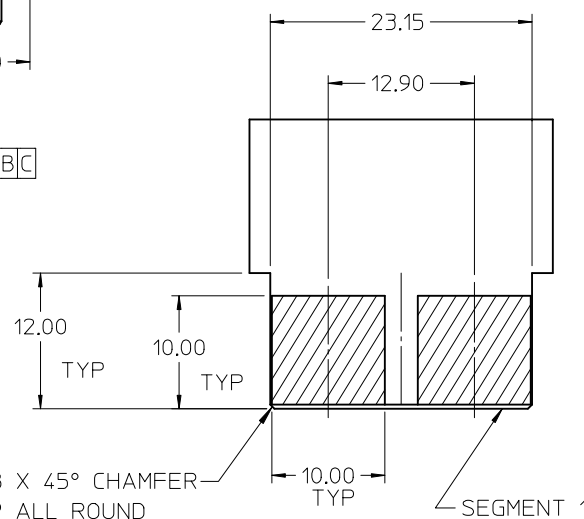




PCB LAYOUT: COMPONENT SIDE

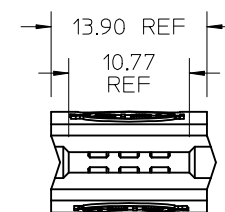


0.38 X 45° CHAMFER
TYP ALL ROUND

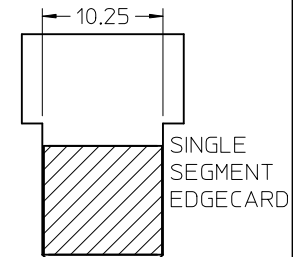
RECOMMENDED EDGE CARD LAYOUT
EDGE CARD TOLERANCE ± 0.05

NOTES:

- HOUSING MATL: LCP, UL94V-0, COLOR BLACK
TERMINAL MATL: COPPER ALLOY
- FINISH: SELECT GOLD: 30 M.I. MIN IN CONTACT AREA
SELECT TIN: 150 M.I. MIN IN SOLDER AREA
OVERALL NICKEL UNDERPLATE.
- PRODUCT SPECIFICATION: PS-45719-001.
- PACKAGING SPECIFICATION: ES-45714-001.
- PCB THICKNESS: SEE CHART
- EDGE CARD THICKNESS: 1.57 ± 0.15
- DIMENSIONS GIVEN ACROSS CENTERLINES ARE SYMMETRICAL
ABOUT THOSE CENTERLINES WITHIN HALF TOTAL TOLERANCE.
- VOIDED SEGMENTS DO NOT HAVE TERMINALS IN THE TOP OR BOTTOM ROW
- PCB HOLE SPECIFICATION: SEE PS-45719-001

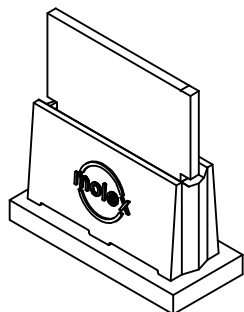


SINGLE
SEGMENT
MODULE
45719-1000



SINGLE
SEGMENT
EDGE CARD

MATERIAL NUMBER	SEG- MENTS	VOIDED SEGMENT	P	PCB THK
45719-1000	1	NONE	4.50	3.18
45719-1001	1	NONE	3.63	2.36
45719-1002	1	NONE	2.92	1.57
45719-0001	2	NONE	4.50	3.18
45719-0004	2	NONE	3.63	2.36
45719-0007	2	NONE	2.92	1.57



REMOVE VOIDS, SHT 1	DRWN:JSCHAFER	2012/10/19	CHKD:APATEL	2012/11/06
EC NO: UCP2013-1672	APPR:APATEL	2012/11/06	REV	DESCRIPTION

GENERAL TOLERANCES (UNLESS SPECIFIED)

	mm	INCH
4 PLACES	$\pm$ ---	$\pm$ ---
3 PLACES	$\pm$ ---	$\pm$ ---
2 PLACES	± 0.25	$\pm$ ---
1 PLACE	± 0.25	$\pm$ ---

ANGULAR $\pm 1/2^\circ$

DRAFT WHERE APPLICABLE
MUST REMAIN
WITHIN DIMENSIONS

DIMENSION STYLE MM ONLY

DRAWN BY	DATE
MARGULIS	2003/11/11
CHECKED BY	DATE
MARGULIS	2003/11/11
APPROVED BY	DATE
MARGULIS	2003/11/11

SCALE
1:1

DESIGN UNITS
METRIC

THIRD ANGLE
PROJECTION

TITLE	POWER EDGE ASSEMBLY POWER VERTICAL SOLDER TAIL
MOLEX	MOLEX INCORPORATED

DOCUMENT NO. SD-45719-001	SHEET NO. 1 OF 5
------------------------------	---------------------

THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX
INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION

6

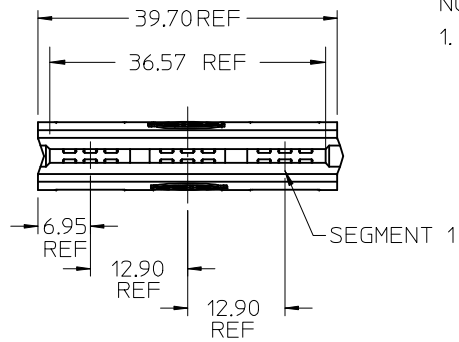
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4

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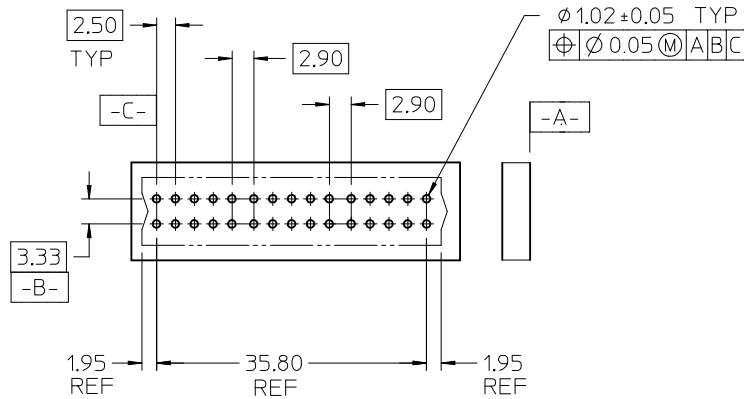
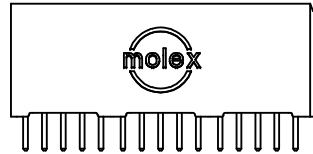
2

1

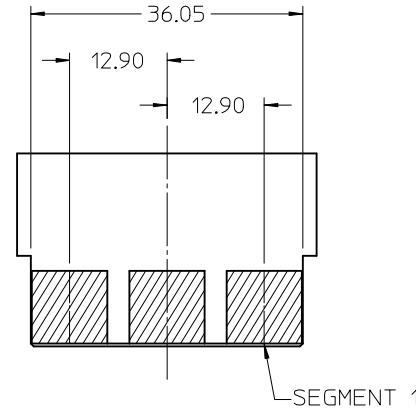


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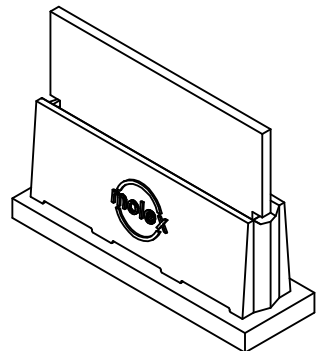
1. FOR ALL OTHER DIMENSIONS AND NOTES SEE SHEET 1.



PCB LAYOUT: COMPONENT SIDE

RECOMMENDED EDGE CARD LAYOUT
EDGE CARD TOLERANCE: ±0.05

MATERIAL NUMBER	VOIDED SEGMENT	P	PCB THK
45719-0002	NONE	4.50	3.18
45719-3001	1		
45719-3002	2		
45719-3003	3		
45719-3004	1,2		
45719-3005	1,3		
45719-3006	2,3		
45719-0005	NONE	3.63	2.36
45719-3007	1		
45719-3008	2		
45719-3009	3		
45719-3010	1,2		
45719-3011	1,3		
45719-3012	2,3		
45719-0008	NONE	2.92	1.57
45719-3013	1		
45719-3014	2		
45719-3015	3		
45719-3016	1,2		
45719-3017	1,3		
45719-3018	2,3		



EC NO: UCP2013-1672 DRWN: JSCHAFER 2012/10/19 CHKD: APPR: APATEL 2012/11/06	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY	DATE	POWER EDGE ASSEMBLY POWER VERTICAL SOLDER TAIL MOLEX INCORPORATED				
		4 PLACES	± ---	± ---	MARGULIS	2003/11/11					
		3 PLACES	± ---	± ---	CHECKED BY	DATE					
		2 PLACES	± 0.25	± ---	MARGULIS	2003/11/11					
		1 PLACE	± 0.25	± ---	APPROVED BY	DATE					
		ANGULAR ±1/2°			MARGULIS	2003/11/11					
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS						DOCUMENT NO. SD-45719-001			SHEET NO. 2		
	REV				SIZE A	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

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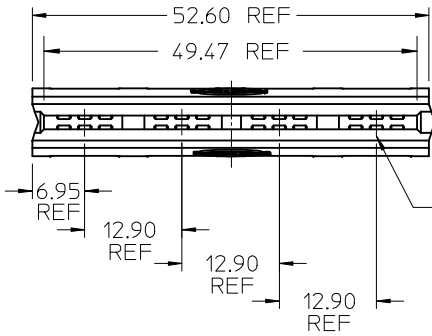
5

4

3

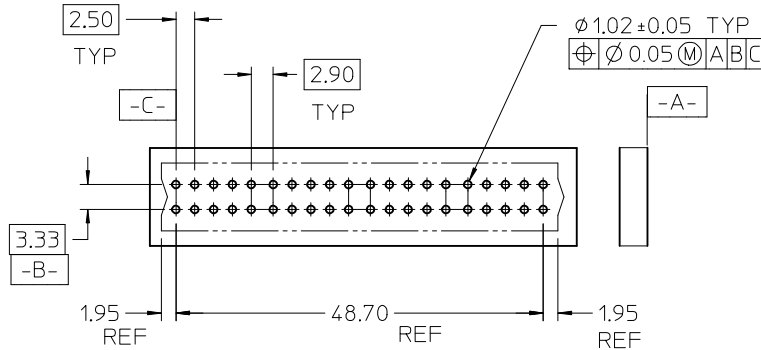
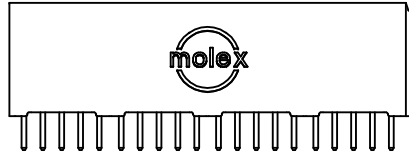
2

1

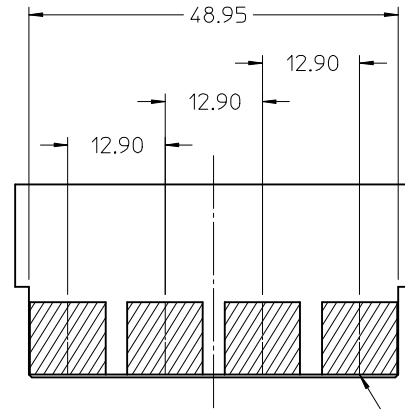


NOTES:

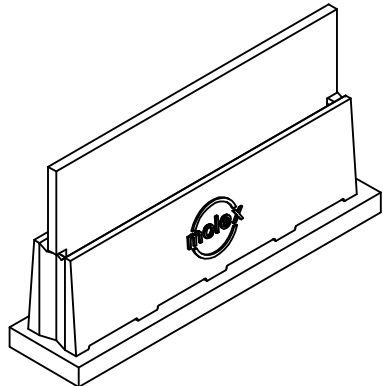
1. FOR ALL OTHER DIMENSIONS AND NOTES SEE SHEET 1.



PCB LAYOUT: COMPONENT SIDE

RECOMMENDED EDGE CARD LAYOUT
EDGE CARD TOLERANCE: ±0.05

MATERIAL NUMBER	VOIDED SEGMENT	P	PCB THK
45719-0003	NONE	4.50	3.18
45719-4001	1		
45719-4002	2		
45719-4003	3		
45719-4004	4		
45719-4005	1, 2		
45719-4006	1, 3		
45719-4007	1, 4		
45719-4008	2, 3		
45719-4009	2, 4		
45719-4010	3, 4	3.63	2.36
45719-0006	NONE		
45719-4011	1		
45719-4012	2		
45719-4013	3		
45719-4014	4		
45719-4015	1, 2		
45719-4016	1, 3		
45719-4017	1, 4		
45719-4018	2, 3		
45719-4019	2, 4	2.92	1.57
45719-4020	3, 4		
45719-0009	NONE		
45719-4021	1		
45719-4022	2		
45719-4023	3		
45719-4024	4		
45719-4025	1, 2		
45719-4026	1, 3		
45719-4027	1, 4		
45719-4028	2, 3		
45719-4029	2, 4		
45719-4030	3, 4		



EC NO: UCP2013-1672 DRWN: JSCHAFER 2012/10/19 CHKD: APPR: APATEL 2012/11/06	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY	DATE	TITLE POWER EDGE ASSEMBLY POWER VERTICAL SOLDER TAIL				
		4 PLACES	± ---	± ---	MARGULIS	2003/11/11					
		3 PLACES	± ---	± ---	CHECKED BY	DATE	MOLEX INCORPORATED				
		2 PLACES	± 0.25	± ---	MARGULIS	2003/11/11					
		1 PLACE	± 0.25	± ---	APPROVED BY	DATE	DOCUMENT NO. SD-45719-001				
ANGULAR ±1/2°			MARGULIS	2003/11/11	SHEET NO. 3						
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			SIZE A	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

5

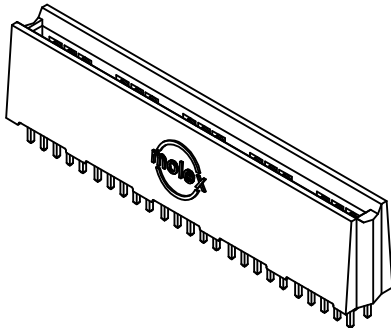
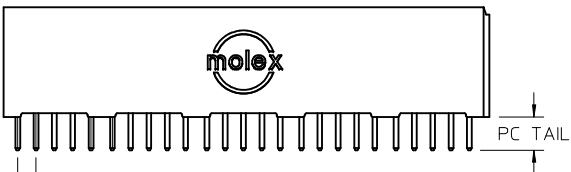
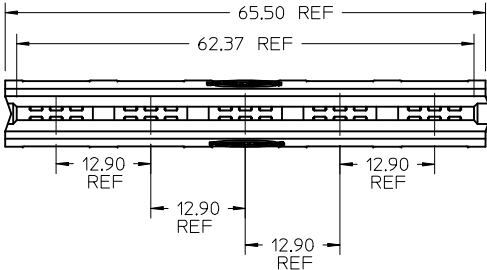
4

3

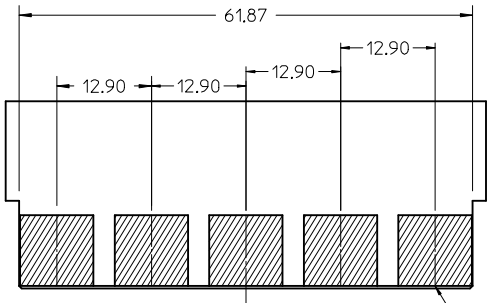
2

1

MATERIAL NUMBER	NO OF SEG'S	VOIDED SEGMENT	PCTAIL	PCB THK
45719-0010	5	NONE	4.50	3.18
45719-5001	5	1		
45719-5002	5	2		
45719-5003	5	3		
45719-5004	5	4		
45719-5005	5	5		
45719-5006	5	1, 2		
45719-5007	5	1, 3		
45719-5008	5	1, 4		
45719-5009	5	1, 5		
45719-5010	5	2, 3		
45719-5011	5	2, 4		
45719-5012	5	2, 5		
45719-5013	5	3, 4		
45719-5014	5	3, 5		
45719-5015	5	4, 5		

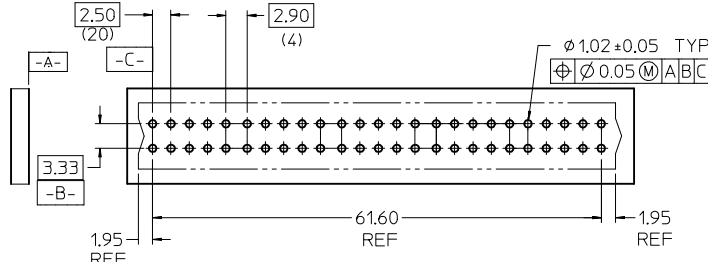


MATERIAL NUMBER	NO. OF SEG'S	VOIDED SEGMENT	PCTAIL	PCB THK
45719-0011	5	NONE	3.63	2.36
45719-5016	5	1		
45719-5017	5	2		
45719-5018	5	3		
45719-5019	5	4		
45719-5020	5	5		
45719-5021	5	1, 2		
45719-5022	5	1, 3		
45719-5023	5	1, 4		
45719-5024	5	1, 5		
45719-5025	5	2, 3		
45719-5026	5	2, 4		
45719-5027	5	2, 5		
45719-5028	5	3, 4		
45719-5029	5	3, 5		
45719-5030	5	4, 5		



RECOMMENDED EDGE CARD LAYOUT
EDGE CARD TOLERANCE: ±0.05

MATERIAL NUMBER	NO. OF SEG'S	VOIDED SEGMENT	PCTAIL	PCB THK
45719-0012	5	NONE	2.92	1.57
45719-5031	5	1		
45719-5032	5	2		
45719-5033	5	3		
45719-5034	5	4		
45719-5035	5	5		
45719-5036	5	1, 2		
45719-5037	5	1, 3		
45719-5038	5	1, 4		
45719-5039	5	1, 5		
45719-5040	5	2, 3		
45719-5041	5	2, 4		
45719-5042	5	2, 5		
45719-5043	5	3, 4		
45719-5044	5	3, 5		
45719-5045	5	4, 5		



PCB LAYOUT: COMPONENT SIDE

NOTE: FOR ALL OTHER NOTES & DIMENSIONS
SEE SHEET ONE

QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
$\nabla = 0$ $\nabla = 0$ $\nabla = 0$	4 PLACES ± --- ± ---	MM ONLY	1:1	METRIC	
	3 PLACES ± --- ± ---				
	2 PLACES ± 0.25 ± ---	DRAWN BY DATE	TITLE		
	1 PLACE ± 0.25 ± ---	MARGULIS 2003/11/11			
	ANGULAR ± 1/2°	CHECKED BY DATE	MARGULIS 2003/11/11		
	APPROVED BY DATE				
	MATERIAL NO.	DOCUMENT NO.	MOLEX INCORPORATED		
	SIZE B	SEE CHART	SD-45719-001		
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			SHEET NO. 4 OF 5		

SEE SHT 1

EC NO: UCP2013-1672

DRWN: JSCHAFER 2012/10/19

CHKD: 2012/11/06

APPR: APATEL

DESCRIPTION

REV

tb_frame_B_P_AM.T

Rev. F 2009/06/18

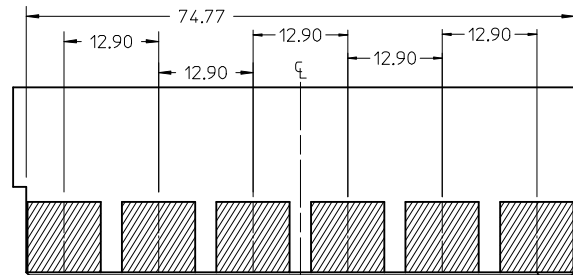
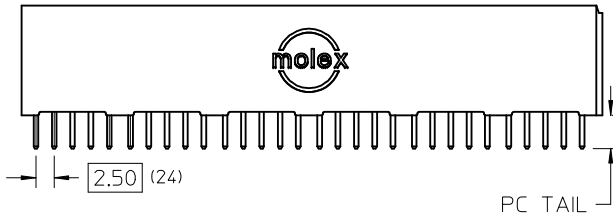
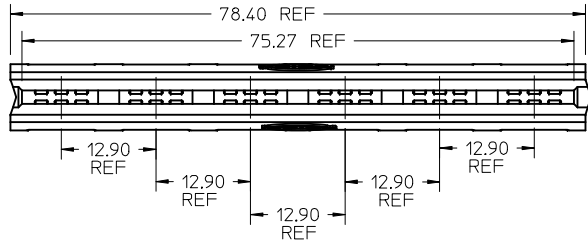
10 9 8 7 6 5 4 3 2 1

NOTE: FOR ALL OTHER NOTES & DIMENSIONS
SEE SHEET ONE.

NO OF SEG'S	MATERIAL NUMBER	VOIDED SEGMENT	PC TAIL	PCB THK
6	45719-0013	NONE	4.50	3.18
6	45719-6001	1		
6	45719-6002	2		
6	45719-6003	3		
6	45719-6004	4		
6	45719-6005	5		
6	45719-6006	6		
6	45719-6007	1, 2		
6	45719-6008	1, 3		
6	45719-6009	1, 4		
6	45719-6010	1, 5		
6	45719-6011	1, 6		
6	45719-6012	2, 3		
6	45719-6013	2, 4		
6	45719-6014	2, 5		
6	45719-6015	2, 6		
6	45719-6016	3, 4		
6	45719-6017	3, 5		
6	45719-6018	3, 6		
6	45719-6019	4, 5		
6	45719-6020	1, 6		
6	45719-6021	2, 6		
6	45719-6022	3, 6		
6	45719-6023	4, 6		
6	45719-6024	5, 6		

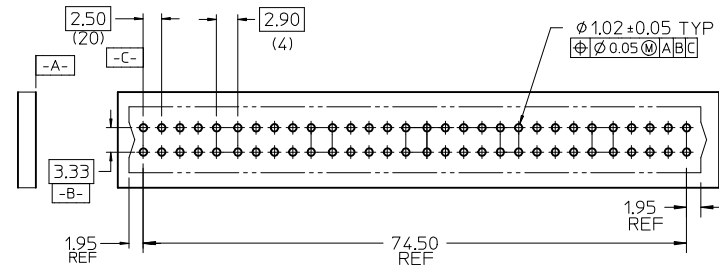
NO OF SEG'S	MATERIAL NUMBER	VOIDED SEGMENT	PC TAIL	PCB THK
6	45719-0014	NONE	3.63	2.36
6	45719-6025	1		
6	45719-6026	2		
6	45719-6027	3		
6	45719-6028	4		
6	45719-6029	5		
6	45719-6030	6		
6	45719-6031	1, 2		
6	45719-6032	1, 3		
6	45719-6033	1, 4		
6	45719-6034	1, 5		
6	45719-6035	1, 6		
6	45719-6036	2, 3		
6	45719-6037	2, 4		
6	45719-6038	2, 5		
6	45719-6039	2, 6		
6	45719-6040	3, 4		
6	45719-6041	3, 5		
6	45719-6042	3, 6		
6	45719-6043	4, 5		
6	45719-6044	1, 6		
6	45719-6045	2, 6		
6	45719-6046	3, 6		
6	45719-6047	4, 6		
6	45719-6048	5, 6		

NO OF SEG'S	MATERIAL NUMBER	VOIDED SEGMENT	PC TAIL	PCB THK
6	45719-0015	NONE	2.92	1.57
6	45719-6049	1		
6	45719-6050	2		
6	45719-6051	3		
6	45719-6052	4		
6	45719-6053	5		
6	45719-6054	6		
6	45719-6055	1, 2		
6	45719-6056	1, 3		
6	45719-6057	1, 4		
6	45719-6058	1, 5		
6	45719-6059	1, 6		
6	45719-6060	2, 3		
6	45719-6061	2, 4		
6	45719-6062	2, 5		
6	45719-6063	2, 6		
6	45719-6064	3, 4		
6	45719-6065	3, 5		
6	45719-6066	3, 6		
6	45719-6067	4, 5		
6	45719-6068	1, 6		
6	45719-6069	2, 6		
6	45719-6070	3, 6		
6	45719-6071	4, 6		
6	45719-6072	5, 6		

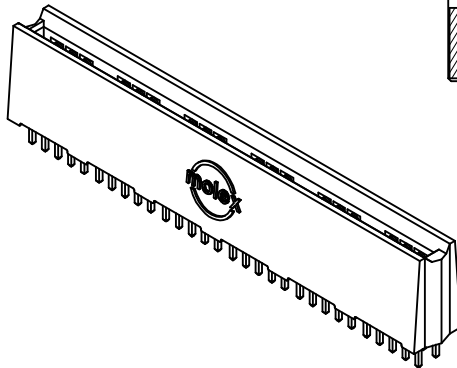


RECOMMENDED EDGE CARD LAYOUT
EDGE CARD TOLERANCE ± 0.05

SEGMENT 1



PCB LAYOUT: COMPONENT SIDE



SEE SHT 1 EC NO: UCP2013-1672 GDRWN: JSCHAFER CHKD: CHKD: APPR: APATEL REV	DESCRIPTION 2012/11/06	QUALITY SYMBOLS ▽ F=0 ▽ E=0 ▽ B=0	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE 1:1	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
				mm	INCH	DRAWN BY MARGULIS	DATE 2003/11/11	TITLE POWER EDGE ASSEMBLY POWER VERTICAL SOLDER TAIL				
			4 PLACES	± ---	± ---	CHECKED BY	DATE					
			3 PLACES	± ---	± ---	MARGULIS	2003/11/11	MOLEX INCORPORATED				
			2 PLACES	± 0.25	± ---	APPROVED BY	DATE					
1 PLACE	± 0.25	± ---	MARGULIS	2003/11/11	DOCUMENT NO. SD-45719-001							
ANGULAR ±1/2°			MATERIAL NO.							SHEET NO. 5 OF 5		
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			SEE CHART		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							



Компания «ЭлектроПласт» предлагает заключение долгосрочных отношений при поставках импортных электронных компонентов на взаимовыгодных условиях!

Наши преимущества:

- Оперативные поставки широкого спектра электронных компонентов отечественного и импортного производства напрямую от производителей и с крупнейших мировых складов;
- Поставка более 17-ти миллионов наименований электронных компонентов;
- Поставка сложных, дефицитных, либо снятых с производства позиций;
- Оперативные сроки поставки под заказ (от 5 рабочих дней);
- Экспресс доставка в любую точку России;
- Техническая поддержка проекта, помощь в подборе аналогов, поставка прототипов;
- Система менеджмента качества сертифицирована по Международному стандарту ISO 9001;
- Лицензия ФСБ на осуществление работ с использованием сведений, составляющих государственную тайну;
- Поставка специализированных компонентов (Xilinx, Altera, Analog Devices, Intersil, Interpoint, Microsemi, Aeroflex, Peregrine, Syfer, Eurofarad, Texas Instrument, Miteq, Cobham, E2V, MA-COM, Hittite, Mini-Circuits, General Dynamics и др.);

Помимо этого, одним из направлений компании «ЭлектроПласт» является направление «Источники питания». Мы предлагаем Вам помощь Конструкторского отдела:

- Подбор оптимального решения, техническое обоснование при выборе компонента;
- Подбор аналогов;
- Консультации по применению компонента;
- Поставка образцов и прототипов;
- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



Как с нами связаться

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Факс: 8 (812) 320-02-42

Электронная почта: org@eplast1.ru

Адрес: 198099, г. Санкт-Петербург, ул. Калинина, дом 2, корпус 4, литера А.