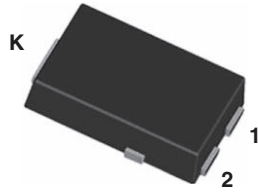
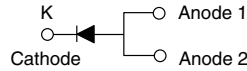


Hyperfast Rectifier, 4 A FRED Pt[®]



TO-277A (SMPC)



FEATURES

- Hyperfast recovery time, reduced Q_{rr} , and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


RoHS
 COMPLIANT
 HALOGEN
FREE

PRODUCT SUMMARY	
Package	TO-277A (SMPC)
$I_{F(AV)}$	4 A
V_R	200 V
V_F at I_F	0.73 V
t_{rr} (typ.)	27 ns
T_J max.	175 °C
Diode variation	Single die

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		200	V
Average rectified forward current	$I_{F(AV)}$	$T_{Sp} = 165$ °C	4	A
Non-repetitive peak surge current	I_{FSM}	$T_J = 25$ °C	130	
Operating junction and storage temperatures	T_J, T_{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100$ μ A	200	-	-	V
Forward voltage	V_F	$I_F = 4$ A	-	0.86	0.93	
		$I_F = 4$ A, $T_J = 125$ °C	-	0.73	0.79	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	-	2	μ A
		$T_J = 125$ °C, $V_R = V_R$ rated	-	2	10	
Junction capacitance	C_T	$V_R = 200$ V	-	23	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25\text{ }^\circ\text{C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t_{rr}	$I_F = 1\text{ A}$, $dI_F/dt = 50\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	27	-	ns	
		$I_F = 0.5\text{ A}$, $I_R = 1\text{ A}$, $I_{rr} = 0.25\text{ A}$	-	-	25		
		$T_J = 25\text{ }^\circ\text{C}$	-	20	-		
		$T_J = 125\text{ }^\circ\text{C}$	-	31	-		
Peak recovery current	I_{RRM}	$I_F = 4\text{ A}$ $dI_F/dt = 200\text{ A}/\mu\text{s}$ $V_R = 160\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$	-	2.2	-	A
			$T_J = 125\text{ }^\circ\text{C}$	-	4.4	-	
Reverse recovery charge	Q_{rr}		$T_J = 25\text{ }^\circ\text{C}$	-	22	-	nC
			$T_J = 125\text{ }^\circ\text{C}$	-	70	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J , T_{Stg}		-65	-	175	$^\circ\text{C}$
Thermal resistance, junction to solder pad	R_{thJ-Sp}		-	2.2	3	$^\circ\text{C}/\text{W}$
Approximate weight			0.1			g
			0.0035			oz.
Marking device		Case style TO-277A (SMPC)	JEH2			

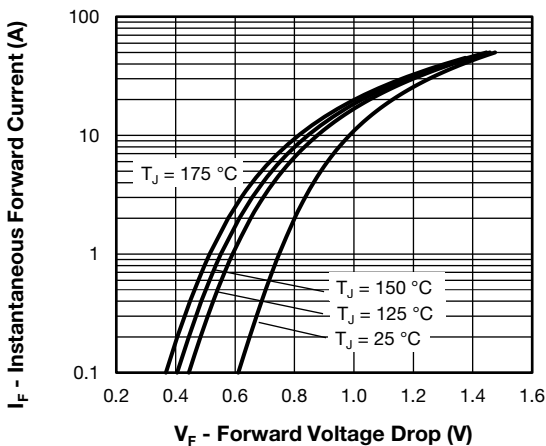


Fig. 1 - Typical Forward Voltage Drop Characteristics

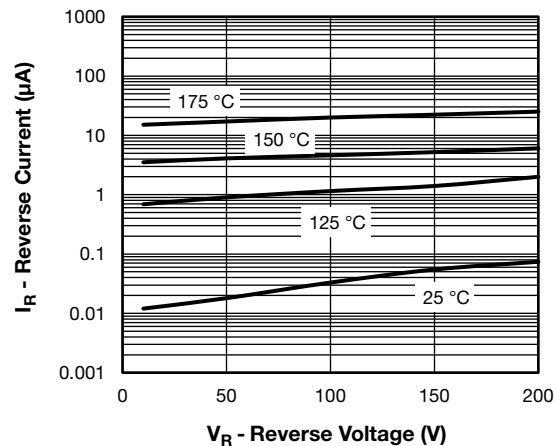


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

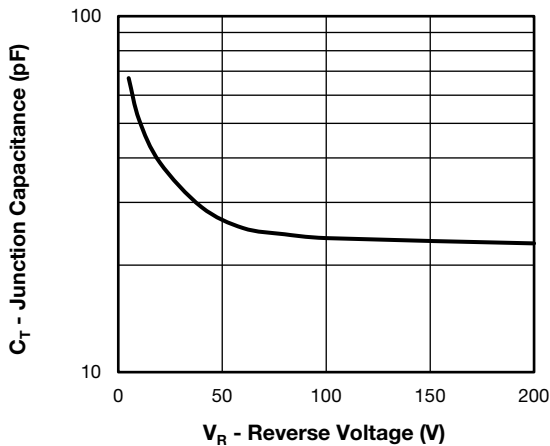


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

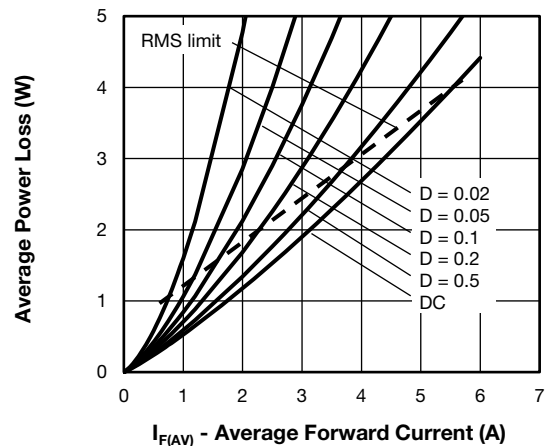


Fig. 5 - Forward Power Loss Characteristics

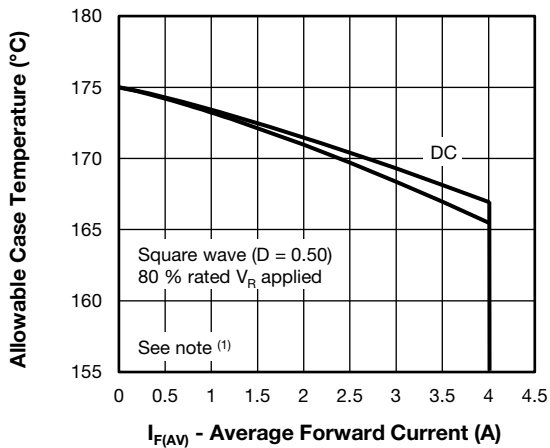


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

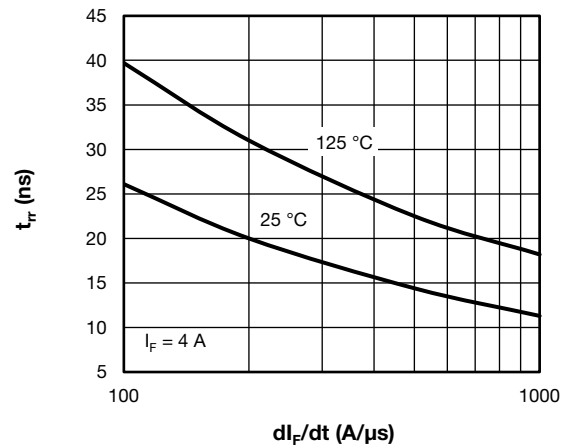


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

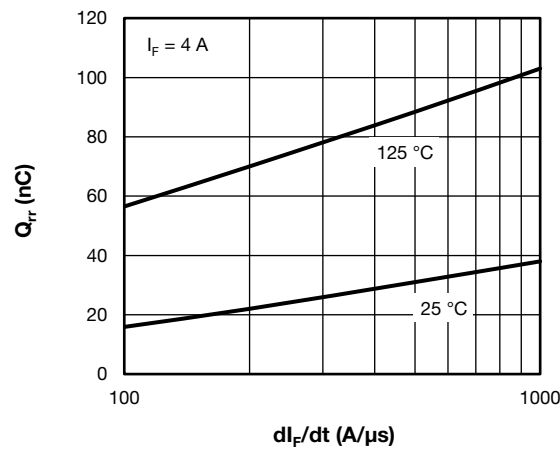


Fig. 7 - Typical Stored Charge vs. dI_F/dt

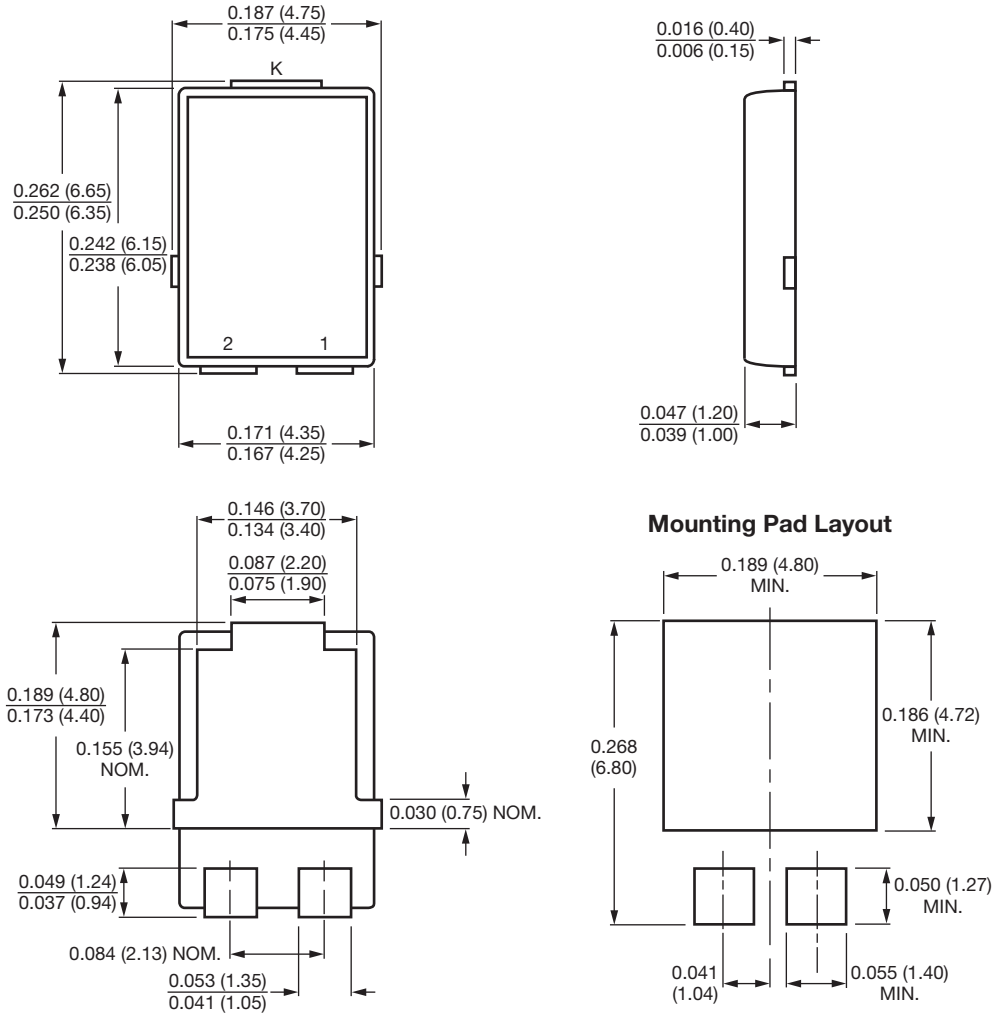
Note

- (1) Formula used: $T_C = T_J - (P_d + P_{d_{REV}}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5);
 $P_{d_{REV}}$ = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = rated V_R



TO-277A (SMPC)

DIMENSIONS in inches (millimeters)



Conform to JEDEC® TO-277A



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- Подбор аналогов;
- Консультации по применению компонента;
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- Техническая поддержка проекта;
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