

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on) max}$	$I_D$ $T_C = +25^\circ C$
-60V	110m $\Omega$ @ $V_{GS} = -10V$	-14A
	140m $\Omega$ @ $V_{GS} = -4.5V$	-12A

## Description

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

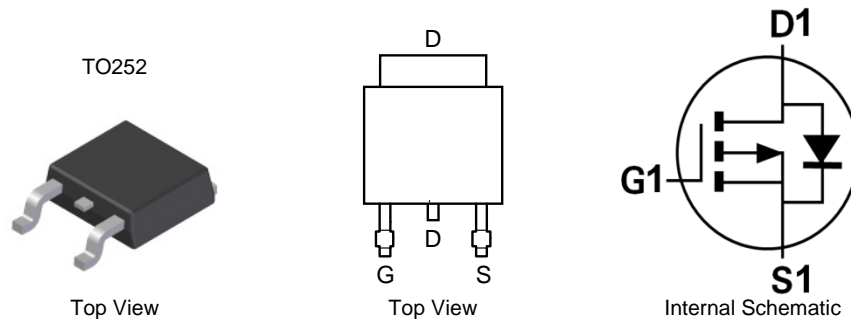
- DC-DC Converters
- Power Management Functions
- Analog Switch

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- **Totally Lead-Free & Fully RoHS compliant (Note 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

## Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound.  
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin annealed over Copper leadframe.  
Solderable per MIL-STD-202, Method 208  $\text{e3}$
- Weight: 0.33 grams (approximate)

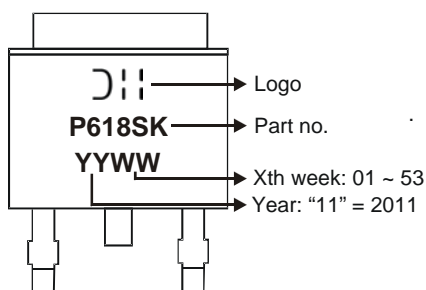


## Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
DMP6180SK3-13	Standard	TO252	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See [http://www.diodes.com/quality/lead\\_free.html](http://www.diodes.com/quality/lead_free.html) for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

## Marking Information



**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	-60	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	T <sub>C</sub> = +25°C T <sub>C</sub> = +100°C	I <sub>D</sub>	-14 -10	A
Maximum Body Diode Forward Current (Note 6)			I <sub>S</sub>	4.1	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	25	A

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	P <sub>D</sub>	1.7	W
	T <sub>A</sub> = +70°C		1.0	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	R <sub>θJA</sub>	76	°C/W
	t < 10s		33	
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	P <sub>D</sub>	2.7	W
	T <sub>A</sub> = +70°C		1.5	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	R <sub>θJA</sub>	50	°C/W
	t < 10s		24	
Total Power Dissipation (Note 6)	T <sub>C</sub> = +25°C	P <sub>D</sub>	40	W
	T <sub>C</sub> = +100°C		16	
Thermal Resistance, Junction to Case (Note 6)		R <sub>θJC</sub>	3.1	°C/W
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-60	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -48V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.2	—	-2.7	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	60	110	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -12A
		—	80	140		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8A
Forward Transfer Admittance	Y <sub>fs</sub>	—	15	—	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -12A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	984.7	—	pF	V <sub>DS</sub> = -30V, V <sub>GS</sub> = 0V, f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	58	—		
Reverse Transfer Capacitance	C <sub>rss</sub>	—	45.5	—		
Gate Resistance	R <sub>G</sub>	—	12.9	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	8.1	—	nC	V <sub>DS</sub> = -30V, I <sub>D</sub> = -12A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	17.1	—		
Gate-Source Charge	Q <sub>gs</sub>	—	3.2	—		
Gate-Drain Charge	Q <sub>gd</sub>	—	3.9	—		
Turn-On Delay Time	t <sub>D(on)</sub>	—	5.9	—	ns	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -30V, R <sub>GEN</sub> = 3Ω, R <sub>L</sub> = 2.5Ω
Turn-On Rise Time	t <sub>r</sub>	—	21.2	—		
Turn-Off Delay Time	t <sub>D(off)</sub>	—	30.9	—		
Turn-Off Fall Time	t <sub>f</sub>	—	39.1	—		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	—	19.9	—	ns	I <sub>S</sub> = -12A, dI/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	—	1.7	—	nC	I <sub>S</sub> = -12A, dI/dt = 100A/µs

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
  - Short duration pulse test used to minimize self-heating effect
  - Guaranteed by design. Not subject to production testing

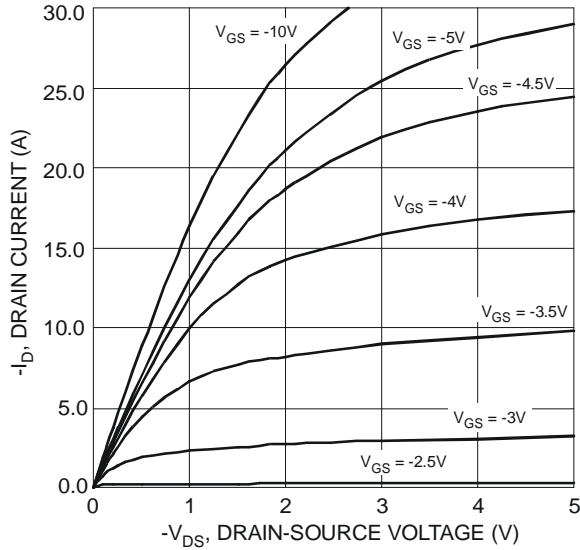


Figure 1 Typical Output Characteristics

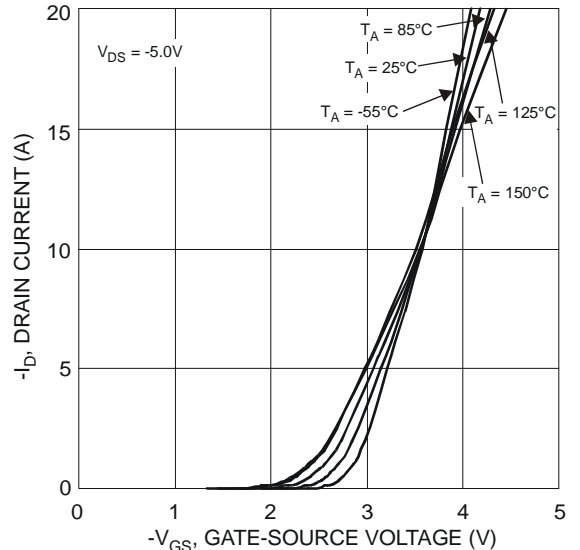


Figure 2 Typical Transfer Characteristics

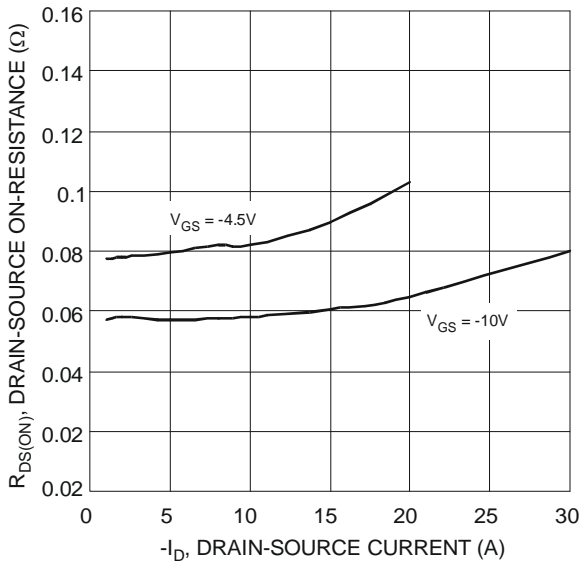


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

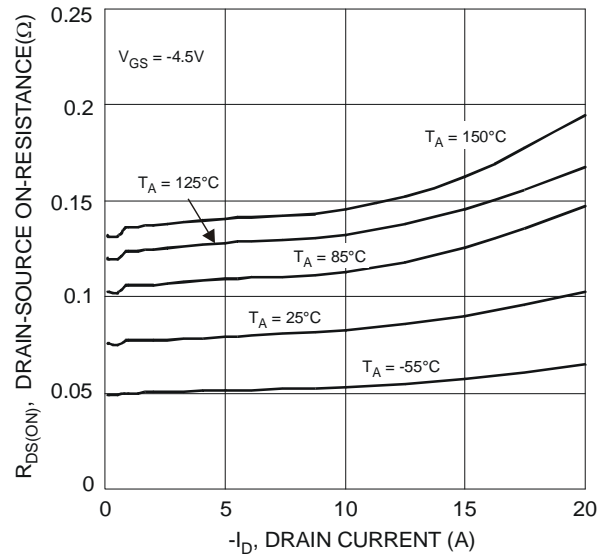


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

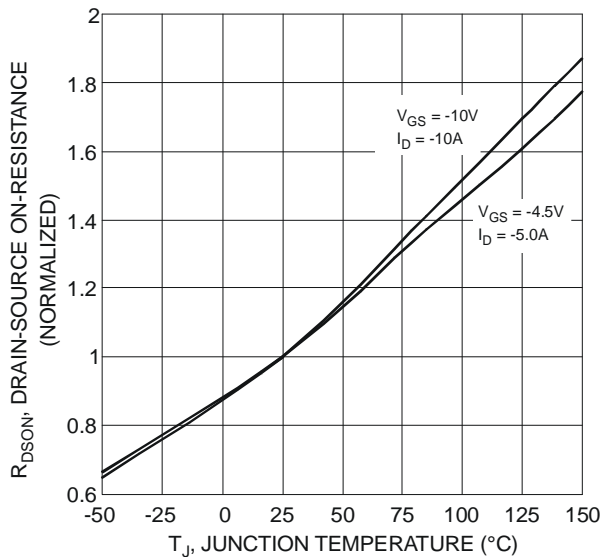


Figure 5 On-Resistance Variation with Temperature

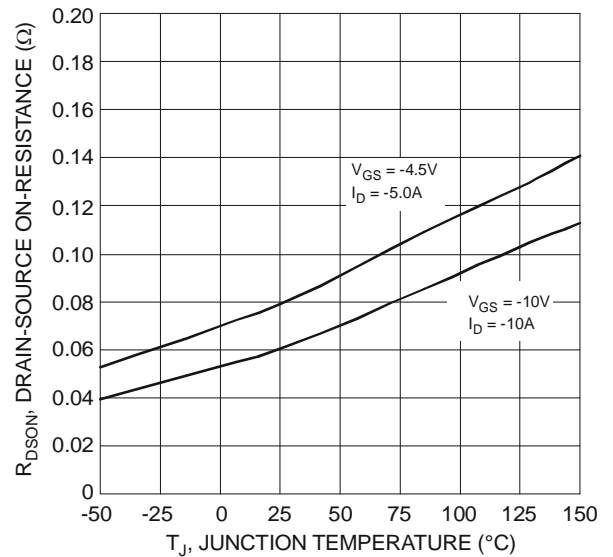


Figure 6 On-Resistance Variation with Temperature

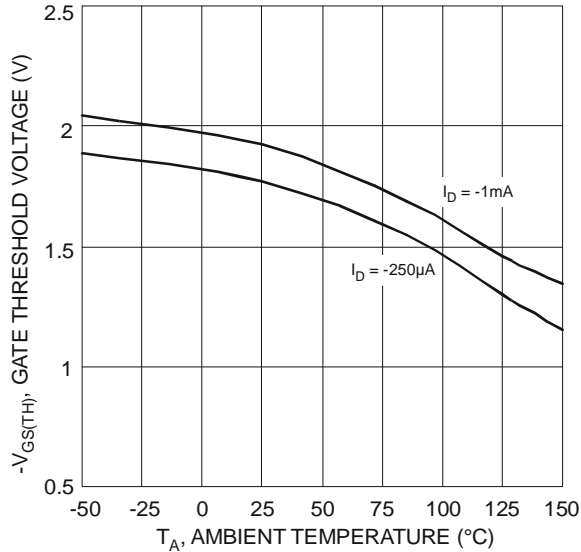


Figure 7 Gate Threshold Variation vs. Ambient Temperature

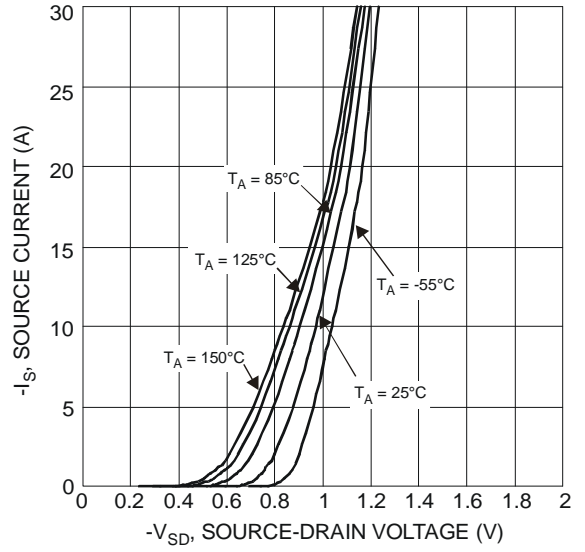


Figure 8 Diode Forward Voltage vs. Current

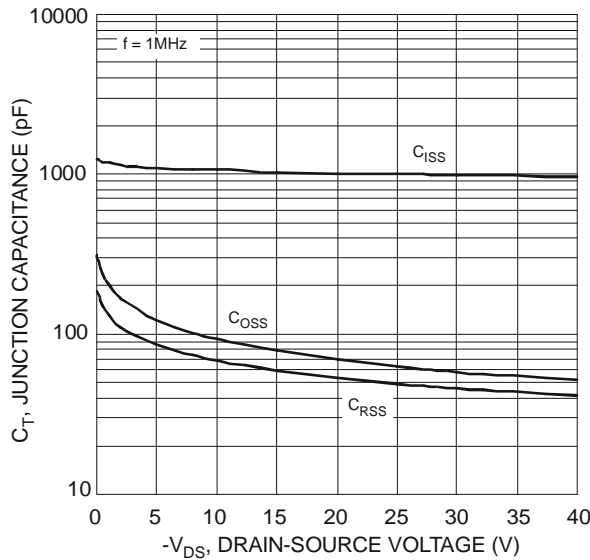


Figure 9 Typical Junction Capacitance

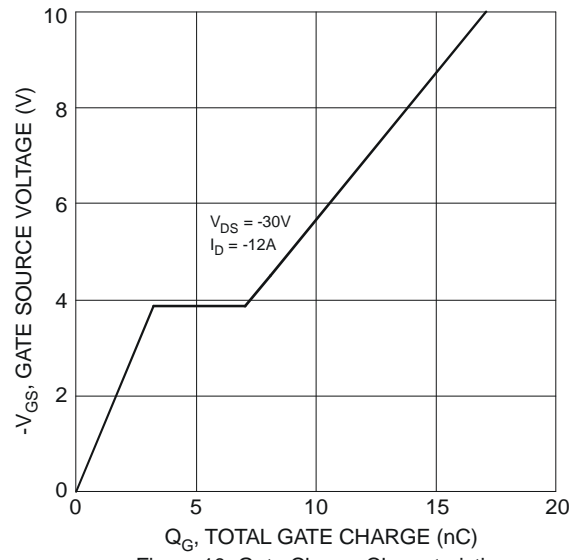


Figure 10 Gate Charge Characteristics

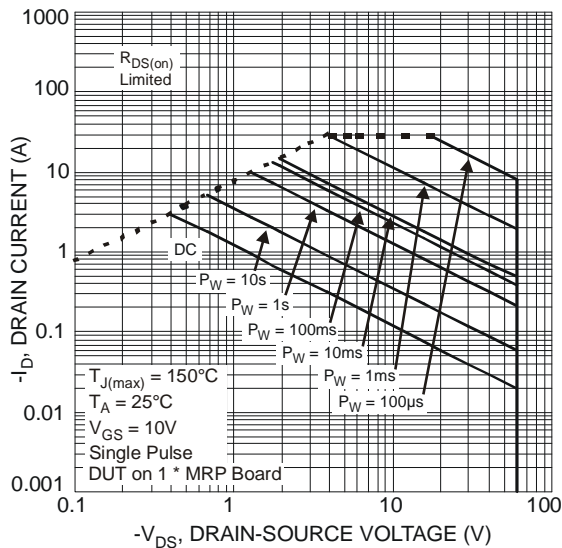
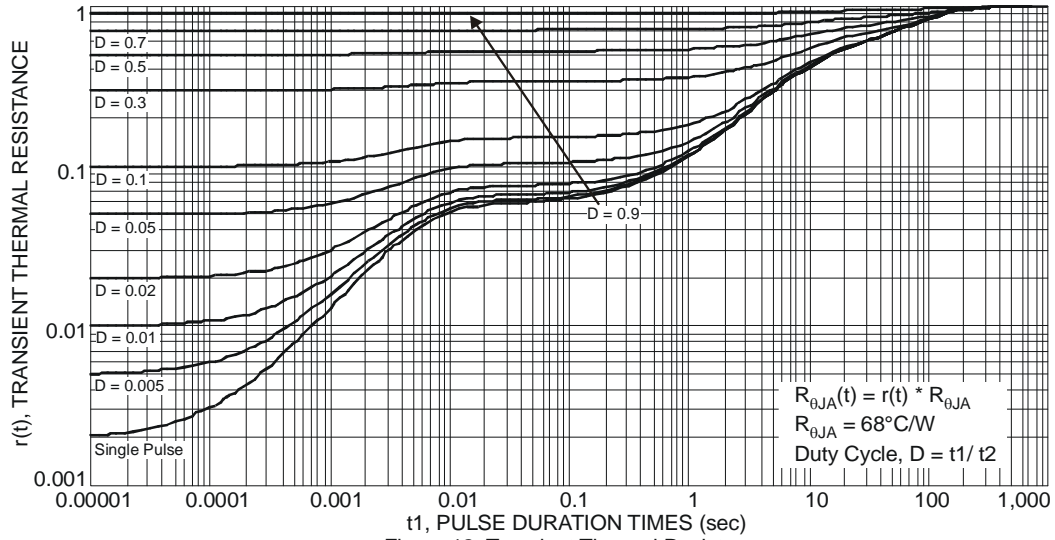
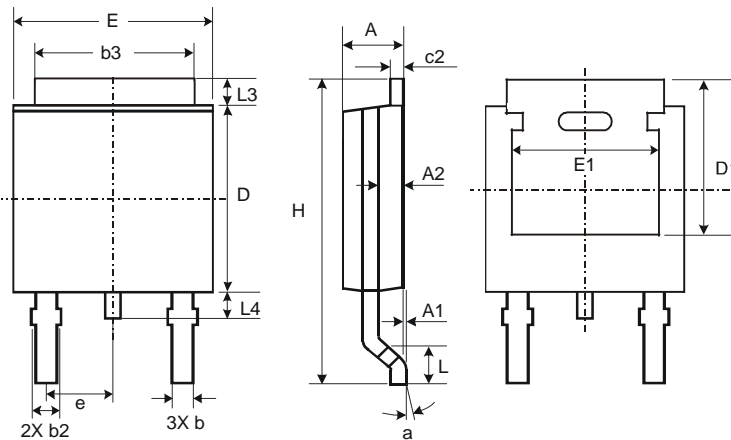


Figure 11 SOA, Safe Operation Area



**Package Outline Dimensions**

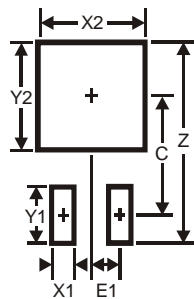
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



TO252			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c2	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

**Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
C	6.9
E1	2.3

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