

VIN = 5 to 39 V, VOUT = 1.2 to 35 V, 1-channel DC-DC Step down Regulator integrated N-channel Power MOSFET

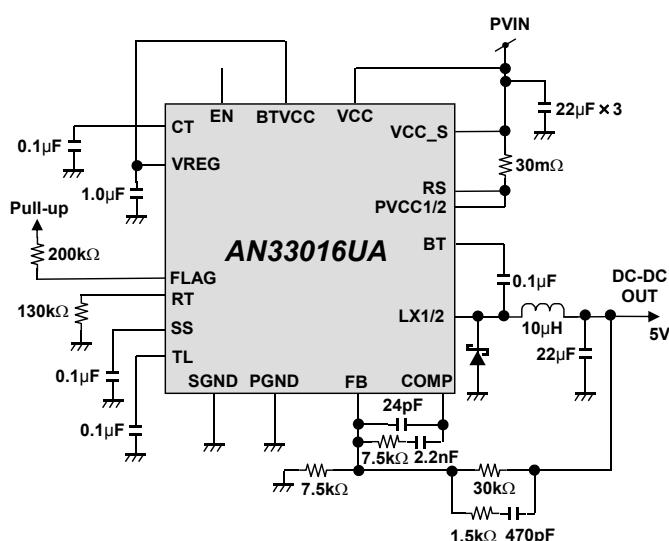
FEATURES

- 1-channel DC-DC Step Down Regulator Circuit that employs Voltage Mode Switching Control System
- Internal reference voltage is within $\pm 1\%$ accuracy
- Input Voltage Range : PVCC , VCC: 5 V ~ 39 V
- Adjustable Output Voltage Range with external Resistor : 1.2 V ~ 35V (Note)
- Adjustable Switching Frequency with external Resistor : 200 kHz ~ 2 MHz
- Standby mode consumes less than 1 μ A current
- Output over voltage protection function (OVP1)
- Input over voltage protection function (OVP2)
- Output ground short protection function
- Over current protection with adjustable threshold.
- Under voltage lockout function (VREG)
- Thermal Shut Down function
- Adjustable soft-start function
- SSOP024-P-0300F
(Size : 8.1 mm X 7.8 mm, 0.65 mm pitch),
24pin Plastic Shrink Small Outline Package (SSOP Type)

Note) This is value in F=490kHz condition .

Please refer Page 10.

SIMPLIFIED APPLICATION



DESCRIPTION

AN33016UA is 1-channel DC-DC Step down Regulator integrated N-channel Power MOSFET and employs the voltage mode switching control system.

This IC can be operated with wide input voltage range and is build in several protection functions, so this IC can provide high reliability power supply system.

Since it is possible to use up to 2 MHz switching frequency and it is unnecessary to add external parts for High Side Switch, this IC realizes downsizing of set and reducing in the number of external parts.

Output voltage is adjustable by user.

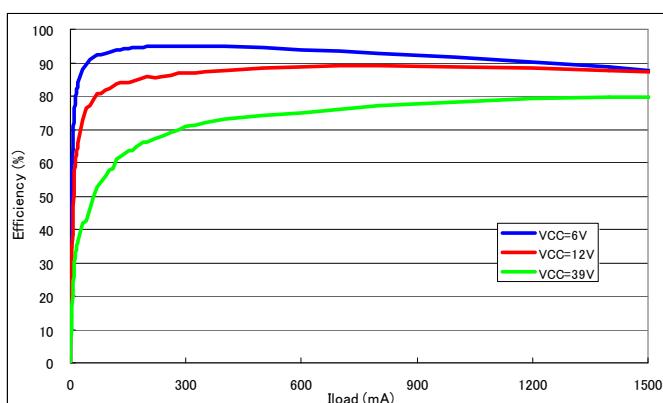
Maximum current is 1.5 A.

APPLICATIONS

High Input Voltage Power Systems such as

- Car-Audio system
- Car-Navigation system
- OA Equipment
- Home Appliances etc.

EFFICIENCY CURVE



Condition)

Vin = 6 , 12 , 39 V, Vout = 5 V,
Lo = 10 μ H, Co = 22 μ F, Frequency = 490 kHz

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Rating	Unit	Notes
Supply voltage	V _{CC}	60	V	*1
Operating free-air temperature	T _{opr}	– 40 to + 85	°C	*2
Operating junction temperature	T _j	– 40 to + 150	°C	*2
Storage temperature	T _{stg}	– 55 to + 150	°C	*2
Input Voltage Range	V _{CC_S} , V _{RS}	(V _{CC} -5.5) to (V _{CC} +0.3)	V	*1 *3
	V _{BT}	-0.3 to (V _{CC} +V _{REG})	V	*1 *4
	V _{EN}	-0.3 to (V _{CC} +0.3)	V	*1 *5
	V _{FB} , V _{BTVCC} , V _{TL_CTRL} , V _{SYNC} , V _{SS} , V _{RT} , V _{TL}	-0.3 to 5.5	V	*1
Output Voltage Range	V _{FLAG} , V _{COMP} , V _{CT}	-0.3 to 5.5	V	*1
	V _{LX1} , V _{LX2}	– 0.3 to (V _{CC} + 0.3)	V	*1 *5
ESD	HBM (Human Body Model)	2	kV	—
	MM (Machine Model)	200	V	—

Notes) Do not apply external currents and voltages to any pin not specifically mentioned.

This product may sustain permanent damage if subjected to conditions higher than the above stated absolute maximum rating.

This rating is the maximum rating and device operating at this range is not guaranteeable as it is higher than our stated recommended operating range. When subjected under the absolute maximum rating for a long time, the reliability of the product may be affected.

V_{CC} is voltage for V_{CC}, PV_{CC1} and PV_{CC2}. V_{CC} = PV_{CC1} = PV_{CC2}.

*1:The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

*2:Except for the power dissipation, operating ambient temperature, and storage temperature, all ratings are for T_a = 25 °C.

*3:(V_{CC} – 5.5) V must not be exceeded – 0.3 V, and (V_{CC} + 0.3) V must not be exceeded 60 V

*4:(V_{CC} + V_{REG}) V must not be exceeded 60 V

*5:(V_{CC} + 0.3) V must not be exceeded 60 V

POWER DISSIPATION RATING

PACKAGE	θ _{j-a}	θ _{j-c}	PD (T _a = 25 °C)	PD (T _a = 85 °C)	Notes
24 Pin Plastic Shrink Small Outline Package (SSOP Type)	135.1 °C / W	11.8 °C / W	0.925 W	0.481 W	*1

Note). For the actual usage, please refer to the PD-Ta characteristics diagram in the package specification, follow the power supply voltage, load and ambient temperature conditions to ensure that there is enough margin and the thermal design does not exceed the allowable value.

*1:Glass Epoxy Substrate(1 Layers) [Glass-Epoxy: 50 X 50 X 0.8t(mm)]

CAUTION

Although this has limited built-in ESD protection circuit, but permanent damage may occur on it. Therefore, proper ESD precautions are recommended to avoid electrostatic damage to the MOS gates

RECOMMENDED OPERATING CONDITIONS

Parameter	Pin Name	Min.	Typ.	Max.	Unit	Notes
Supply voltage range	V_{CC}	5	12	39	V	*1
	PV_{CC1}					
	PV_{CC2}					
Input Voltage Range	V_{CC_S}	$V_{CC} - 5.5$	—	$V_{CC} + 0.3$	V	*1
	V_{RS}					*2
	V_{BT}	— 0.3	—	$V_{CC} + V_{REG}$	V	*1 *3
	V_{EN}	— 0.3	—	$V_{CC} + 0.3$	V	*1 *4
	V_{FB}	— 0.3	—	5.5	V	*1
	$V_{BT_{VCC}}$					
	V_{TL_CTRL}					
	V_{SYNC}					
	V_{SS}					
	V_{RT}					
Output Voltage Range	V_{LX1}, V_{LX2}	— 0.3	—	$V_{CC} + 0.3$	V	*1 *4
	V_{FLAG}	— 0.3	—	5.5	V	*1
	V_{COMP}					
	V_{CT}					

Note) Do not apply external currents and voltages to any pin not specifically mentioned.

Voltage values, unless otherwise specified, are with respect to GND. GND is voltage for SGND, PGND. SGND = PGND
 V_{CC} is voltage for V_{CC} , PV_{CC1} and PV_{CC2} . $V_{CC} = PV_{CC1} = PV_{CC2}$.

*1: The values under the condition not exceeding the above absolute maximum ratings and the power dissipation.

*2: ($V_{CC} - 5.5$) V must not be exceeded — 0.3 V, and ($V_{CC} + 0.3$) V must not be exceeded 60 V

*3: ($V_{CC} + V_{REG}$) V must not be exceeded 60 V

*4: ($V_{CC} + 0.3$) V must not be exceeded 60 V

ELECTRICAL CHARACTERISTICS

$C_o = 22 \mu F$, $L_o = 10 \mu H$, V_{OUT} Setting = 5.0 V, $V_{CC} = 12 V$, $R_T = 130 k\Omega$,
 $T_a = 25^\circ C \pm 2^\circ C$ unless otherwise noted.

Parameter	Symbol	Condition	Limits			Unit	Note
			Min	Typ	Max		
Current Consumption							
Quiescent current	I_{CQ}	No switching FB = 1.1 V EN = High (3.3 V)	—	1.0	1.4	mA	—
Standby current	I_{STBY}	EN = Low	—	0.01	1	μA	—
BGR							
Feedback voltage	V_{REF}	—	0.99	1.0	1.01	V	—
Enable (EN)							
Low input threshold	V_{IL1}	—	—	—	0.4	V	—
High input threshold	V_{IH1}	—	2.0	—	—	V	—
EN terminal input current	V_{IC1}	EN = 3.3 V	—	33	70	μA	—
SYNCHRONIZATION (SYNC)							
Low input threshold	V_{IL2}	—	—	—	0.4	V	—
High input threshold	V_{IH2}	—	2.0	—	—	V	—
Oscillator							
Oscillator frequency	F_{OUT1}	$R_T = 130 k\Omega$	465	490	515	kHz	—
Oscillator frequency range	F_{OUT2}		200	—	2000	kHz	—
External sync frequency range	F_{SYNC}	$R_T = 130 k\Omega$ $F_{out1} = 490$ kHz	520	—	730	kHz	—
Over-current protection							
Over-current threshold voltage	V_{OCP}	(VCC_S-VRS)	66	75	84	mV	*1
Over-voltage protection							
Over-voltage threshold voltage for VFB	V_{OVP1}	For FB	1.04	1.12	1.20	V	—
Over-voltage threshold voltage for VCC	V_{OVP2}	For VCC	40	45	50	V	—
Internal regulator							
Internal regulator output voltage	V_{REG}	$C_{REG} = 1 \mu F$	4.5	4.9	5.3	V	—
GND short protection							
Short detection voltage	V_{SCP}	monitor FB	0.15	0.3	0.45	V	—

Note) *1 : This parameter is tested with DC measurement.

PIN CONFIGURATION

Top View

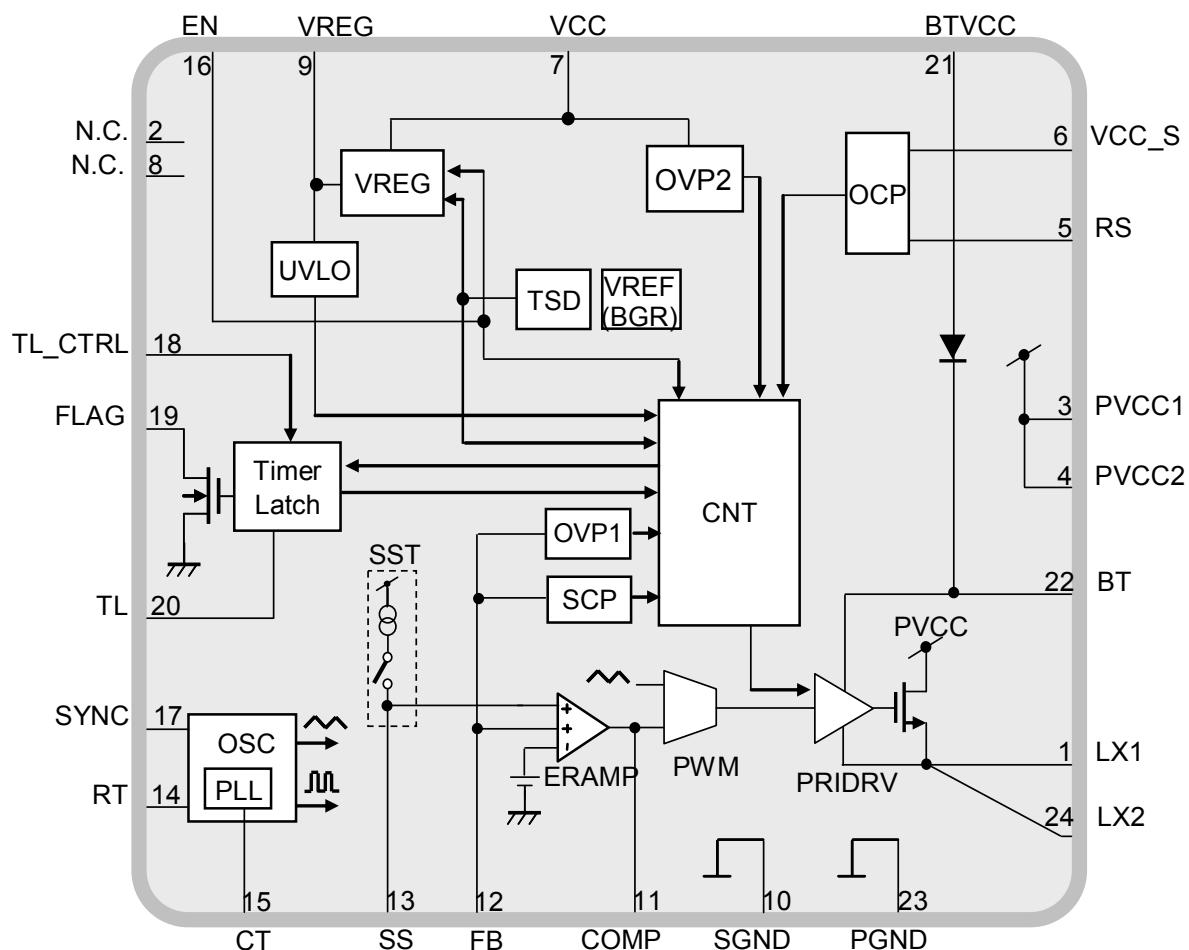
LX1	1	●	24	LX2
NC.	2		23	PGND
PVCC1	3		22	BT
PVCC2	4		21	BTVCC
RS	5		20	TL
VCC_S	6		19	FLAG
VCC	7		18	TL_CTRL
NC.	8		17	SYNC
VREG	9		16	EN
SGND	10		15	CT
COMP	11		14	RT
FB	12		13	SS

PIN FUNCTIONS

Pin No.	Pin name	Type	Description
1	LX1	Output	Connect to an external inductor and schottky diode
2	NC	—	No connection
3	PVCC1	Power Supply	Power supply pin for internal driver
4	PVCC2	Power Supply	Power supply pin for internal driver
5	RS	Input	Connection to current sensing port for OCP
6	VCC_S	Input	VCC pin connection to current sensing port for OCP
7	VCC	Power Supply	Power supply pin
8	NC	—	No connection
9	VREG	Output	Connect to an external capacitor for internal regulator
10	SGND	Ground	Ground pin
11	COMP	Output	Error amplifier output
12	FB	Input	Error amplifier negative input
13	SS	Input	Soft-start capacitor connection pin
14	RT	Input	Connect to an external resistor for adjustment of oscillation frequency
15	CT	Output	Low Pass filter function pin for PLL
16	EN	Input	Enable pin
17	SYNC	Input	External clock input for adjustment of oscillation frequency
18	TL_CTRL	Input	Connect to high to disable shut-down function by OCP/SCP
19	FLAG	Output	Error flag output pin
20	TL	Input	Connect to an external capacitor for adjustment of over-current detection time
21	BTVCC	Input	Boot strap input pin
22	BT	Input	Connect to an external capacitor for Boot strap
23	PGND	Ground	Power ground pin
24	LX2	Output	Connect to an external inductor and schottky diode

Notes) Concerning detail about pin description, please refer to OPERATION and APPLICATION INFORMATION section.

FUNCTIONAL BLOCK DIAGRAM



OPERATION

1. Power ON/OFF sequence

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

AN33016UA consists of one channel and it can be turned ON and OFF by using the EN pin.

EN > 2.0 V : ON
EN < 0.4 V : OFF

1. Power ON Sequence :

- VCC rises to a desired voltage level.
(A 10 us rise time or more is recommended to control and limit any abnormal current flow via the power transistor when VCC is rising.)
- Apply a voltage level of 2.0 V or higher at EN pin after VCC is steady and the DCDC will begin to operate.
(It is possible to connect the EN pin to VCC through a resistor, and, in that case, when VCC rises, DCDC will begin to operate.)
- When VREG voltage reaches 4.3 V and above, and after a delay time (charging time of the soft start capacitor decided by an external capacitor, the DCDC will start to operate.

2. Power OFF Sequence :

- To turn OFF the DCDC output, apply a voltage of 0.4 V or lower to EN pin.
– VOUT will drop after EN pin becomes Low.
(The discharge time is dependent on the applied load current and the feedback resistance connected at the output.)
- The DCDC will turn OFF if the VCC level becomes low even before EN pin becomes low.
The above scenario occurs when the VREG voltage decreases to 4.0 V or less.
(However, the DCDC output voltage will also decrease with VCC when the VCC level drops below a certain minimum level required to maintain the output voltage level.)

3. Points to take note of when re-starting the DCDC :

- Please allow a waiting time of 10 ms or more for the discharge time of the soft start capacitor when starting up the DCDC again after turning it OFF.
The output voltage might overshoot without the soft start function working properly if the DCDC is re-started immediately after it is turned OFF.

4. Points to take note of when shut down:

- Please apply a voltage of Low level or lower to EN PIN when turn OFF the DCDC.

OPERATION (Continued)

2. Start / Stop Control Timing Chart

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

- ON/OFF operation sequence by EN pin control.

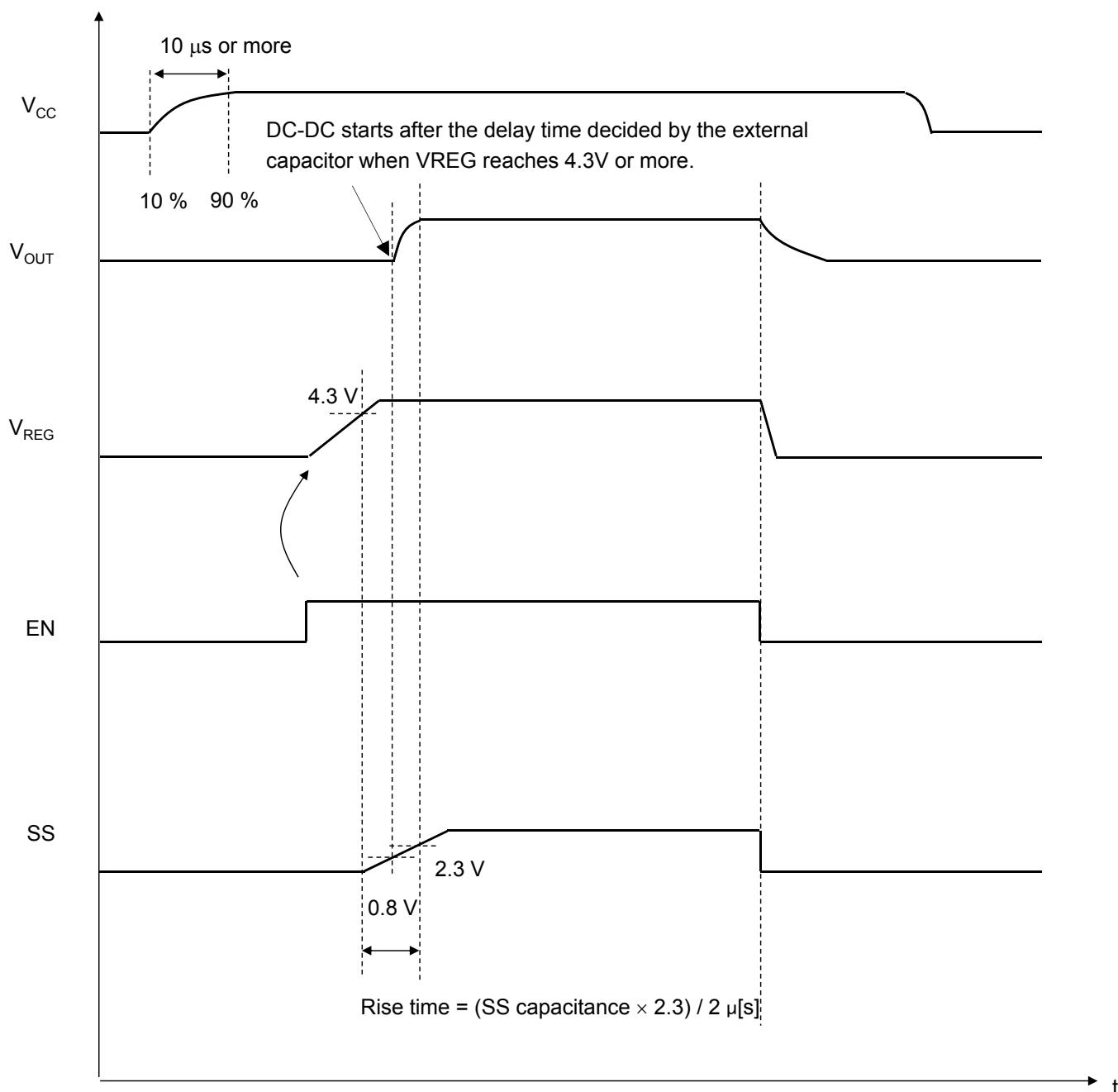


Figure : Power ON/OFF sequence by EN

Note) All values given in the above figure are typical values.

OPERATION (Continued)

2. Start / Stop Control Timing Chart

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

- ON/OFF operation sequence by VCC pin control (EN pin is connected to VCC).

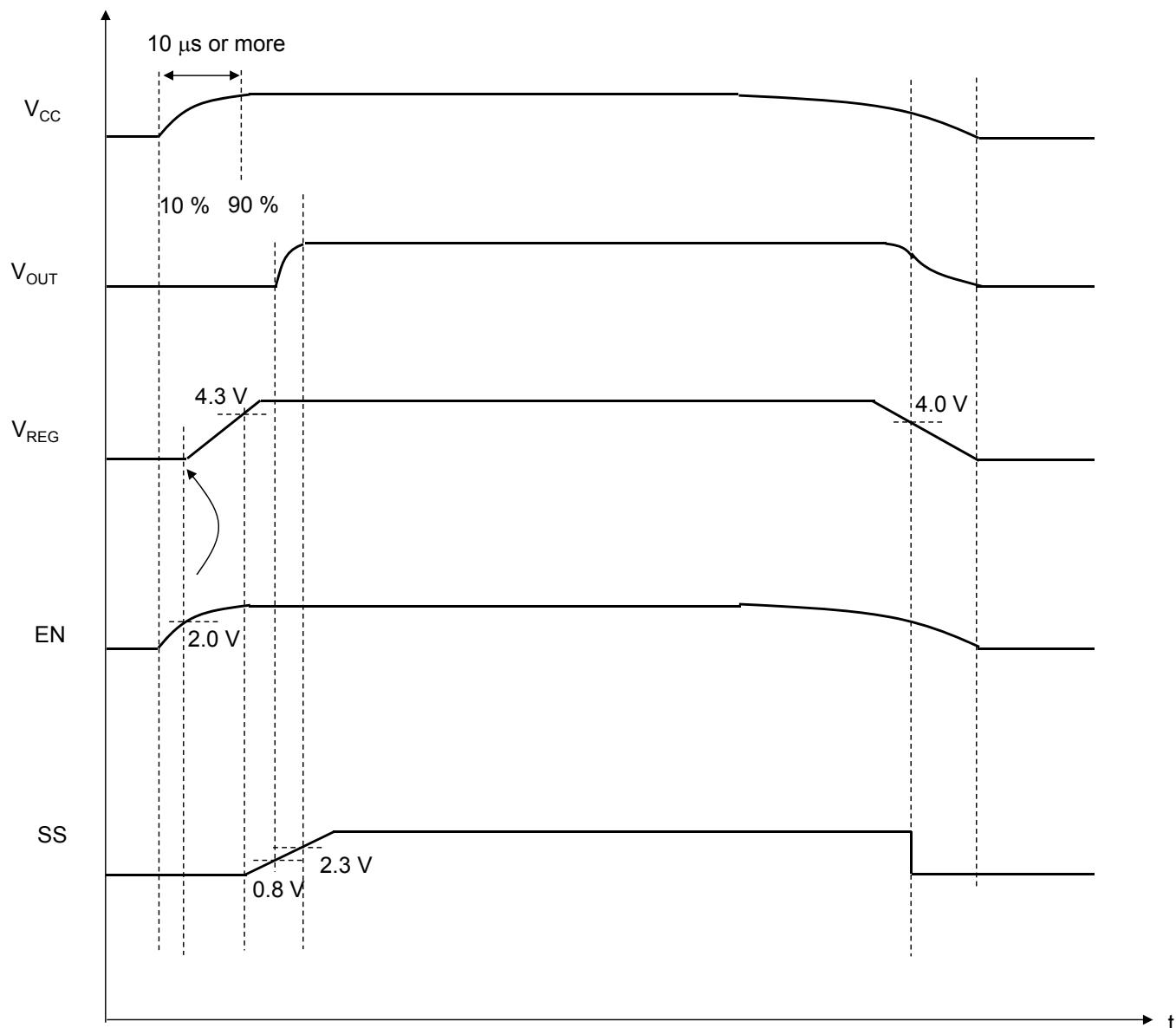


Figure : Power ON/OFF sequence by VCC/EN

Note) All values given in the above figure are typical values.

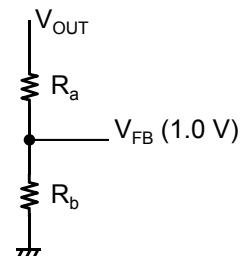
OPERATION (Continued)

3. Output voltage setting

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

The output voltage of AN33016UA is set with the external feedback resistance divider arranged between the terminal FB and GND between the DCDC output and the terminal FB. The output voltage is decided depending on the following equation.

$$V_{out} = 1.0 \times \left[1 + \frac{R_a}{R_b} \right]$$



4. Min/Max Duty Operation

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

MinDuty is decided by the minimum ON time. The minimum ON time, Ton (min), is the time that this IC can turn on the high side switch. This is decided by the gate capacitance of the high side switch and the internal timing delay. The following attention is necessary because in applications where the Input/Output voltage difference is great, and the switching frequency is high, there is a possibility to reach the limiting value of the minimum ON time. The minimum ON time, Ton (min), is about 200ns (max).

This IC has the function of MaxDuty, which will not exceed a certain definite value for safety reasons in case of abnormal circumstances. The following attention is necessary because in applications where the Input/Output voltage difference is close, and the switching frequency is high, there is a possibility to reach the limiting value of the minimum OFF time. The minimum OFF time, Toff (min), is about 200ns (max).

$$Ton(\text{min}) < \frac{V_{out}}{Vin} \times \frac{1}{F}$$

$$Toff(\text{min}) < \left(1 - \frac{V_{out}}{Vin}\right) \times \frac{1}{F}$$

$$V_{OUT}(\text{max}) = VIN \times (1 - 200\text{ns} \times F)$$

*) F : Switching frequency

When operating near the Min/Max limited duty, the ripple voltage and the inductor current ripple increases even if the output voltage is stabilized. It is recommended to use on the condition when the turn on switching time and turn off switching time are 200ns or more.

*) Please take note of the output voltage setting when the switching frequency is high.

OPERATION (Continued)

5. Oscillation Frequency

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

The switching frequency of the built-in oscillator circuit can be set from 200 kHz to 2000 kHz, determined by the resistance of the terminal RT, set by external resistance. The setting accuracy of the frequency is approximately $\pm 5\%$. The equation and the corresponding table are described as follows.

RT [K Ω]	OSC Frequency (KHz)	RT [K Ω]	OSC Frequency (KHz)
22	2067	91	675
24	1950	100	622
27	1789	110	572
30	1661	120	525
33	1550	130	490
36	1448	150	430
39	1365	160	404
43	1265	180	361
47	1176	200	329
51	1099	220	301
56	1024	240	272
62	943	270	248
68	873	300	225
75	799	330	205
82	737	360	187

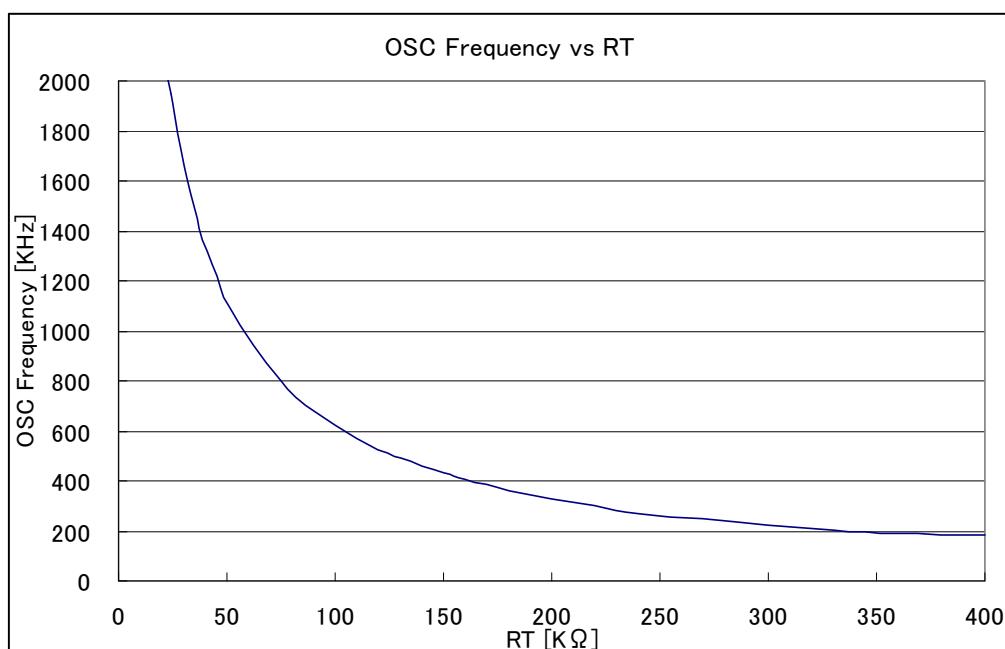


Figure : switching frequency vs external resistance

OPERATION (Continued)

6. Over-Current Protection

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

Over-current protection function (OCP) turns off the Power MOS in IC when the voltage difference of the external resistance R_{SENSE} exceeds 75mV(Typ.) to stop the supply of current from VCC to VOUT. Detection current I_{SENSE} can be calculated as:

$$I_{SENSE} = 75 \text{ mV} / R_{SENSE}$$

The over-current protection function operates via pulse-by-pulse control, and Duty is limited during the over-current protection period. Moreover, because the over-current detects the peak current that flows through R_{SENSE} , it is necessary to convert it from desired output current I_{OUT} value (Refer to Figure). Equation is shown below.

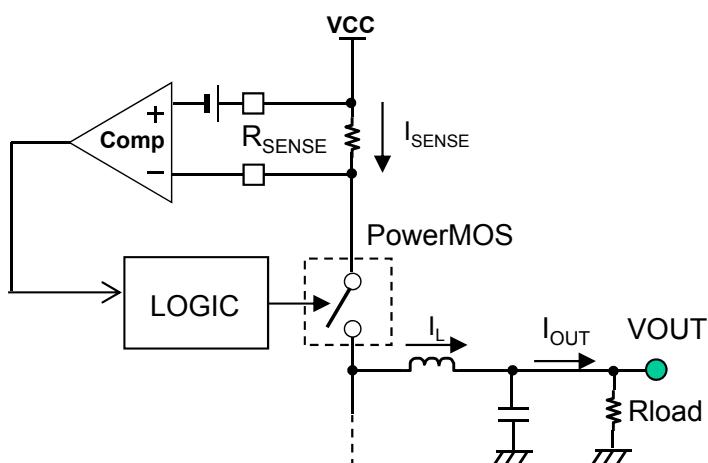


Figure : Over-Current Protection circuit block diagram

$$R_{SENSE} \approx \frac{0.075}{I_{OUT} + \frac{V_{OUT} \cdot (V_{CC} - V_{OUT})}{2 \cdot F \cdot L \cdot V_{CC}}}$$

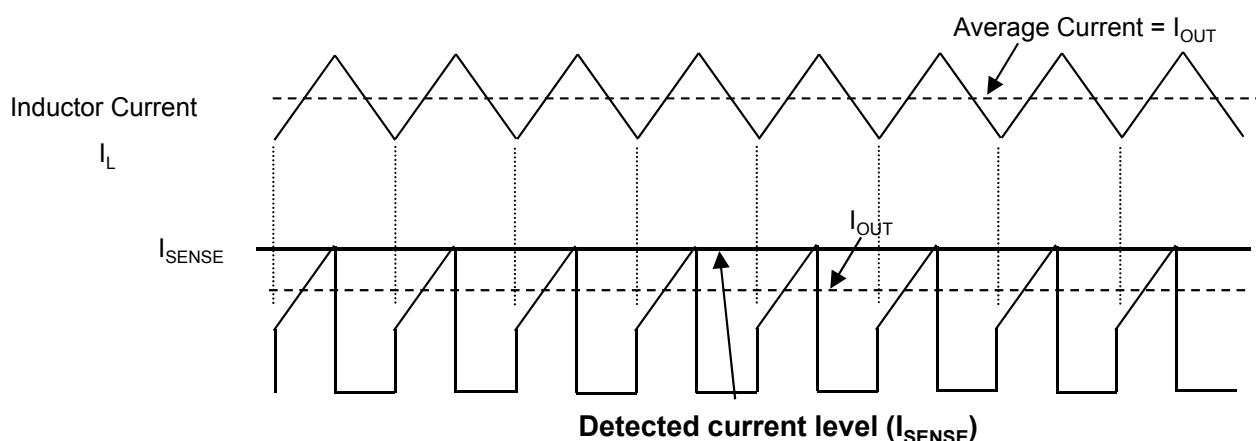


Figure : Method of detection of Over-Current Protection function

OPERATION (Continued)

7. FLAG function after Over current / Short current detection

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

When FB is equal or lower than 0.3 V, short current detection is triggered, FLAG pin will be pull to Low state. The response timing is determined by TL pin capacitor value CTL (The response timing = $CTL / 2\mu A \times 1.2V$).

Note that the pull down current drivability of FLAG pin is 1.2 mA.

Adjusting of FLAG response time in event of "over current" or "short current" condition is make possible by changing the capacitance of TL pin.

In the event of over current detection, FLAG pin will be pull to Low state. Once FLAG is pull to Low state, it will remained at Low state until IC is reset through EN pin.

8. Over Voltage detection

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

When FB is equal or higher than 1.22 V overvoltage detection is triggered, Power MOS will be off and LX will stop switching.

9. Thermal Shut Down (TSD)

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

When the LSI internal temperature becomes more than about 165°C, TSD operates and DC-DC turns off.

10. Input Over Voltage protection

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

When VCC is equal or higher than 45V overvoltage detection is triggered, Power MOS will be off and LX will stop switching.

OPERATION (Continued)

11. Soft Start Timing and Setting

Note) The characteristics listed below are reference values derived from the design of the IC and are not guaranteed.

This IC includes a soft start function whereby start-up time can be set to desired timing by adjusting the SS terminal capacitor (C_{ss}). By adjusting the soft-start time, rush current from the Power supply terminal can be limited and the start-up timing of the output voltage can be changed. The timing chart and the method of selecting the external capacitor value is shown in below.

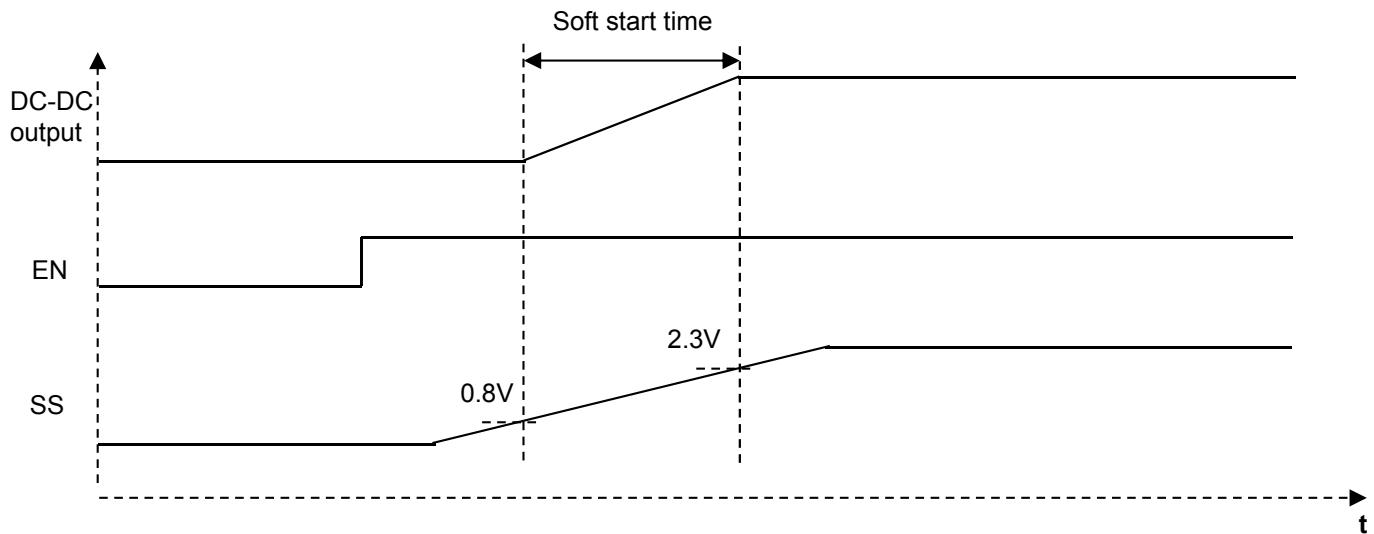


Figure : Soft start operation timing chart

$$\text{Equation to set soft start time by } C_{ss} : \text{Soft - Start Time (s)} \approx \frac{1.5}{2.2 \mu} \cdot C_{ss} \text{ } (\mu\text{F})$$

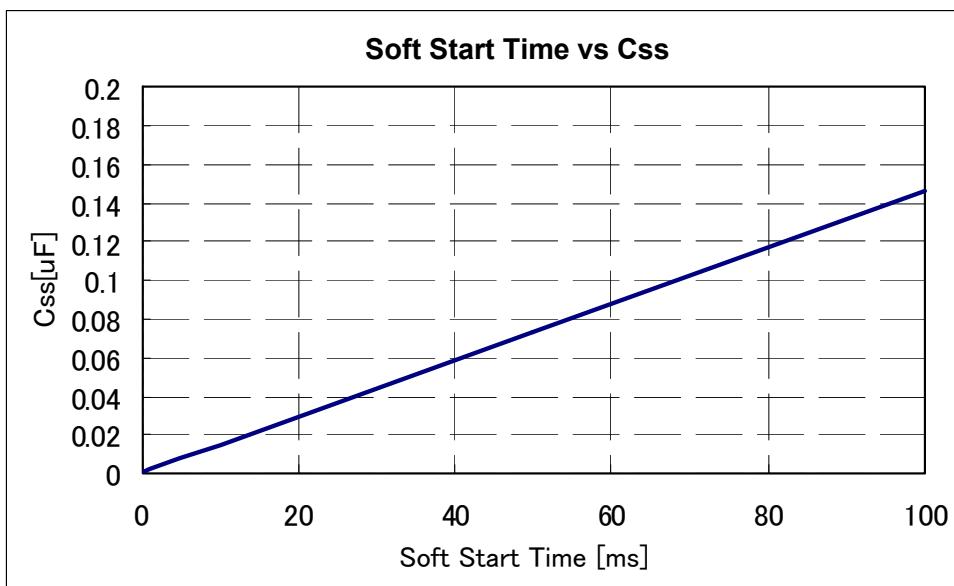


Figure : Soft Start Time vs C_{ss} Value (External Capacitor)

When this IC is used for power supply of communication with external equipment, the communication may not possible during the soft-start period. Depending on the type of external equipment connected, in some cases, when the DC-DC is turned on, even though the soft start maybe halfway through, the voltage level is high enough to enable the communication to start. In such cases, the micro-controller may not recognize the equipment normally and therefore it is required to set the micro-controller to access all possible equipment after the above soft-start setting time.

APPLICATIONS INFORMATION

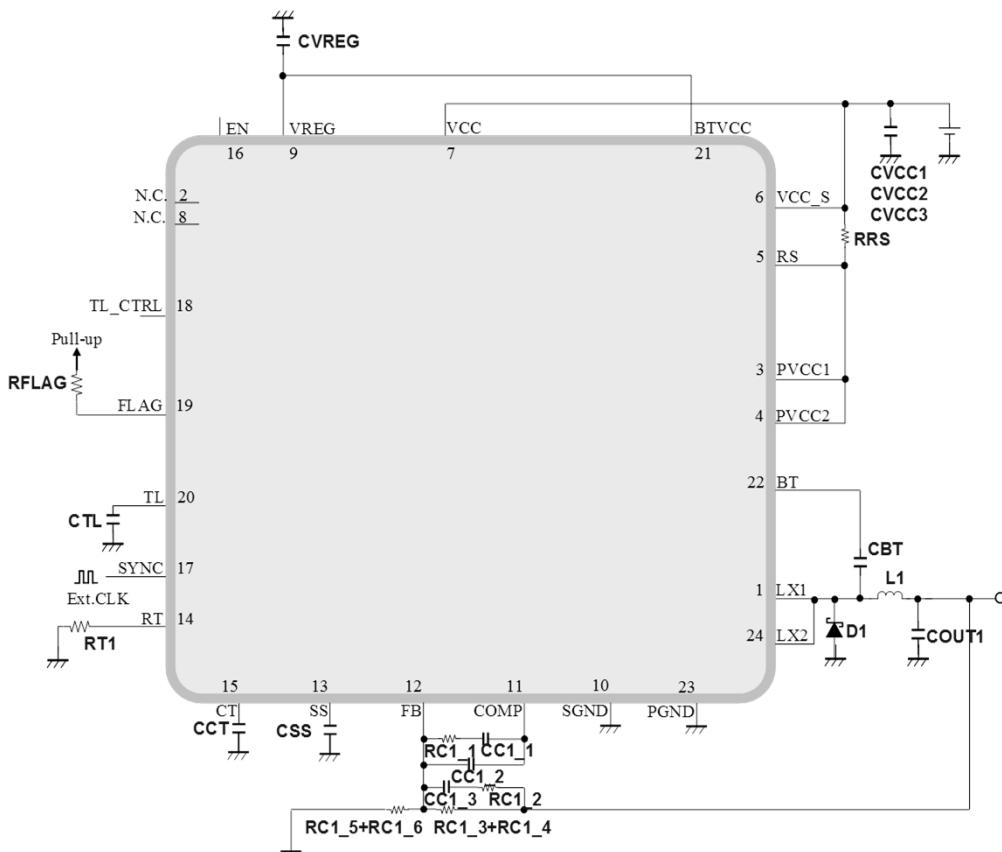


Figure : Application circuit

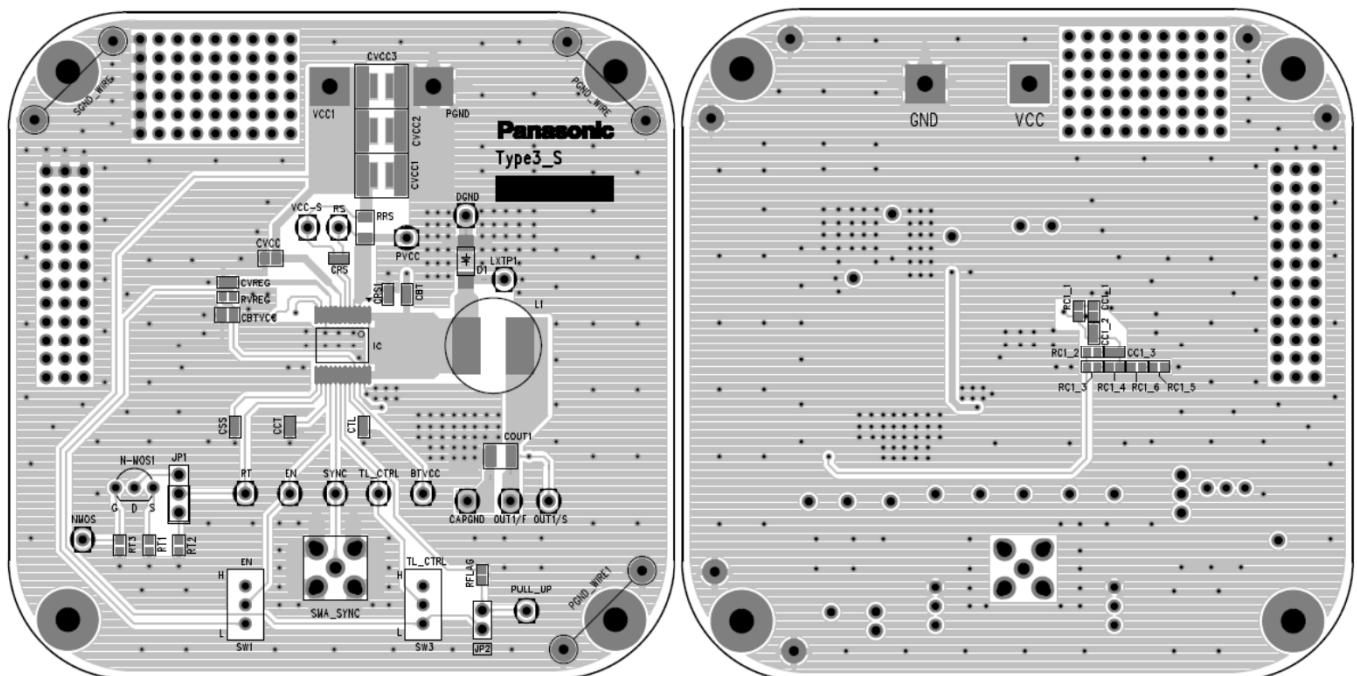


Figure : Top Layer with silk screen
(Top View) with Evaluation board

Figure : Bottom Layer with silk screen
(Bottom View) with Evaluation board

Notes) This application circuit and layout is an example. The operation of mass production set is not guaranteed. You should perform enough evaluation and verification on the design of mass production set. You are fully responsible for the incorporation of the above application circuit and information in the design of your equipment.

APPLICATIONS INFORMATION (Continued)

Reference	Part Name	Value	Maker	Description
CBT,CCT,CSS,CTL	GCM188R11C104KA01J	0.1μF	Murata	Setting Capacitor
CC1_1	GCM1882C1H222JA01J	2.2nF	Murata	Compensation Capacitor
CC1_2	GCM1882C1H240JA01J	24pF	Murata	Compensation Capacitor
CC1_3	GCM1882C1H471JA01J	470pF	Murata	Compensation Capacitor
CVREG	GCM188R71C105KA49J	1μF	Murata	VREG Capacitor
CVCC1,CVCC2, CVCC3	CKG57NX7R1H226MT	22μF	TDK	Input Capacitor
COUT1	TMK325C7226MM-T	22μF	TAIYOYUDEN	Output Capacitor
L1	CDRH8D43-100NC	10μH	SUMIDA	Inductor
IC1	AN33016UA	-	Panasonic	1ch DCDC Converter
D1	DB24416	-	Panasonic	Schottky Diode
RC1_1	ERA3AEB752V	R=7.5K	Panasonic	Compensation & Feedback Resistor
RC1_2	ERA3AEB152V	R=1.5K	Panasonic	Compensation & Feedback Resistor
RC1_3	ERJ3GEY0R00V	R=0	Panasonic	Compensation & Feedback Resistor
RC1_4	ERA3AEB303V	R=30K	Panasonic	Compensation & Feedback Resistor
RC1_5	ERJ3GEY0R00V	R=0	Panasonic	Compensation & Feedback Resistor
RC1_6	ERA3AEB752V	R=7.5K	Panasonic	Compensation & Feedback Resistor
RFLAG	ERA3AEB204V	R=200K	Panasonic	Pull-up Resistor
RT1	ERA3AEB134V	R=130K	Panasonic	OSC Setting Resistor
RRS	ERJ8BWFR030V	R=30m	Panasonic	OCP Sense Resistor

Figure : Recommended component

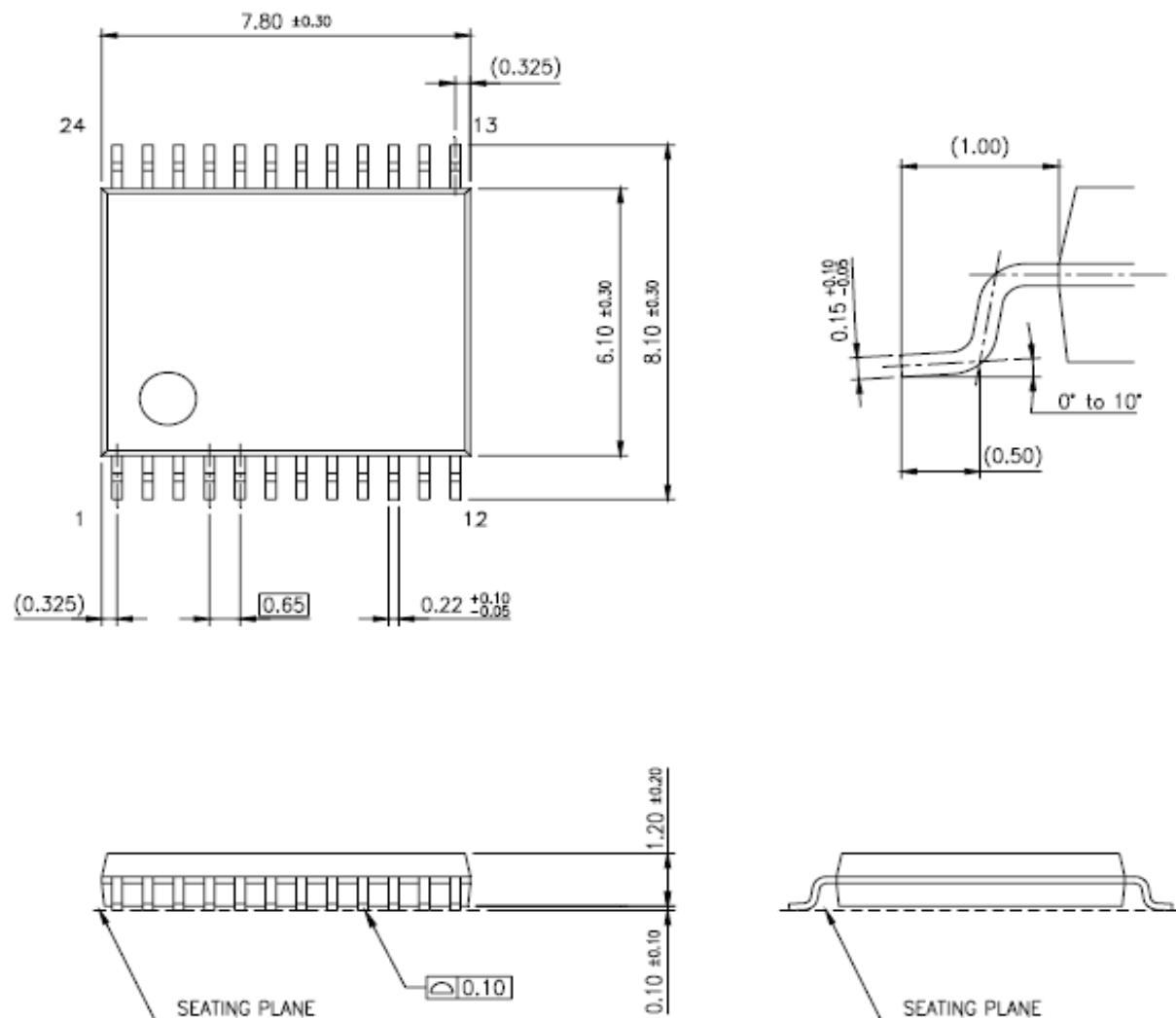
Note) Please set 0.1uF to SS pin and TL pin capacitor. If you change, please to become CTL>CSS × 0.9.

PACKAGE INFORMATION (Reference Data)

Outline Drawing

Unit : mm

Package Code : SSOP024-P-0300F



Body Material : Br/Sb Free Epoxy resin
Lead Material : Cu Alloy
Lead Finish Method : SnBi Plating

IMPORTANT NOTICE

1. When using the LSI for new models, verify the safety including the long-term reliability for each product.
2. When the application system is designed by using this LSI, please confirm the notes in this book.
Please read the notes to descriptions and the usage notes in the book.
3. This LSI is intended to be used for general electronic equipment.
Consult our sales staff in advance for information on the following applications: Special applications in which exceptional quality and reliability are required, or if the failure or malfunction of this LSI may directly jeopardize life or harm the human body.
Any applications other than the standard applications intended.
 - (1) Space appliance (such as artificial satellite, and rocket)
 - (2) Traffic control equipment (such as for airplane, train, and ship)
 - (3) Medical equipment for life support
 - (4) Submarine transponder
 - (5) Control equipment for power plant
 - (6) Disaster prevention and security device
 - (7) Weapon
 - (8) Others : Applications of which reliability equivalent to (1) to (7) is required

Our company shall not be held responsible for any damage incurred as a result of or in connection with the LSI being used for any special application, unless our company agrees to the use of such special application.

4. Please use this product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. Our company shall not be held responsible for any damage incurred as a result of our LSI being used by our customers, not complying with the applicable laws and regulations.
5. Pay attention to the direction of LSI. When mounting it in the wrong direction onto the PCB (printed-circuit-board), it might emit smoke or ignite.
6. Pay attention in the PCB (printed-circuit-board) pattern layout in order to prevent damage due to short circuit between pins. In addition, refer to the Pin Description for the pin configuration.
7. Perform a visual inspection on the PCB before applying power, otherwise damage might happen due to problems such as a solder-bridge between the pins of the semiconductor device. Also, perform a full technical verification on the assembly quality, because the same damage possibly can happen due to conductive substances, such as solder ball, that adhere to the LSI during transportation.
8. Take notice in the use of this product that it might be damaged or occasionally emit smoke when an abnormal state occurs such as output pin-VCC short (Power supply fault), output pin-GND short (Ground fault), or output-to-output-pin short (load short). Safety measures such as an installation of fuses are recommended because the extent of the above-mentioned damage and smoke emission will depend on the current capability of the power supply..
9. The protection circuit is for maintaining safety against abnormal operation. Therefore, the protection circuit should not work during normal operation.
Especially for the thermal protection circuit, if the area of safe operation or the absolute maximum rating is momentarily exceeded due to output pin to VCC short (Power supply fault), or output pin to GND short (Ground fault), the LSI might be damaged before the thermal protection circuit could operate.
10. Unless specified in the product specifications, make sure that negative voltage or excessive voltage are not applied to the pins because the device might be damaged, which could happen due to negative voltage or excessive voltage generated during the ON and OFF timing when the inductive load of a motor coil or actuator coils of optical pick-up is being driven.
11. Product which has specified ASO (Area of Safe Operation) should be operated in ASO
12. Verify the risks which might be caused by the malfunctions of external components.

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