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**MCP19215
Dual Boost/SEPIC
Evaluation Board
User's Guide**

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Object of Declaration: MCP19215 Dual Boost/SEPIC Evaluation Board

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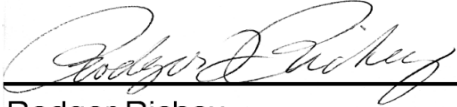
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Rodger Richey
Director of Development Tools



Date

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Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our web site (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a “DS” number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is “DSXXXXXXXXA”, where “XXXXXXXX” is the document number and “A” is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the MCP19215 Dual Boost/SEPIC Evaluation Board. Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Recommended Reading
- The Microchip Web site
- Development Systems Customer Change Notification Service
- Customer Support
- Document Revision History

DOCUMENT LAYOUT

This document describes how to use the MCP19215 Dual Boost/SEPIC Evaluation Board as a development tool to emulate and debug firmware on a target board. The manual layout is as follows:

- **Chapter 1. “Product Overview”** – Includes a short overview of the MCP19215 device, and a general description of the MCP19215 Dual Boost/SEPIC Evaluation Board.
- **Chapter 2. “Installation and Operation”** – Includes instructions on how to get started with the MCP19215 Dual Boost/SEPIC Evaluation Board, a detailed description of the board, and the typical waveforms that are obtained from the board.
- **Appendix A. “Schematic and Layouts”** – Shows the schematic and layout diagrams for the MCP19215 Dual Boost/SEPIC Evaluation Board.
- **Appendix B. “Bill of Materials (BOM)”** – Lists the parts used to build the MCP19215 Dual Boost/SEPIC Evaluation Board.

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CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	<i>MPLAB® IDE User's Guide</i>
	Emphasized text	...is the <i>only</i> compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	<u><i>File>Save</i></u>
Bold characters	A dialog button	Click OK
	A tab	Click the Power tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	#define START
	Filenames	autoexec.bat
	File paths	c:\mcc18\h
	Keywords	_asm, _endasm, static
	Command-line options	-Opa+, -Opa-
	Bit values	0, 1
	Constants	0xFF, 'A'
Italic Courier New	A variable argument	<i>file.o</i> , where <i>file</i> can be any valid filename
Square brackets []	Optional arguments	mcc18 [options] <i>file</i> [options]
Curly brackets and pipe character: { }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}
Ellipses...	Replaces repeated text	var_name [, var_name...]
	Represents code supplied by user	void main (void) { ... }

RECOMMENDED READING

This user's guide describes how to use the MCP19215 Dual Boost/SEPIC Evaluation Board. Other useful documents are listed below. The following Microchip documents are available and recommended as supplemental reference resources:

- **MCP19214/5 – “Digitally-Enhanced Power Analog, Dual Channel, Low-Side PWM Controller” (DS20005681)** – This data sheet provides detailed information regarding the MCP19214/5 devices.
- **MCP19214/5 – “Monitoring Graphical Interface User’s Guide” (DS50002557)** – This user’s guide includes information on how to install, understand, and use the monitoring and calibration tool for the MCP19214/5 devices.

THE MICROCHIP WEB SITE

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- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip consultant program member listing
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The Development Systems product group categories are:

- **Compilers** – The latest information on Microchip C compilers, assemblers, linkers and other language tools. These include all MPLAB C compilers; all MPLAB assemblers (including MPASM assembler); all MPLAB linkers (including MPLINK object linker); and all MPLAB librarians (including MPLIB object librarian).
- **Emulators** – The latest information on Microchip in-circuit emulators. This includes the MPLAB REAL ICE™ and MPLAB ICE 2000 in-circuit emulators.
- **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debuggers. This includes MPLAB ICD 3 in-circuit debuggers and PICKit 3 Debug Express.
- **MPLAB IDE** – The latest information on Microchip MPLAB IDE, the Windows Integrated Development Environment for development systems tools. This list is focused on the MPLAB IDE, MPLAB IDE Project Manager, MPLAB Editor and MPLAB SIM simulator, as well as general editing and debugging features.
- **Programmers** – The latest information on Microchip programmers. These include production programmers such as MPLAB REAL ICE in-circuit emulator, MPLAB ICD 3 in-circuit debugger and MPLAB PM3 device programmers. Also included

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are nonproduction development programmers such as PICSTART Plus and PICKit 2 and 3 programmers.

CUSTOMER SUPPORT

Users of Microchip products can receive assistance through several channels:

- Distributor or Representative
- Local Sales Office
- Field Application Engineer (FAE)
- Technical Support

Customers should contact their distributor, representative or field application engineer (FAE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the web site at:

<http://www.microchip.com/support>.

DOCUMENT REVISION HISTORY

Revision A (March 2017)

- Initial Release of this Document.

Revision B (August 2017)

- Corrected typographical errors.

Chapter 1. Product Overview

1.1 INTRODUCTION

This chapter provides an overview of the MCP19215 Dual Boost/SEPIC Evaluation Board and covers the following topics:

- [MCP19215 Device – Short Overview](#)
- [What Is the MCP19215 Dual Boost/SEPIC Evaluation Board?](#)
- [What Does the MCP19215 Dual Boost/SEPIC Evaluation Board Kit Include?](#)

1.2 MCP19215 DEVICE – SHORT OVERVIEW

The MCP19215 is a highly-integrated, digitally-enhanced Pulse Width Modulation (PWM) controller used for low-side switch PWM applications such as battery chargers, bidirectional converters and LED lighting systems.

Since the MCP19215 uses traditional analog control circuitry to regulate the output of the DC/DC converter, the integration of the PIC[®] controller mid-range core is used to provide complete customization of device operating parameters, start-up and shut-down profiles, protection levels and fault handling procedures.

The MCP19215 is designed to efficiently operate from a single 4.5V-42V supply. It features integrated synchronous drivers, a bootstrap device, an internal linear regulator and 8K word nonvolatile memory, all in a space-saving 32-pin, 5 x 5 mm QFN package.

After initial device configuration by using Microchip's MPLAB[®] X Integrated Development Environment (IDE) software, I²C communication can be used by a host to communicate with or modify the operation of the MCP19215.

An internal 5V rail provides power to the PIC controller, and is also present on the V_{DD} pin. It is recommended that a minimum 4.7 μF capacitor be placed between V_{DD} and P_{GND}. The V_{DD} pin may also be directly connected to the V_{DR} pin or connected through a low-pass RC filter. The V_{DR} pin provides power to the internal synchronous driver.

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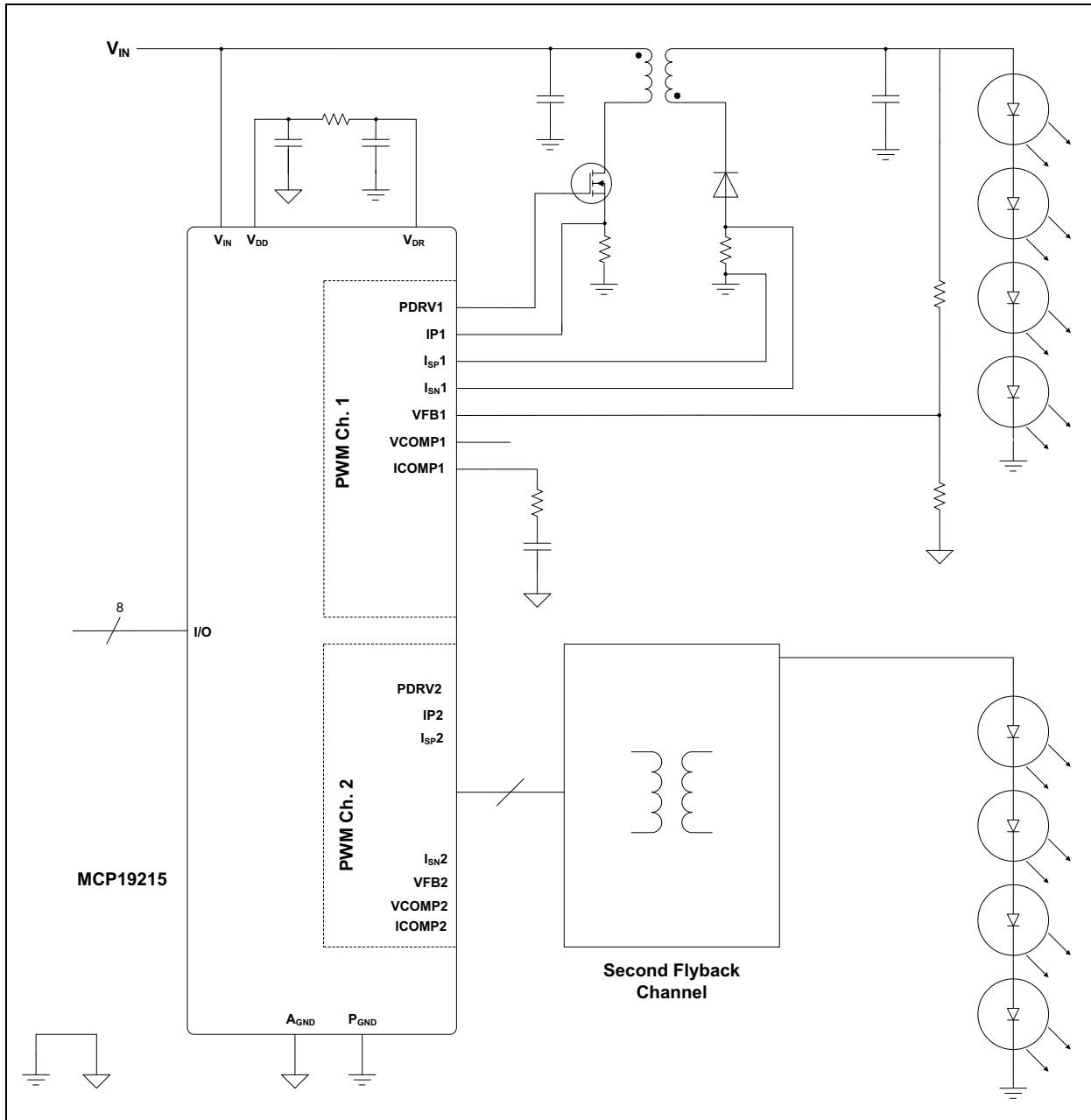


FIGURE 1-1: MCP19215 Typical Application Circuit.

1.3 WHAT IS THE MCP19215 DUAL BOOST/SEPIC EVALUATION BOARD?

The MCP19215 Dual Boost/SEPIC Evaluation Board demonstrates how the MCP19215 device operates in Boost and SEPIC topologies over a wide input voltage and load range. Nearly all operational and control system parameters are programmable by utilizing the integrated PIC controller.

For precise measurements of output current, a 0.1Ω shunt is used on each channel. Output current is measured using an internal op amp.

The Graphical User Interface (GUI) can be used to program the functioning parameters, and check operational status. To simplify the connection, a USB-to-I²C communication bridge is implemented on board, using the MCP2221-I/SL SOIC 14-Lead, allowing standard interface to any Microsoft® Windows® computer.

Alternatively, users can program the MCP19215 using their own firmware, further tailoring it to the application, using the MPLAB[®] X Integrated Development Environment (IDE) and a PICkit[™] 3 Programmer/Debugger on the J5 Header (see [Figure 2-1](#)).

The MCP19215 Dual Boost/SEPIC Evaluation Board board is equipped with headers for In-Circuit Serial Programming[™] (ICSP[™]), a mini-USB type B for communication, and test point pads on each GPIO pin.

The MCP19215 Dual Boost/SEPIC Evaluation Board is also intended to demonstrate an optimized Printed Circuit Board (PCB) layout that minimizes parasitic noise while increasing efficiency and power density. Proper PCB layout is critical to achieve optimum MCP19215 operation, as well as power train efficiency and noise minimization.

The MPLAB X (IDE), the MCP19215 monitoring graphical user interface, and example firmware are available for download from the Microchip web site, at www.microchip.com.

1.4 WHAT DOES THE MCP19215 DUAL BOOST/SEPIC EVALUATION BOARD KIT INCLUDE?

This MCP19215 Dual Boost/SEPIC Evaluation Board kit includes:

- The MCP19215 Dual Boost/SEPIC Evaluation Board (ADM00799)
- USB A to Mini-USB Type B Cable
- Important Information Sheet

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Chapter 2. Installation and Operation

2.1 INTRODUCTION

This chapter provides a detailed description of the MCP19215 Dual Boost/SEPIC Evaluation Board, instructions on how to begin using the board, and lists the typical waveforms obtained from the board.

The chapter is laid out as follows:

- [MCP19215 Dual Boost/SEPIC Evaluation Board Features](#)
- [Getting Started](#)
- [Setup Procedure](#)
- [Operating the MCP19215 Dual Boost/SEPIC Evaluation Board](#)
- [Typical Performance Data and Waveforms](#)

2.2 MCP19215 DUAL BOOST/SEPIC EVALUATION BOARD FEATURES

The MCP19215 Dual Boost/SEPIC Evaluation Board was developed to provide a compact, low-cost and highly-efficient DC/DC conversion for low-to-medium output currents.

The key features of the MCP19215 Dual Boost/SEPIC Evaluation Board include:

- input voltage range: 8V-5V
- output voltage: 20V for both channels (can be software adjusted from input voltage + 1V-4V for channel 1, and 9V-24V for channel 2)
- maximum output current: 1A (can be software adjusted from 0A to 1A; the board default setting is 0.2A)
- 300 kHz switching frequency (can be software adjusted from 100 kHz to 2 MHz)
- on-board high performance power MOSFET transistors
- overcurrent and overvoltage protection
- status report (including errors, input voltage, output voltage and current) via I²C communication
- under voltage lockout (UVLO) with programmable thresholds (via software)

2.3 GETTING STARTED

The MCP19215 Dual Boost/SEPIC Evaluation Board is fully assembled and tested to evaluate and demonstrate the MCP19215 device capabilities.

2.3.1 Necessary Instruments and Tools Required for Operation

- Adjustable DC power supply with 0V-5V/2 A_{DC} range output capability
- Electronic load with at least 2A current capability and load stepping capability
- Digital oscilloscope with a minimum bandwidth of 50 MHz
- Digital voltmeter/ammeter
- Wires for connections; these wires must sustain high currents:
 - 4A for the connection between the adjustable DC power supply and board
 - 2A for the connection between the board and the electronic load

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2.4 SETUP PROCEDURE

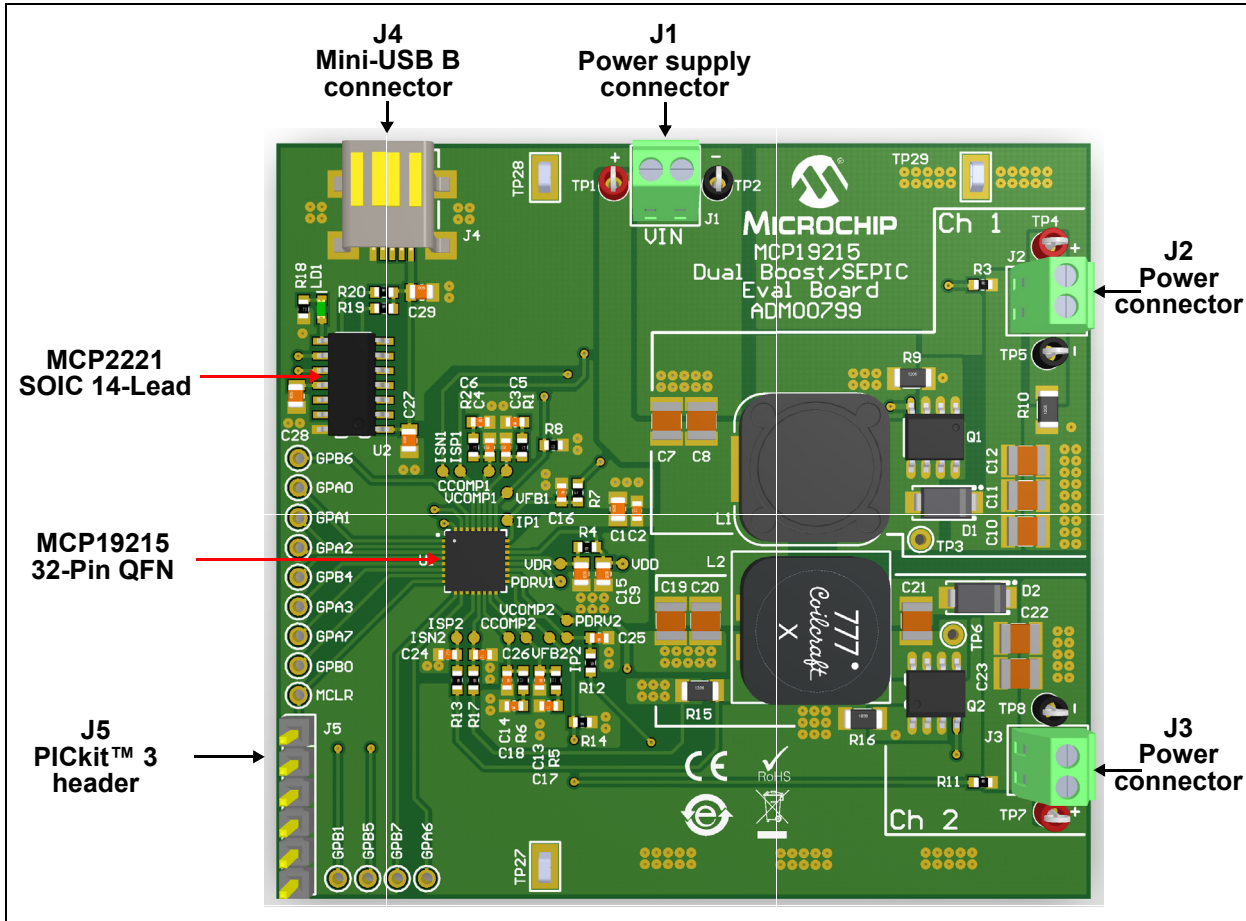


FIGURE 2-1: MCP19215 Dual Boost/SEPIC Evaluation Board – Top View.

To power up the MCP19215 Dual Boost/SEPIC Evaluation Board, the following steps must be completed:

1. connect the electronic load to the J2 and/or J3 connector on the board by observing the polarity; the positive (+) and negative (-) connector pins are marked on the board silkscreen (refer to [Figure 2-1](#)).
2. connect the adjustable DC power supply to the J1 connector on the board by matching the polarity to the positive (+) and negative (-) connector pins marked on the board silkscreen (refer to [Figure 2-1](#)).
3. set the DC voltage supplied by the adjustable DC power supply to 12V.

- connect the board to a PC (on which the MCP19214/5 Monitoring GUI is installed) via the USB-A to mini-USB type B cable supplied, by using the J4 connector on the board.

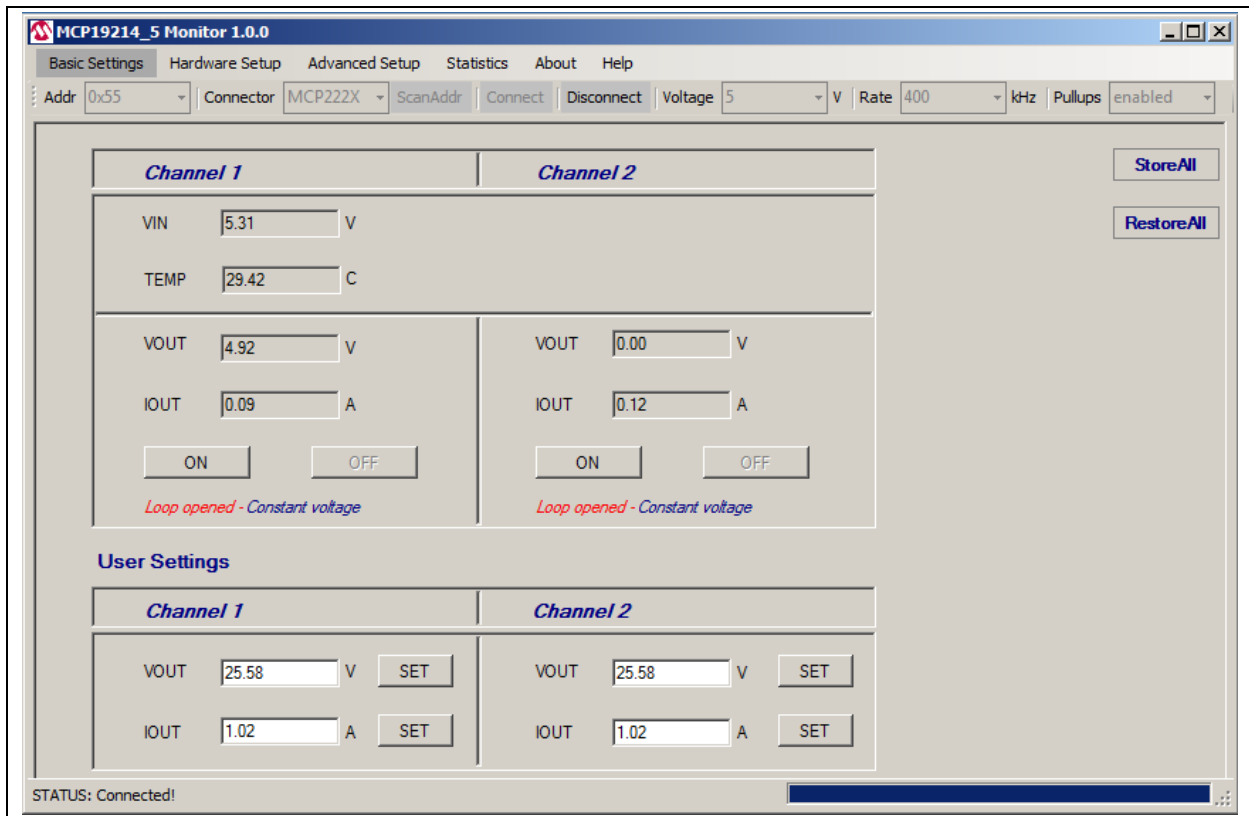


FIGURE 2-2: MCP19214/5 Monitoring GUI.

- after powering up, the output voltage is automatically enabled on for both channels. Alternatively, the output voltage may be enabled or disabled individually for each channel by using the **ON** and **OFF** buttons in the GUI.
- the board is factory set to deliver 20V at 1A maximum, with the loop adjusted for optimum performance, and current measurement performed through the internal op amp.
If different settings are desired, changes may be performed in two ways:
 - through the I²C-to-USB on-board interface. Refer to the MCP19214/5 Monitoring GUI User Guide for details.
 - by the user's own software that can be loaded into the MCP19215 by connecting J5 on the board to a PICKit™3 or another suitable programming tool (see [Figure 2-1](#)).

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2.5 OPERATING THE MCP19215 DUAL BOOST/SEPIC EVALUATION BOARD

Figure 2-3 shows the board typical test setup.

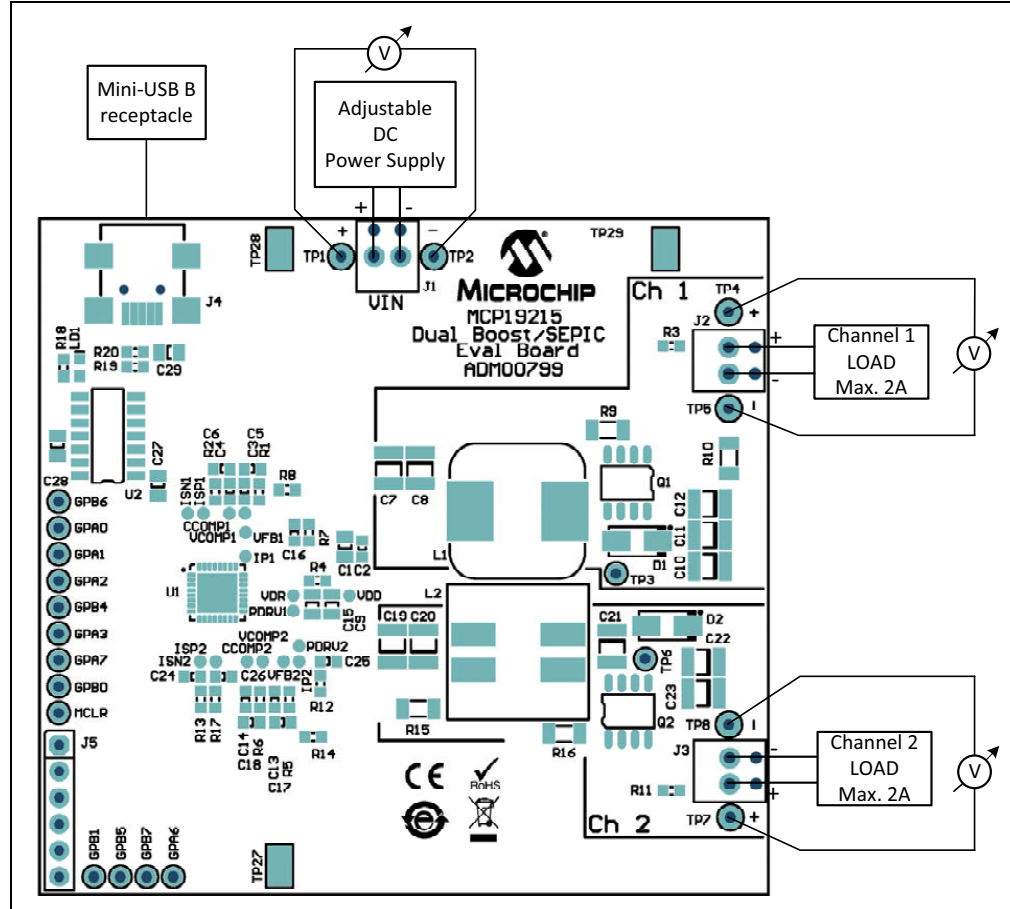


FIGURE 2-3: MCP19215 Dual Boost/SEPIC Evaluation Board – Typical Test Setup.

Table 2-1 lists all the available test points on the board.

The user can connect various instruments to the listed test points to evaluate the parameters of the converter. The typical performance data, curves and waveforms are presented in [Section 2.6](#).

TABLE 2-1: TEST POINTS DESCRIPTION

Label	Function	Description
TP1_GND TP2_GND TP3_GND	GND	Power GND
TP1	$V_{IN (+)}$	Input Voltage (+)
TP2	$V_{IN (-)}$	Input Voltage (-)
TP3	SW1	Switching Node for Channel 1
TP4	$V_{OUT1 (+)}$	Output Voltage (+) for Channel 1
TP5	$V_{OUT1 (-)}$	Output Voltage (-) for Channel 1
TP6	SW2	Switching Node for Channel 2
TP7	$V_{OUT2 (+)}$	Output Voltage (+) for Channel 2

Note 1: Refer to the MCP19214/5 data sheet, for a detailed description of the port pin functions.

TABLE 2-1: TEST POINTS DESCRIPTION (CONTINUED)

Label	Function	Description
TP8	$V_{OUT2(-)}$	Output Voltage (-) for Channel 2
PDRV1	PDRV1	Driver Signal for Channel 1
ISN1	I_{SN1}	Output Current Sense Measurement (-) for Channel 1
ISP1	I_{SP1}	Output Current Sense Current Measurement (+) for Channel 1
CCOMP1	CCOMP1	Current Loop Compensation Circuitry for Channel 1
VCOMP1	VCOMP1	Voltage Loop Compensation Circuitry for Channel 1
VFB1	VFB1	Voltage Feedback for Channel 1
IP1	IP1	Primary Inductor Current Sense Input for Channel 1
PDRV2	PDRV2	Driver Signal for Channel 2
ISN2	I_{SN2}	Output Current Sense Measurement (-) for Channel 2
ISP2	I_{SP2}	Output Current Sense Current Measurement (+) for Channel 2
CCOMP2	CCOMP2	Current Loop Compensation Circuitry for Channel 2
VCOMP2	VCOMP2	Voltage Loop Compensation Circuitry for Channel 2
VFB2	VFB2	Voltage Feedback for Channel 2
IP2	IP2	Primary Inductor Current Sense Input for Channel 2
VDD	V_{DD}	Linear Regulator Output (5V)
VDR	V_{DR}	Input for Driver Supply (5V)
MCLR	MCLR ⁽¹⁾	—
GPA0	GPA0 ⁽¹⁾	GPA0 or Analog Test Output
GPA1	GPA1 ⁽¹⁾	—
GPA2	GPA2 ⁽¹⁾	—
GPA3	GPA3 ⁽¹⁾	—
GPA6	GPA6 ⁽¹⁾	—
GPA7	GPA7 ⁽¹⁾	Clock Signal for I ² C
GPB0	GPB0 ⁽¹⁾	Data Signal for I ² C
GPB1	GPB1 ⁽¹⁾	—
GPB4	GPB4 ⁽¹⁾	Data Signal for ICSP™
GPB5	GPB5 ⁽¹⁾	Clock Signal for ICSP
GPB6	GPB6 ⁽¹⁾	—
GPB7	GPB7 ⁽¹⁾	—

Note 1: Refer to the MCP19214/5 data sheet, for a detailed description of the port pin functions.

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2.6 TYPICAL PERFORMANCE DATA AND WAVEFORMS

Table 2-2 describes the converter parameters.

TABLE 2-2: MCP19215 CONVERTER PARAMETERS

Parameter	Value	Comments
Input Voltage Range (V)	8 – 15	—
Output Voltage (V)	18	± 2.5% tolerance
Maximum Output Current (A)	0.75	Steady state output current
Channel 1 Output Voltage Ripple (mV)	< 100	$V_{IN} = 12V, I_{OUT} = 250\text{ mA}$
Channel 2 Output Voltage Ripple (mV)	< 200	$V_{IN} = 12V, I_{OUT} = 500\text{ mA}$
Input Voltage Ripple (mV)	< 50	$V_{IN} = 12V, I_{OUT} = 750\text{ mA}$
Switching Frequency (kHz)	305 kHz (typical)	—

Figure 2-4 through 2-9 show the typical waveforms that are obtained from the evaluation board.

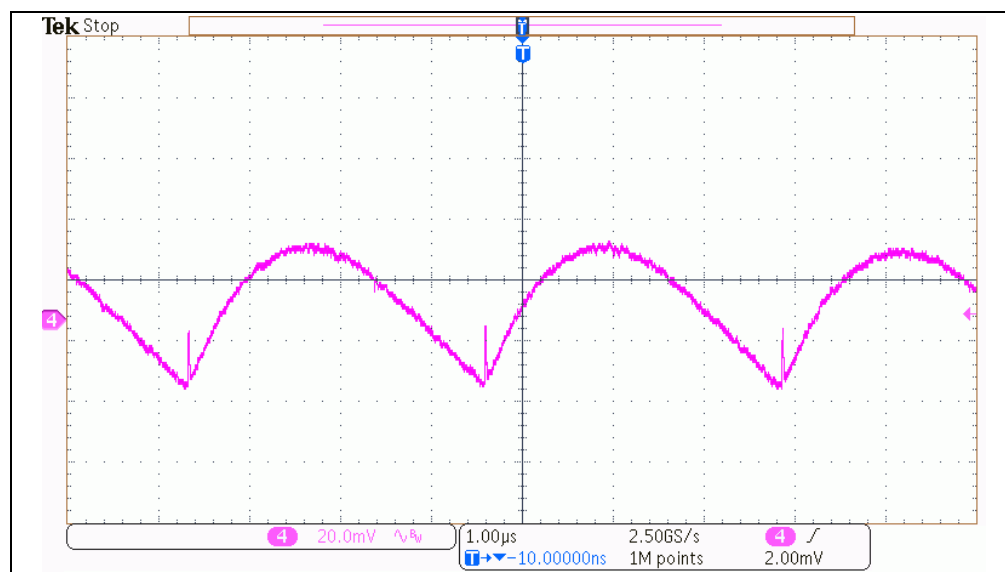


FIGURE 2-4: Output Voltage Ripple/Noise Channel 1 ($V_{IN} = 12V$, $I_{OUT1} = 250\text{ mA}$, $BW = 20\text{ MHz}$).

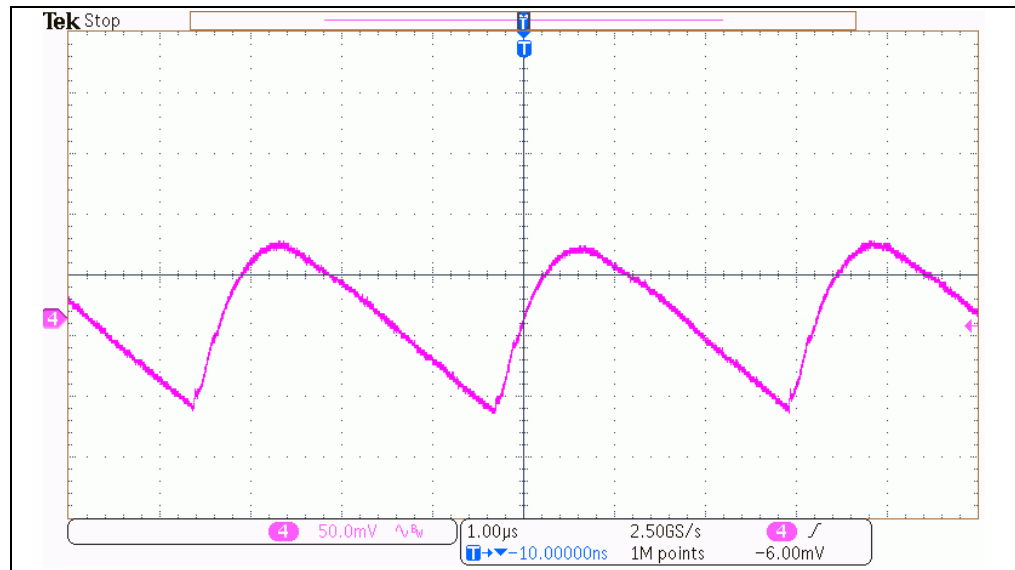


FIGURE 2-5: Output Voltage Ripple/Noise Channel 2 ($V_{IN} = 12V$, $I_{OUT2} = 500\text{ mA}$, $BW = 20\text{ MHz}$).

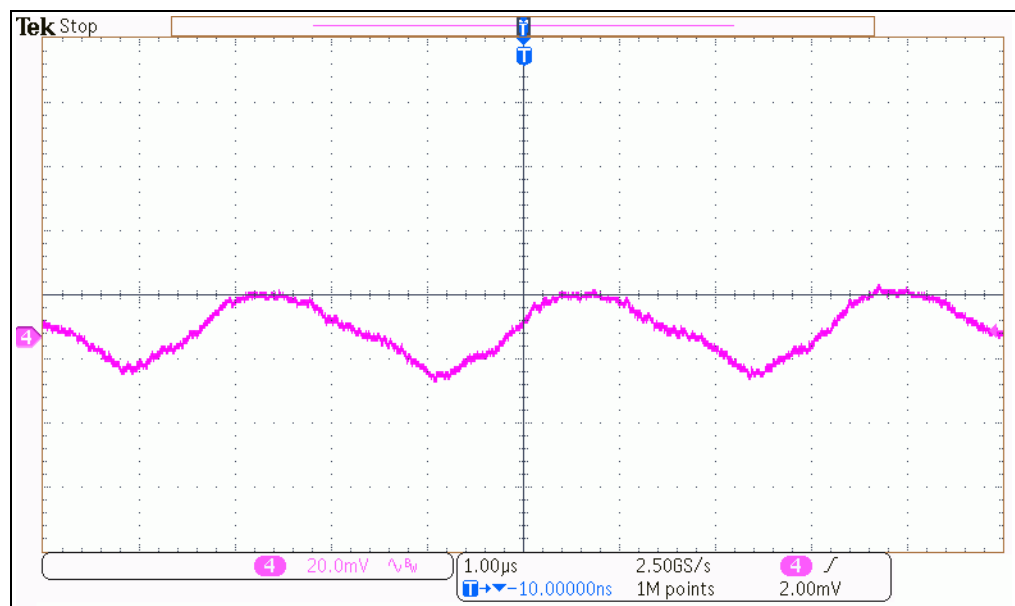


FIGURE 2-6: Input Voltage Ripple/Noise ($V_{IN} = 12V$, $I_{OUT} = 750\text{ mA}$, $BW = 20\text{ MHz}$).

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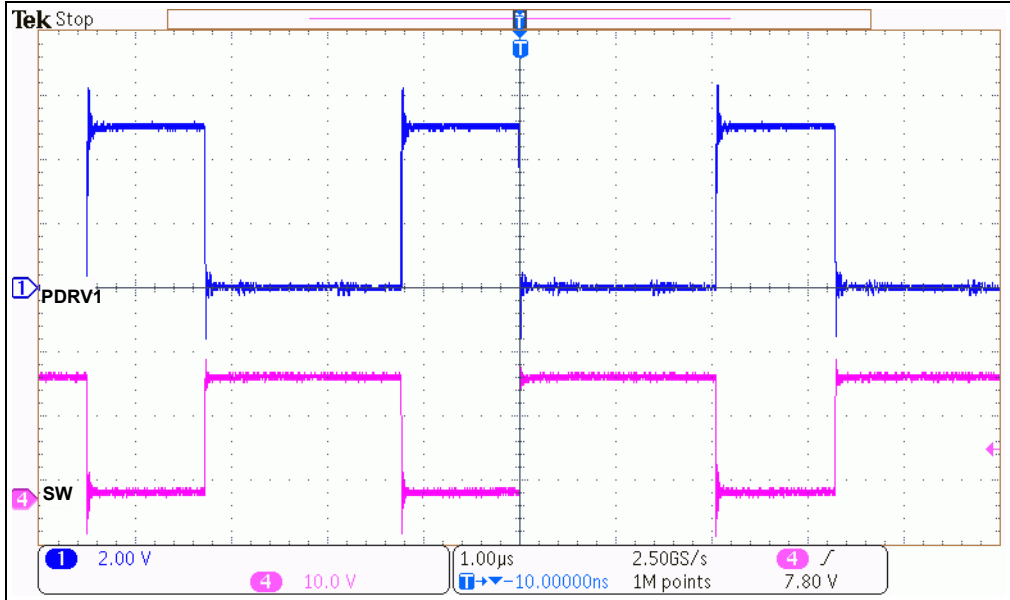


FIGURE 2-7: SW1, PDRV1 Signals ($V_{IN} = 12V$, $I_{OUT1} = 250\text{ mA}$, $BW = 300\text{ MHz}$).

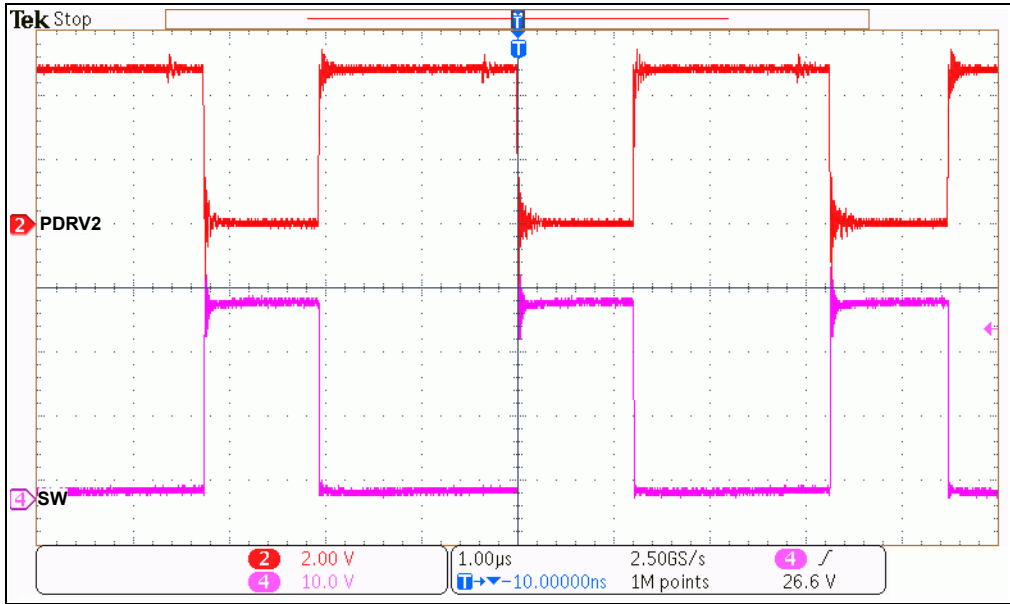


FIGURE 2-8: SW2, PDRV2 Signals ($V_{IN} = 12V$, $I_{OUT2} = 500\text{ mA}$, $BW = 300\text{ MHz}$).

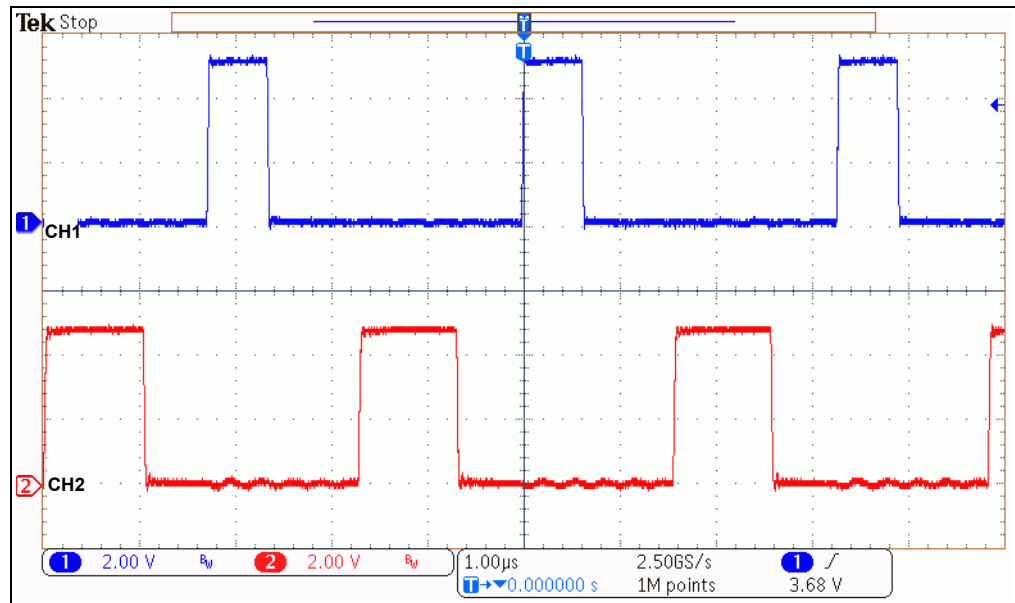


FIGURE 2-9: LDRV and HDRV Signals ($V_{IN} = 12V$, $I_{OUT} = 15A$, $BW = 300\text{ MHz}$).

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NOTES:



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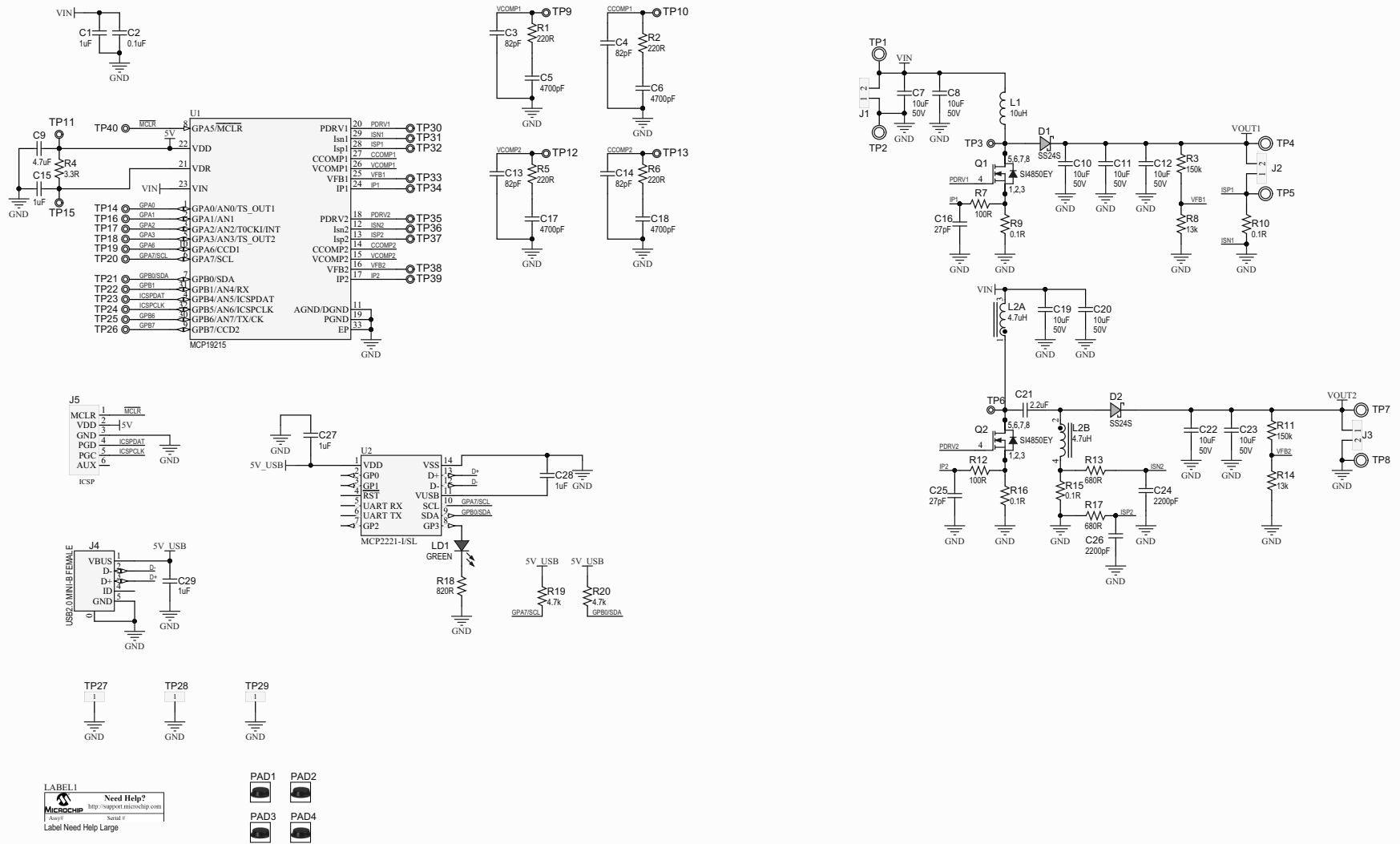
Appendix A. Schematic and Layouts

A.1 INTRODUCTION

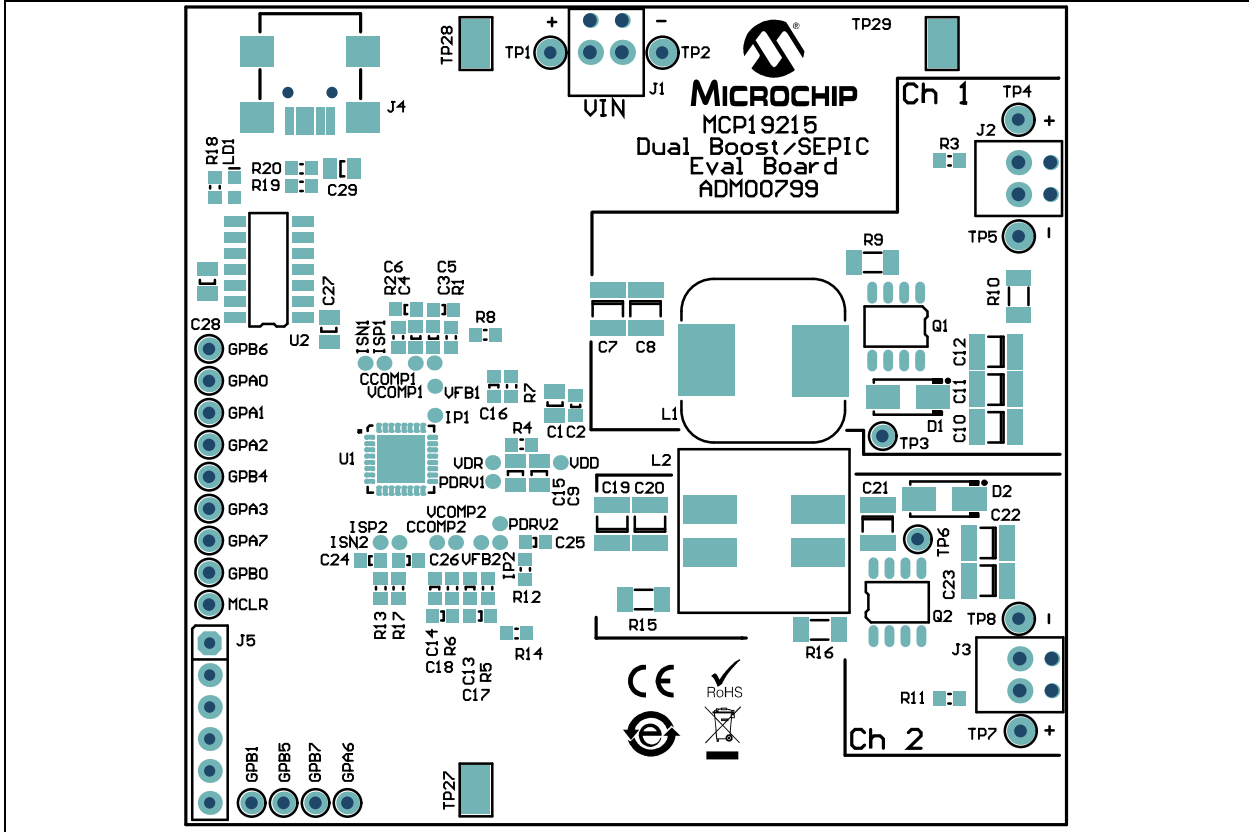
This appendix contains the following schematics and layouts for the MCP19215 Dual Boost/SEPIC Evaluation Board (ADM00799):

- [Board – Schematic](#)
- [Board – Top Silk](#)
- [Board – Top Copper and Silk](#)
- [Board – Top Copper](#)
- [Board – Bottom Copper](#)
- [Board – Bottom Copper and Silk](#)
- [Board – Bottom Silk](#)

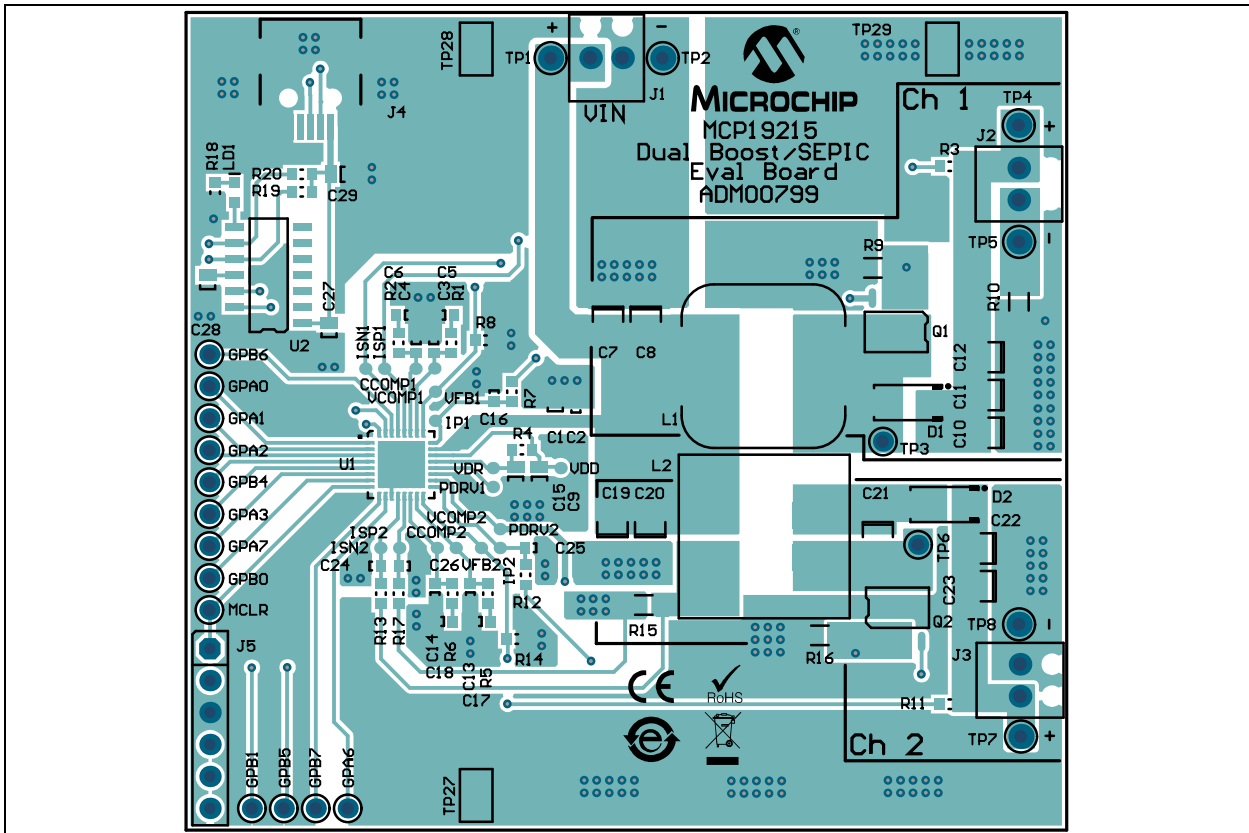
A.2 BOARD - SCHEMATIC



A.3 BOARD – TOP SILK

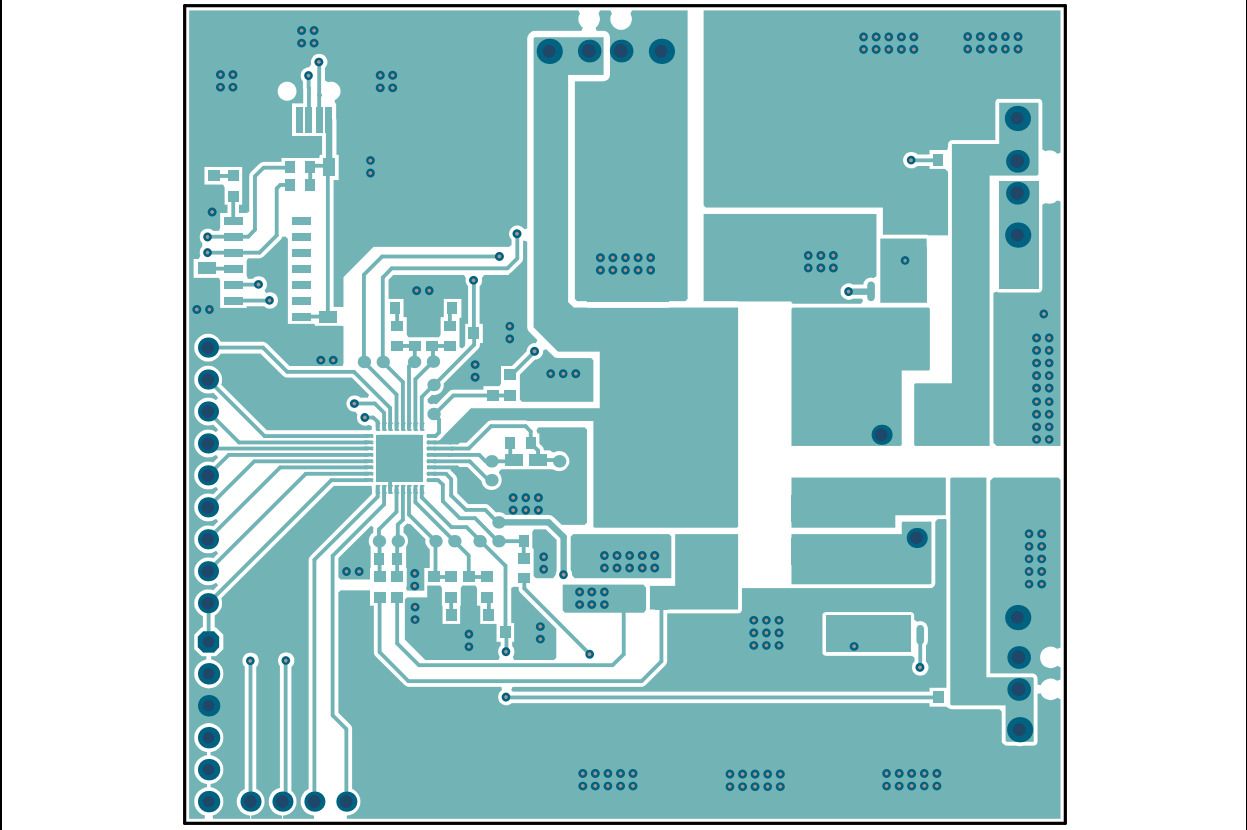


A.4 BOARD – TOP COPPER AND SILK

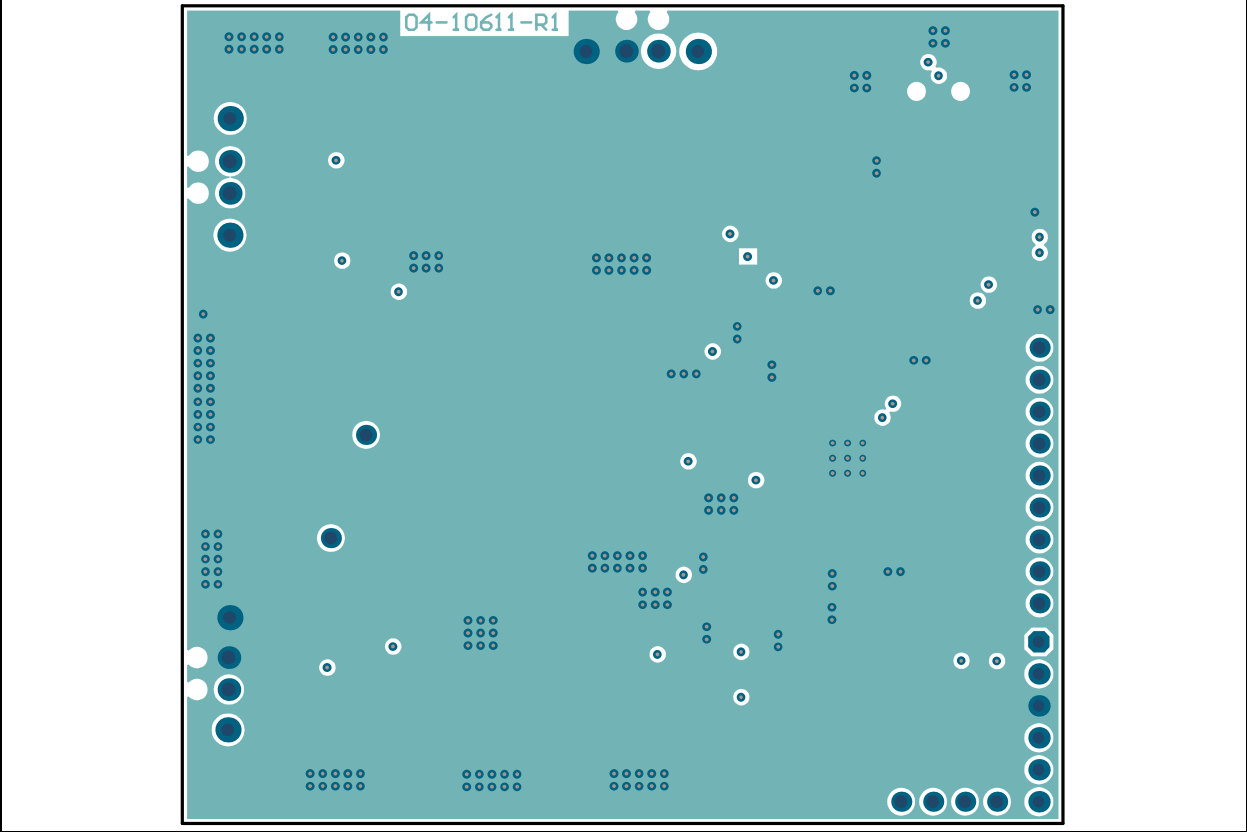


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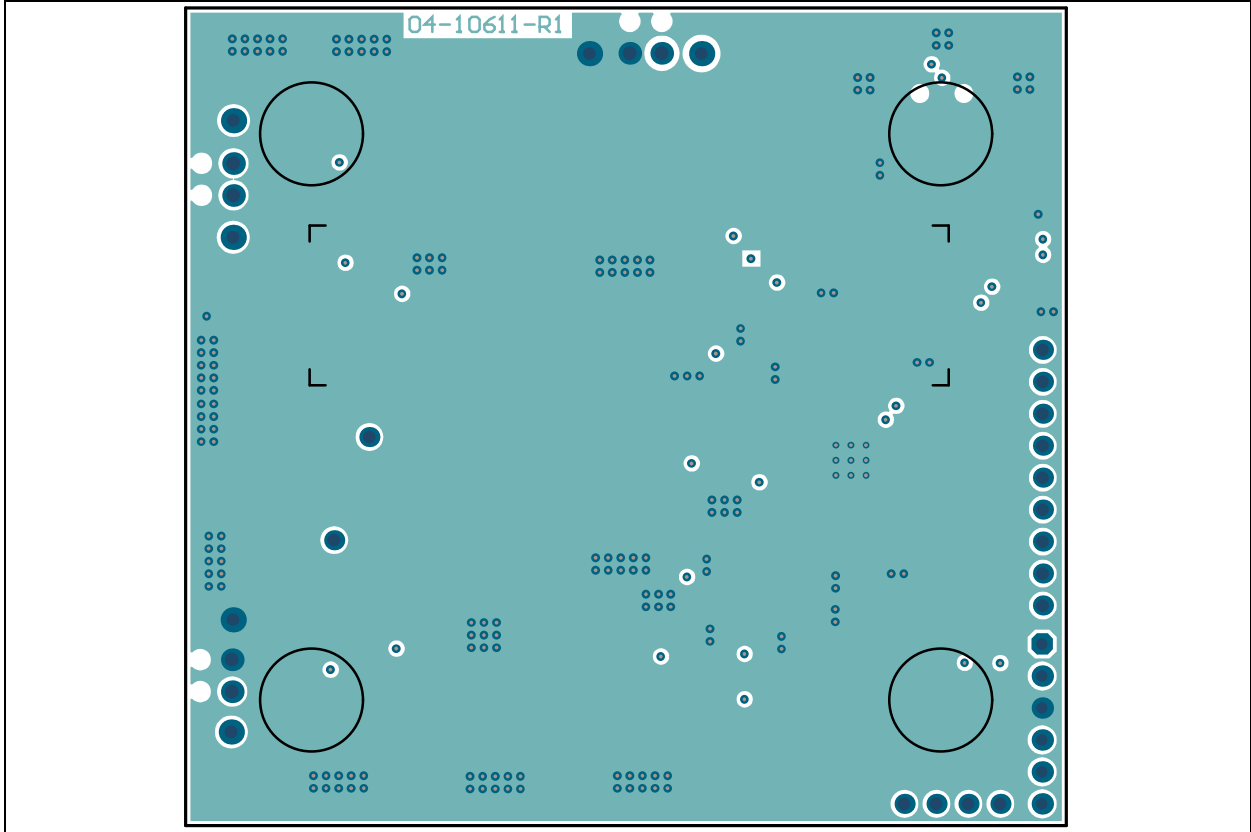
A.5 BOARD – TOP COPPER



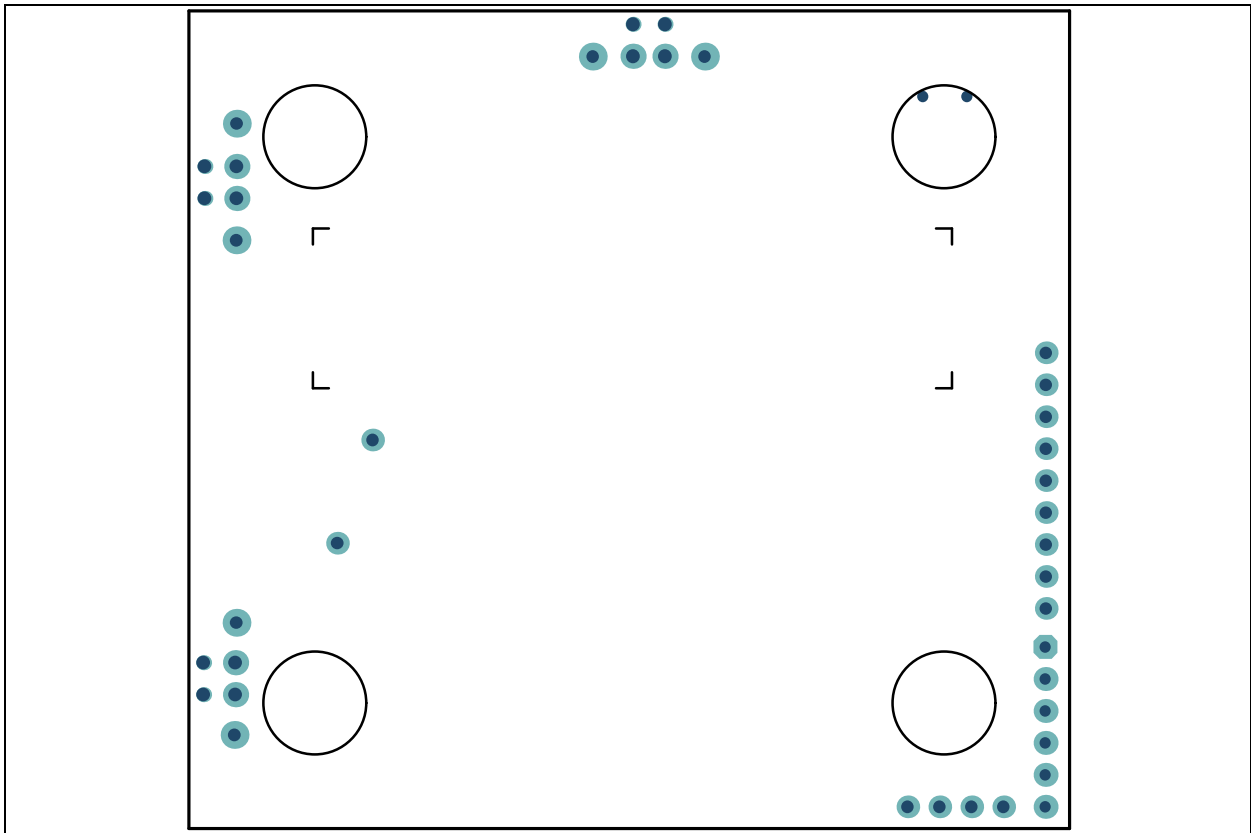
A.6 BOARD – BOTTOM COPPER



A.7 BOARD – BOTTOM COPPER AND SILK



A.8 BOARD – BOTTOM SILK



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NOTES:

Appendix B. Bill of Materials (BOM)

TABLE B-1: MCP19215 DUAL BOOST/SEPIC EVALUATION BOARD (ADM00799)
BILL OF MATERIALS (Note 1)

Qty.	Reference	Description	Manufacturer	Part Number
5	C1, C15 C27-C29	1 μ F 50V Ceramic Capacitor X7R 0805 (2012 Metric) 0.079 L x 0.049" W (2.00 mm x 1.25 mm)	Murata Electronics, Inc.	GRM21BR71H105KA12L
1	C2	0.10 μ F 50V Ceramic Capacitor X7R 0603 (1608 Metric) 0.063" L x 0.031" W (1.60 mm x 0.80 mm)	Murata Electronics, Inc.	GRM188R71H104KA93D
4	C3, C4 C13, C14	82 pF 50V Ceramic Capacitor C0G, NP0 0603 (1608 Metric) 0.063" L x 0.031" W (1.60 mm x 0.80 mm)	Murata Electronics, Inc.	GRM1885C1H820JA01D
4	C5, C6 C17, C18	4700 pF 50V Ceramic Capacitor X7R 0603 (1608 Metric) 0.063" L x 0.031" W (1.60 mm x 0.80 mm)	KEMET	C0603C472K5RACTU
9	C7, C8 C10-C12 C19, C20 C22, C23	10 μ F 50V Ceramic Capacitor X7R 1210 (3225 Metric) 0.126" L x 0.098" W (3.20 mm x 2.50 mm)	TDK Corporation	C3225X7R1H106M250AC
1	C9	4.7 μ F 25V Ceramic Capacitor X7R 0805 (2012 Metric) 0.079" L x 0.049" W (2.00 mm x 1.25 mm)	TDK Corporation	C2012X7R1E475K125AB
2	C16, C25	27 pF 50V Ceramic Capacitor C0G, NP0 0603 (1608 Metric) 0.063" L x 0.031" W (1.60 mm x 0.80 mm)	KEMET	C0603C270J5GACTU
1	C21	2.2 μ F 100V Ceramic Capacitor X7R 1210 (3225 Metric) 0.126" L x 0.098" W (3.20 mm x 2.50 mm)	KEMET	C1210C225K1RACTU
2	C24, C26	2200 pF 50V Ceramic Capacitor X7R 0603 (1608 Metric) 0.063" L x 0.031" W (1.60 mm x 0.80 mm)	KEMET	C0603C222K5RACTU
2	D1, D2	Diode Schottky 40V 2A Surface Mount DO-214AC (SMA)	Vishay/Dale	SS24S-E3/61T
3	J1-J3	2 Position Wire-to-Board Terminal Block Horizontal with Board 0.100" (2.54 mm) Through Hole	PHOENIX CONTACT	1725656
1	J4	Connector USB 2.0 Mini-B Female SMD R/A	Hirose Electric Co., Ltd.	UX60-MB-5ST
1	J5	6 Positions Header, Unshrouded Connector 0.100" (2.54 mm) Through Hole Gold or Gold, GXT™	Amphenol Commercial	68001-106HLF
1	L1	Surface Mount Power Inductor 10 μ H 4A 20% SMD L12.3W12.3H6	Coilcraft	MSS1260-103MLD

Note 1: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

MCP19215 Dual Boost/SEPIC Evaluation Board User's Guide

**TABLE B-1: MCP19215 DUAL BOOST/SEPIC EVALUATION BOARD (ADM00799)
BILL OF MATERIALS (Note 1) (CONTINUED)**

Qty.	Reference	Description	Manufacturer	Part Number
1	L2	Shielded Coupled Power Inductor 4.7 μ H 3.16A 20% SMD L12.3W12.3H6	Coilcraft	MSD1260-472MLB
1	LD1	Green 571 nm LED Indication - Discrete 2V 0603 (1608 Metric)	Lite-On [®] , Inc.	LTST-C191KGKT
2	Q1, Q2	MOSFET N-CH 60V 6A 8-SOIC	Vishay Siliconix	SI4850EY-T1-E3
4	PAD1-PAD4	Bumper Cylindrical, Tapered 0.312" Dia (7.92 mm) Polyurethane Black	3M	SJ61A11
1	PCB	MCP19215 Dual Boost/SEPIC Evalua- tion Board – Printed Circuit Board	—	04-10611
4	R1, R2 R5, R6	RES SMD 220 OHM 5% 1/10W 0603	ROHM Semiconductor	MCR03EZPJ221
2	R3, R11	RES TKF 150k 1% 1/8W SMD 0603	KOA Speer Electronics, Inc.	SG73S1JTDD1503F
1	R4	RES SMD 3.3 OHM 1% 1/10W 0603	Panasonic - ECG	ERJ-3RQF3R3V
2	R7, R12	RES SMD 100 OHM 5% 1/10W 0603	Vishay/Dale	CRCW0603100RJNEA
2	R8, R14	RES SMD 13K OHM 1% 1/10W 0603	Panasonic - ECG	ERJ-3EKF1302V
4	R9, R10 R15, R16	RES TKF 0.1R 1% 1/2W SMD 1206	Bourns [®] , Inc.	CRM1206-FX-R100ELF
2	R13, R17	RES SMD 680 OHM 1% 1/10W 0603	Panasonic - ECG	ERJ-3EKF6800V
1	R18	RES SMD 820 OHM 5% 1/10W 0603	Panasonic - ECG	ERJ-3GEYJ821V
2	R19, R20	RES SMD 4.7K OHM 5% 1/10W 0603	Panasonic - ECG	ERJ-3GEYJ472V
3	TP1, TP4 TP7	Red PC Test Point, Miniature 0.100" Dia x 0.180" L (2.54 mm x 4.57 mm) Phos- phor Bronze, Silver Plating 0.040" (1.02 mm) Hole Diameter Mounting Type	Keystone Electronics Corp.	5000
3	TP2, TP5 TP8	Black PC Test Point, Miniature 0.100" Dia x 0.180" L (2.54 mm x 4.57 mm) Phosphor Bronze, Silver Plating 0.040" (1.02 mm) Hole Diameter Mounting Type	Keystone Electronics Corp.	5001
3	TP27, TP28, TP29	Natural PC Test Point 0.128" x 0.065" x 0.079" (3.25 mm x 1.65 mm x 2.00 mm) Brass, Tin Plating Surface-Mount Mount- ing Type	Harwin Plc.	S1751-46R
1	U1	Analog Dual PWM Controller 2 MHz MCP19215-E/S8 QFN-32	Microchip Technology, Inc.	MCP19215-E/S8
1	U2	Interface USB I ² C/UART MCP2221-I/SL SOIC-14	Microchip Technology, Inc.	MCP2221-I/SL

Note 1: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.



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