

# EP2600ETTS-25.000M

 <p><b>Lead Free</b> COMPLIANT</p>	 <p><b>EU RoHS</b> 2011/65 + 2015/863 COMPLIANT</p>	 <p><b>China RoHS</b> COMPLIANT</p>	 <p><b>REACH</b> SVHC 163 Jun 15, 2015 COMPLIANT</p>
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## ITEM DESCRIPTION

Quartz Crystal Clock Oscillators XO (SPXO) LVCMOS (CMOS) 3.3Vdc 4 Pad 5.0mm x 7.0mm Ceramic Surface Mount (SMD) 25.000MHz  $\pm$ 100ppm -40°C to +85°C

## ELECTRICAL SPECIFICATIONS

<b>Nominal Frequency</b>	25.000MHz
<b>Frequency Tolerance/Stability</b>	$\pm$ 100ppm Maximum (Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration)
<b>Aging at 25°C</b>	$\pm$ 5ppm/year Maximum
<b>Operating Temperature Range</b>	-40°C to +85°C
<b>Supply Voltage</b>	3.3Vdc $\pm$ 10%
<b>Input Current</b>	28mA Maximum (Unloaded)
<b>Output Voltage Logic High (Voh)</b>	Vdd-0.4Vdc Minimum (IOH= -8mA)
<b>Output Voltage Logic Low (Vol)</b>	0.4Vdc Maximum (IOL= +8mA)
<b>Rise/Fall Time</b>	4nSec Maximum (Measured at 20% to 80% of waveform)
<b>Duty Cycle</b>	50 $\pm$ 5(%) (Measured at 50% of waveform)
<b>Load Drive Capability</b>	30pF Maximum
<b>Output Logic Type</b>	CMOS
<b>Pin 1 Connection</b>	Tri-State (Disabled Output: High Impedance)
<b>Tri-State Input Voltage (Vih and Vil)</b>	70% of Vdd Minimum to enable output, 20% of Vdd Maximum to disable output, No Connect to enable output.
<b>Disable Current</b>	16mA Maximum (Pin 1 = Ground)
<b>Absolute Clock Jitter</b>	$\pm$ 250pSec Maximum, $\pm$ 100pSec Typical
<b>One Sigma Clock Period Jitter</b>	$\pm$ 50pSec Maximum
<b>Start Up Time</b>	10mSec Maximum
<b>Storage Temperature Range</b>	-55°C to +125°C

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

<b>ESD Susceptibility</b>	MIL-STD-883, Method 3015, Class 1, HBM: 1500V
<b>Fine Leak Test</b>	MIL-STD-883, Method 1014, Condition A
<b>Flammability</b>	UL94-V0
<b>Gross Leak Test</b>	MIL-STD-883, Method 1014, Condition C
<b>Mechanical Shock</b>	MIL-STD-883, Method 2002, Condition B
<b>Moisture Resistance</b>	MIL-STD-883, Method 1004
<b>Moisture Sensitivity</b>	J-STD-020, MSL 1
<b>Resistance to Soldering Heat</b>	MIL-STD-202, Method 210, Condition K
<b>Resistance to Solvents</b>	MIL-STD-202, Method 215
<b>Solderability</b>	MIL-STD-883, Method 2003
<b>Temperature Cycling</b>	MIL-STD-883, Method 1010, Condition B
<b>Vibration</b>	MIL-STD-883, Method 2007, Condition A

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## MECHANICAL DIMENSIONS (all dimensions in millimeters)



PIN	CONNECTION
1	Tri-State (High Impedance)
2	Ground/Case Ground
3	Output
4	Supply Voltage

LINE	MARKING
1	ECLIPTEK
2	25.000M
3	XXXXX XXXXX=Ecliptek Manufacturing Identifier

## Suggested Solder Pad Layout

All Dimensions in Millimeters



All Tolerances are ±0.1

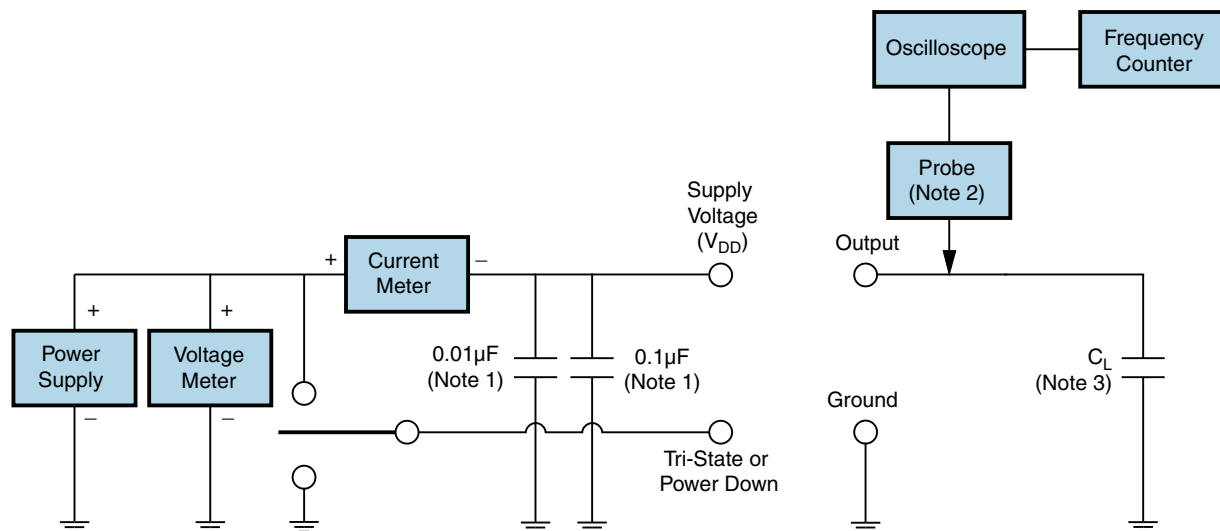
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## OUTPUT WAVEFORM & TIMING DIAGRAM



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## Test Circuit for CMOS Output



Note 1: An external  $0.01\mu\text{F}$  ceramic bypass capacitor in parallel with a  $0.1\mu\text{F}$  high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low input capacitance ( $<12\text{pF}$ ), 10X Attenuation Factor, High Impedance ( $>10\text{Mohms}$ ), and High bandwidth ( $>300\text{MHz}$ ) passive probe is recommended.

Note 3: Capacitance value  $C_L$  includes sum of all probe and fixture capacitance. See applicable specification sheet for 'Load Drive Capability'.

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## Recommended Solder Reflow Methods



## High Temperature Infrared/Convection

$T_S$  MAX to  $T_L$  (Ramp-up Rate) 3°C/Second Maximum

### Preheat

- Temperature Minimum ( $T_S$  MIN) 150°C
- Temperature Typical ( $T_S$  TYP) 175°C
- Temperature Maximum ( $T_S$  MAX) 200°C
- Time ( $t_s$  MIN) 60 - 180 Seconds

Ramp-up Rate ( $T_L$  to  $T_P$ ) 3°C/Second Maximum

### Time Maintained Above:

- Temperature ( $T_L$ ) 217°C
- Time ( $t_L$ ) 60 - 150 Seconds

Peak Temperature ( $T_P$ ) 260°C Maximum for 10 Seconds Maximum

Target Peak Temperature ( $T_P$  Target) 250°C +0/-5°C

Time within 5°C of actual peak ( $t_p$ ) 20 - 40 Seconds

Ramp-down Rate 6°C/Second Maximum

Time 25°C to Peak Temperature (t) 8 Minutes Maximum

Moisture Sensitivity Level Level 1

Additional Notes Temperatures shown are applied to body of device.

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## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

Ts MAX to TL (Ramp-up Rate) 5°C/Second Maximum

#### Preheat

- Temperature Minimum (Ts MIN) N/A  
 - Temperature Typical (Ts TYP) 150°C  
 - Temperature Maximum (Ts MAX) N/A  
 - Time (ts MIN) 60 - 120 Seconds

Ramp-up Rate (TL to TP) 5°C/Second Maximum

#### Time Maintained Above:

- Temperature (TL) 150°C  
 - Time (tL) 200 Seconds Maximum

Peak Temperature (TP) 240°C Maximum

Target Peak Temperature (TP Target) 240°C Maximum 2 Times / 230°C Maximum 1 Time

Time within 5°C of actual peak (tp) 10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time

Ramp-down Rate 5°C/Second Maximum

Time 25°C to Peak Temperature (t) N/A

Moisture Sensitivity Level Level 1

Additional Notes Temperatures shown are applied to body of device.

### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)

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- Техническая поддержка проекта;
- Защита от снятия компонента с производства.



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