TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74ACT08P, TC74ACT08F, TC74ACT08FT

### Quad 2-Input AND Gate

The TC74ACT08 is an advanced high speed CMOS 2-INPUT AND GATE fabricated with silicon gate and double-layer metal wiring  $C^2 MOS$  technology.

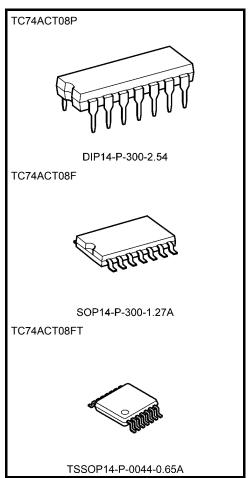
It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This device may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

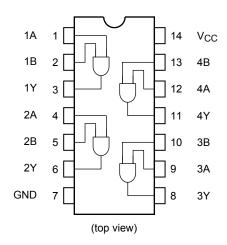
- High speed:  $t_{pd} = 4.7 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $I_{CC} = 4 \mu A \text{ (max)}$  at  $T_a = 25 \text{°C}$
- Compatible with TTL outputs:  $V_{IL}$  = 0.8 V (max)  $V_{IH}$  = 2.0 V (min)
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24$  mA (min) Capability of driving 50  $\Omega$  transmission lines.
- $\bullet \quad Balanced\ propagation\ delays\hbox{:}\ t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74F08



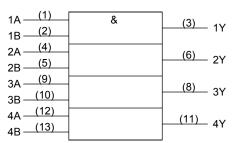
Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)

### **Pin Assignment**



#### **IEC Logic Symbol**



#### **Truth Table**

| Α | В | Υ |
|---|---|---|
| L | L | L |
| L | Н | L |
| Н | L | L |
| Н | Н | Н |

## **Absolute Maximum Ratings (Note 1)**

| Characteristics                    | Symbol           | Rating                             | Unit |
|------------------------------------|------------------|------------------------------------|------|
| Supply voltage range               | Vcc              | −0.5 to 7.0                        | V    |
| DC input voltage                   | V <sub>IN</sub>  | -0.5 to V <sub>CC</sub> + 0.5      | V    |
| DC output voltage                  | Vout             | -0.5 to V <sub>CC</sub> + 0.5      | V    |
| Input diode current                | Ιικ              | ±20                                | mA   |
| Output diode current               | lok              | ±50                                | mA   |
| DC output current                  | lout             | ±50                                | mA   |
| DC V <sub>CC</sub> /ground current | Icc              | ±100                               | mA   |
| Power dissipation                  | PD               | 500 (DIP) (Note 2)/180 (SOP/TSSOP) | mW   |
| Storage temperature                | T <sub>stg</sub> | -65 to 150                         | °C   |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.



### **Operating Ranges (Note)**

| Characteristics          | Symbol           | Rating               | Unit |
|--------------------------|------------------|----------------------|------|
| Supply voltage           | $V_{CC}$         | 4.5 to 5.5           | V    |
| Input voltage            | V <sub>IN</sub>  | 0 to V <sub>CC</sub> | V    |
| Output voltage           | V <sub>OUT</sub> | 0 to V <sub>CC</sub> | V    |
| Operating temperature    | T <sub>opr</sub> | −40 to 85            | °C   |
| Input rise and fall time | dt/dV            | 0 to 10              | ns/V |

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{CC}$  or GND.

#### **Electrical Characteristics**

#### **DC Characteristics**

| Characteristics Symbol    |                 | Test Condition   |   |        | Ta = 25°C           |      |      | Ta =<br>-40 to 85°C |      | Unit |    |
|---------------------------|-----------------|--|---|--------|---------------------|------|------|---------------------|------|------|----|
|                           |                 |  |   |        | V <sub>CC</sub> (V) | Min  | Тур. | Max                 | Min  | Max  | 0  |
| High-level input voltage  | V <sub>IH</sub> | _  |   |        | 4.5 to 5.5          | 2.0  | _    | _                   | 2.0  | _    | V  |
| Low-level input voltage   | $V_{IL}$        | _  |   |        | 4.5 to 5.5          |      | _    | 0.8                 | ı    | 0.8  | ٧  |
|                           | V <sub>OH</sub> | V <sub>IN</sub><br>= V <sub>IH</sub>                       | I <sub>OH</sub> = -50 μA  |        | 4.5                 | 4.4  | 4.5  | _                   | 4.4  | _    |    |
| High-level output voltage |                 |  | I <sub>OH</sub> = -24 mA  |        | 4.5                 | 3.94 | _    | _                   | 3.80 | _    | ٧  |
| ŭ                         |                 |  | I <sub>OH</sub> = −75 mA  | (Note) | 5.5                 | 1    | _    | _                   | 3.85 | _    |    |
|                           |                 | V <sub>IN</sub><br>= V <sub>IH</sub> or<br>V <sub>IL</sub> | I <sub>OL</sub> = 50 μA   |        | 4.5                 | -    | 0.0  | 0.1                 | _    | 0.1  |    |
| Low-level output voltage  | $V_{OL}$        |  | I <sub>OL</sub> = 24 mA   |        | 4.5                 | _    | _    | 0.36                | _    | 0.44 | V  |
|                           |                 |  | I <sub>OL</sub> = 75 mA   | (Note) | 5.5                 | 1    | _    | _                   | _    | 1.65 |    |
| Input leakage current     | I <sub>IN</sub> | V <sub>IN</sub> = V <sub>CC</sub> or GND                   |   |        | 5.5                 | l    | _    | ±0.1                | l    | ±1.0 | μΑ |
| Quiescent supply current  | I <sub>CC</sub> | V <sub>IN</sub> = V <sub>CC</sub> or GND                   |   |        | 5.5                 | 1    | _    | 4.0                 | _    | 40.0 | μΑ |
|                           | Ic              |  | nput: V <sub>IN</sub> = 3.4 V<br>er input: V <sub>CC</sub> or GND |        | 5.5                 | _    | _    | 1.35                | _    | 1.5  | mA |

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines. One output should be tested at a time for a 10 ms maximum duration.

#### AC Characteristics ( $C_L = 50 \text{ pF}$ , $R_L = 500 \Omega$ , input: $t_r = t_f = 3 \text{ ns}$ )

| Characteristics Symbol        | Symbol                               | Test Condition |                     | Ta = 25°C |      |     | Ta =<br>-40 to 85°C |      | Unit |
|-------------------------------|--------------------------------------|----------------|---------------------|-----------|------|-----|---------------------|------|------|
|                               | -,                                   |                | V <sub>CC</sub> (V) | Min       | Тур. | Max | Min                 | Max  |      |
| Propagation delay time        | t <sub>pLH</sub><br>t <sub>pHL</sub> | _              | 5.0 ± 0.5           | _         | 5.4  | 8.7 | 1.0                 | 10.0 | ns   |
| Input capacitance             | C <sub>IN</sub>                      | _              |                     | _         | 5    | 10  | _                   | 10   | pF   |
| Power dissipation capacitance | C <sub>PD</sub>                      |                | (Note)              | _         | 21   | -   | _                   | 1    | pF   |

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

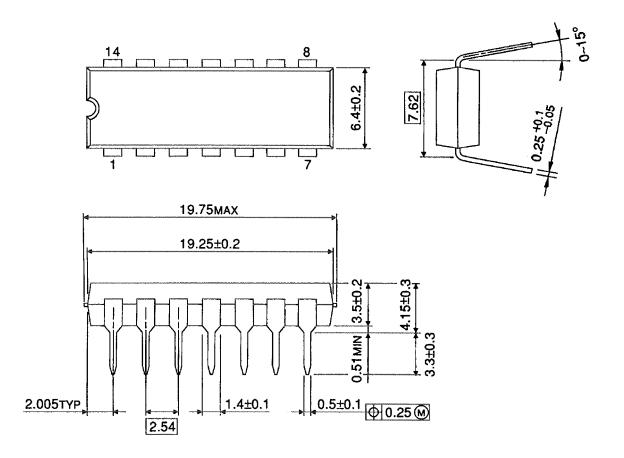
Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4 \text{ (per gate)}$ 

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# **Package Dimensions**

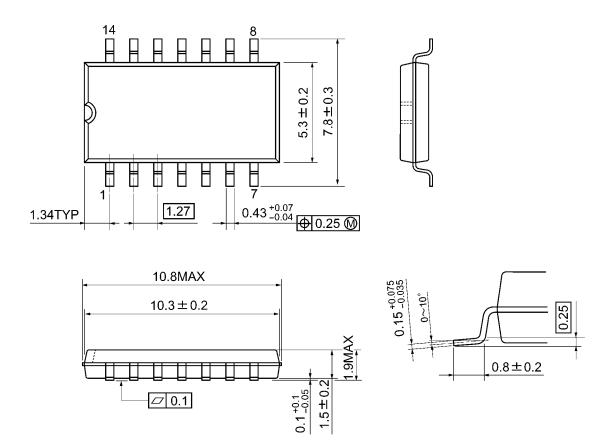
DIP14-P-300-2.54 Unit: mm



Weight: 0.96 g (typ.)

# **Package Dimensions**

SOP14-P-300-1.27A Unit: mm



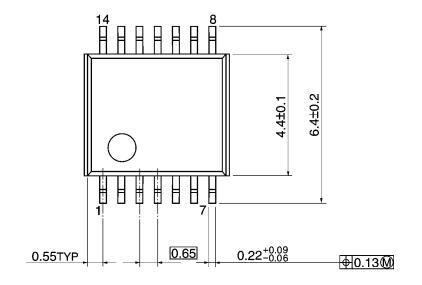
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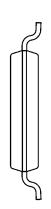
Weight: 0.18 g (typ.)

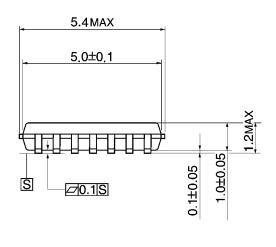
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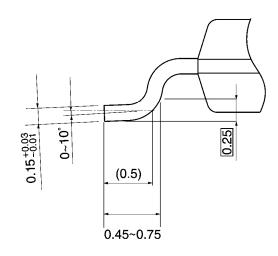
TSSOP14-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

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